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FIELD MAINTENANCE PRINT SET

TABLE OF CONTENTS

UNIT VARIATIONS COVERED BY THIS PRINT SET

KA750

Field Maintenance Print Set

MP01024

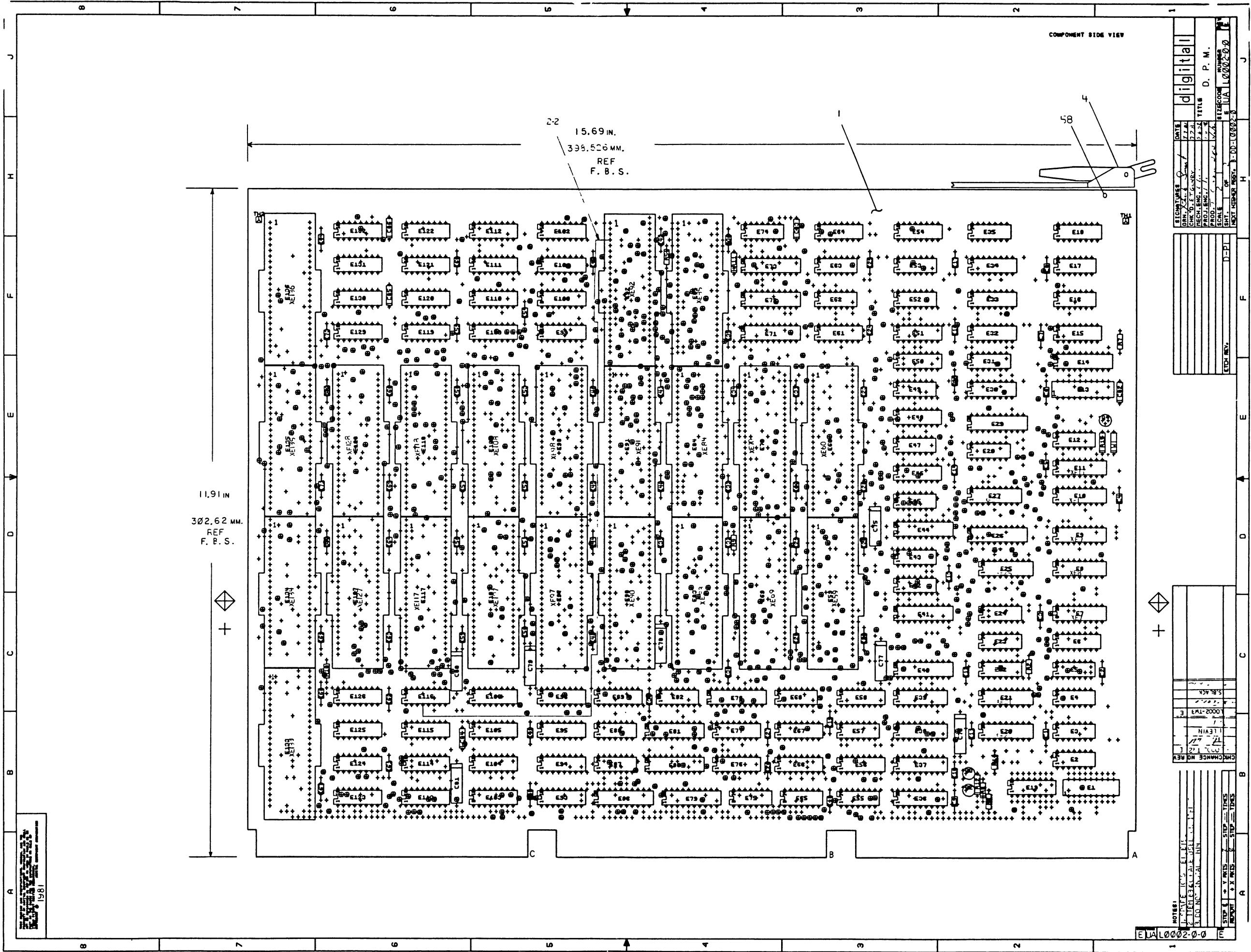
Digital Equipment Corporation

KA750

11750 CPU

digital

DRB 124



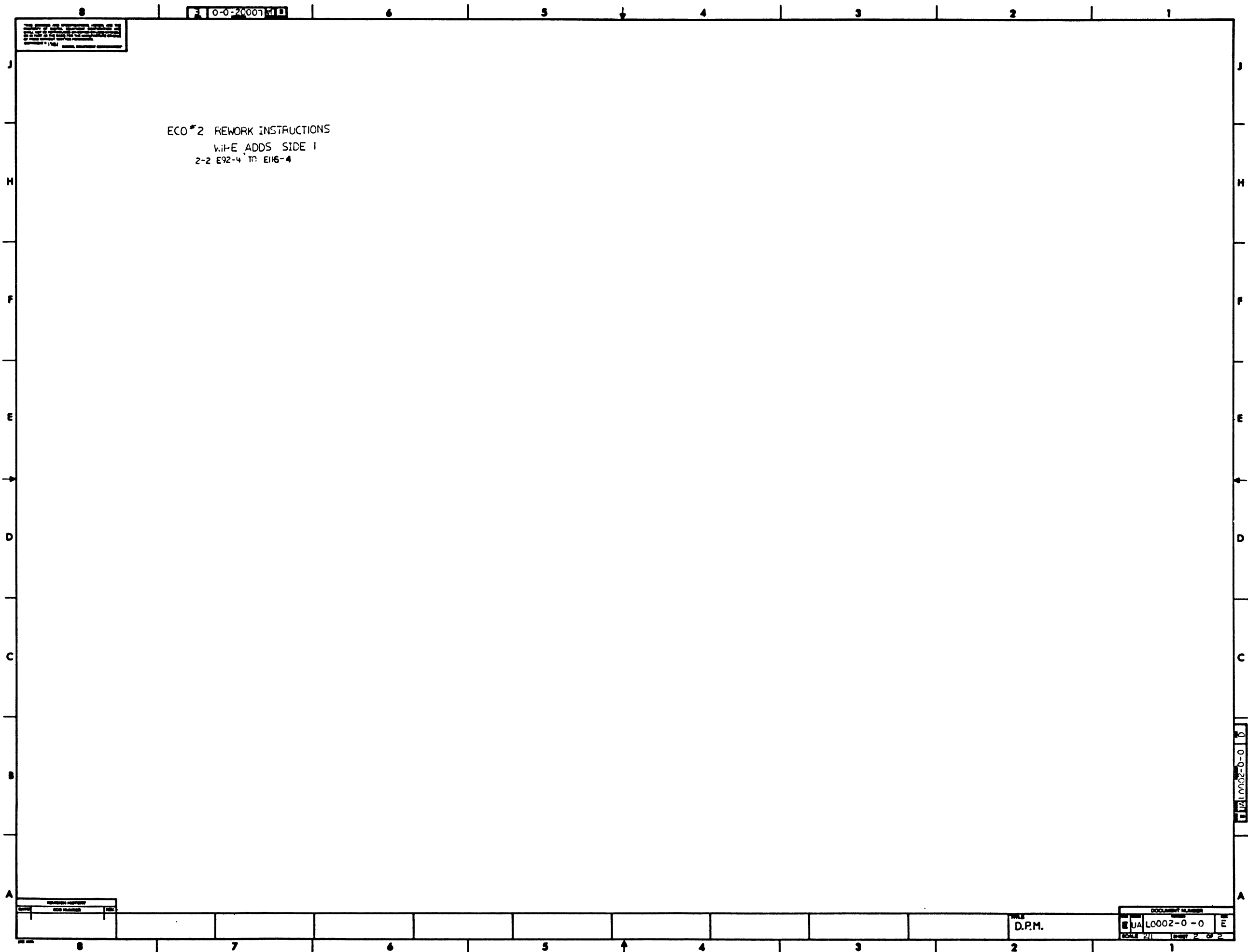
1981

SIGNATURE	DATE	FUNCTION
DRN. D. P. M.	7/2/81	DESIGNED
CHK. D. P. M.	7/2/81	CHECKED
PROJ. ENG. D. P. M.	7/2/81	PROJECT ENGINEER
PROJ. MGR. D. P. M.	7/2/81	PROJECT MANAGER
ENT. D. P. M.	7/2/81	ENTERED
EXT. REV.	D-PI	EXTENSION

CHANGE NO.	REV.	DESCRIPTION
1	1	INITIAL
2	2	REVISION
3	3	REVISION
4	4	REVISION
5	5	REVISION
6	6	REVISION
7	7	REVISION
8	8	REVISION
9	9	REVISION
10	10	REVISION

STEP	Y AXIS	X AXIS	TIME
1	1	1	1
2	2	2	2
3	3	3	3
4	4	4	4
5	5	5	5
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EUA L0002-0-0

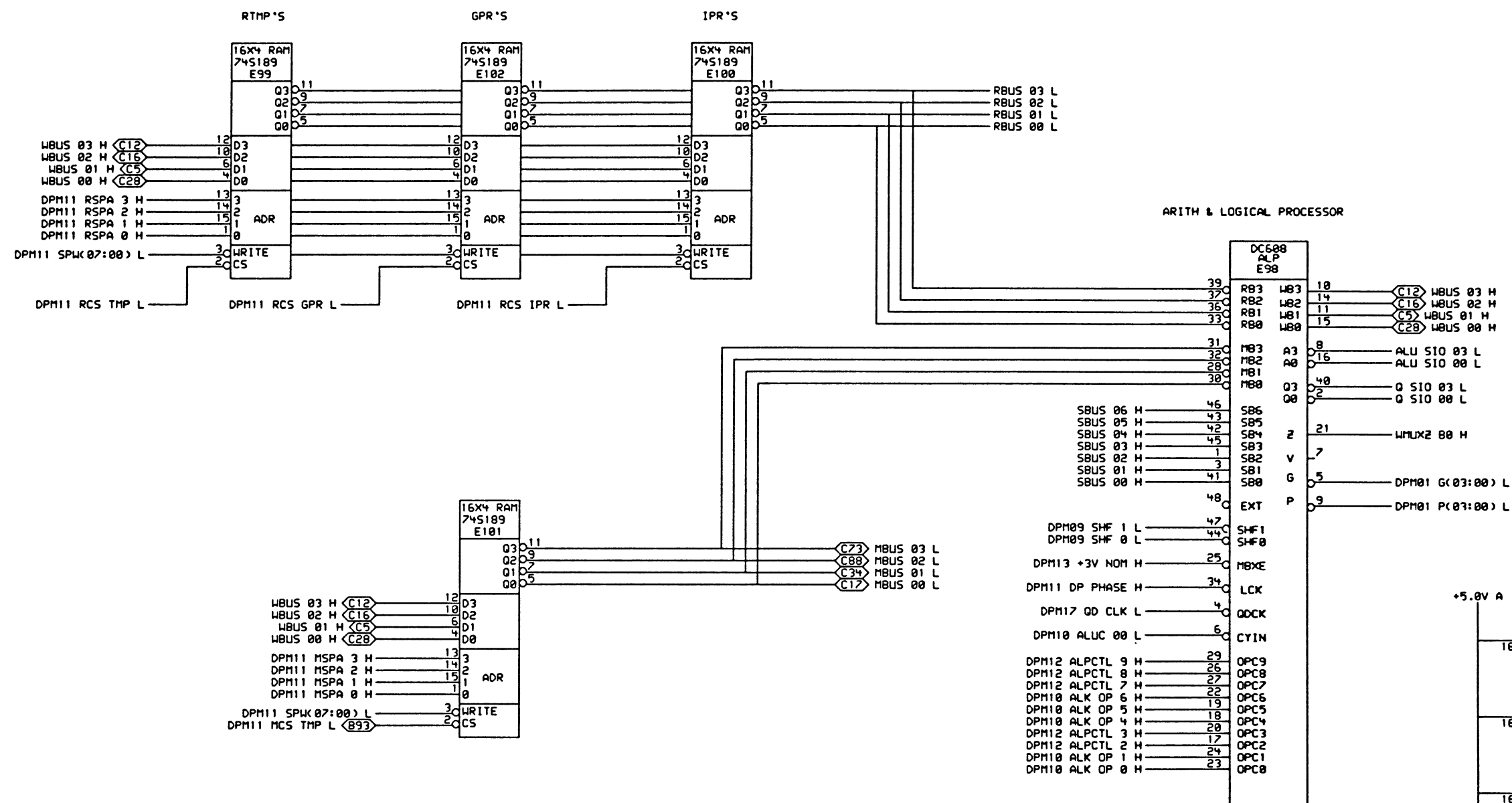


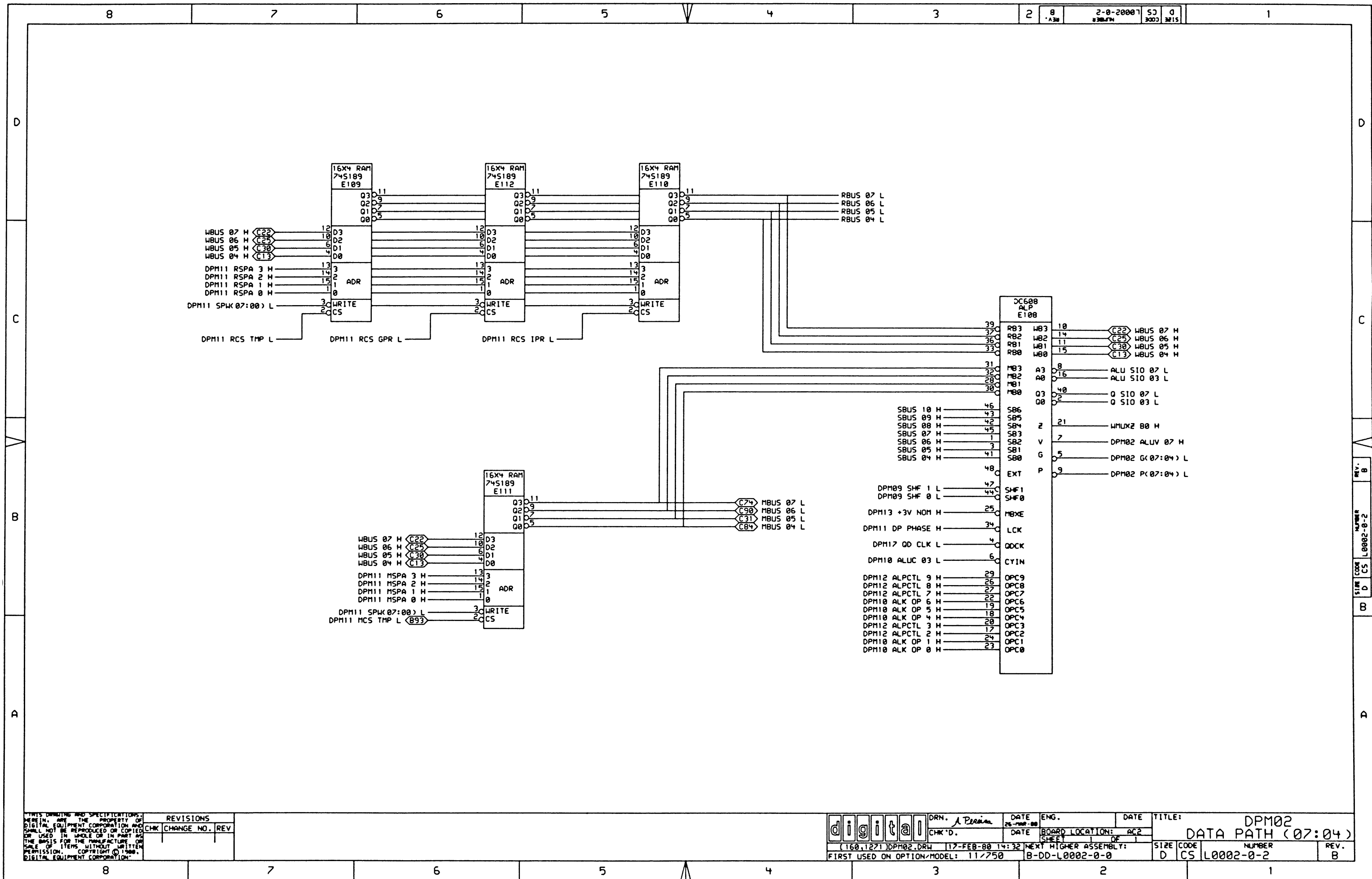
LINE	ITEM	DOCUMENT NUMBER	PART NUMBER	DESCRIPTION	QTY PER VARIATION 00	REFERENCE DESIGNATOR
1	1	E-MD-5013555-0-0	5013555-00	ETCHED CIRCUIT BOARD	1	
2	2	SEE NOTES	1012084-01	8 MFD 25V +75-10% AL EL	6	C75-C80
3	3		1012784-00	.047 MFD 50V +80-20% CER	73	C1-C35,C37-C74
4	4		1210711-02	/REPLACED BY 12-16988-02	1	
5	5		1215924-00	SOCKET IC W/METAL CONT	22	CONT XE59-XE60,XE69-XE70,XE83-XE85, CONT XE90-XE92,XE97,XE98,XE107,XE108, CONT XE117,XE118,XE127,XE128, CONT XE133-XE136
6	6		1300005-04	R NETWORK 15-470 5.0 % 16PIN	2	E61,E89
7	7		1300365-00	1.0 K .25 W 5.0 % CC	1	R10
8	8		1301890-00	560.0 .25 W 5.0 % CC	2	R1,R2
9	9		1811660-21	*** THIS ITEM IS NOT USED ***	-	
10	10		1910532-00	74S00 NAND GATE-QUAD 2IN	5	E17,E24,E31,E52,E35
11	11		1910533-00	74S03 NAND GATE-QUAD 2IN,0	1	E42
12	12		1910534-00	74S04 INVERTER GATE-HEX 1I	4	E4,E-5,E55,E57
13	13		1910535-00	74S05 INVERTER GATE-HEX 1	1	E68
14	14		1910536-00	74S10 NAND GATE-TRIPLE 3IN	4	E5,E49,E63,E75
15	15		1910539-00	74S20 NAND GATE-DUAL 4INPU	1	E62
16	16		1910540-00	74S22 NAND GATE-DUAL 4INPU	1	E22
17	17		1910544-00	74S74 FF-D DUAL,EDGE TRIGG	1	E3
18	18		1910545-00	74S112 FF-JK DUAL,EDGE TRIG	1	E56
19	19		1910547-00	74S153 MUX 1 OF 4 (DUAL)	2	E34,E64
20	20		1910548-00	74S157 MUX 1 OF 2 (QUAD)	1	E6
21	21		1910552-00	74S194 SHIFT REG.,4BIT RIGH	1	E30
22	22		1910956-00	74S151 MUX 1 OF 8	9	E18,E32,E33,E46,E54,E66,E67,E76, CONT E77
23	23		1911573-00	74S280 PARITY GEN/CHKR,9BIT	8	E12,E16,E28,E47,E74,E82,E87,E88
24	24		1911675-00	74S138 DECODER/DEMUX 3-8 LIN	1	E48
25	25		1912389-00	74S08 AND GATE-QUAD 2IN,PO	1	E43
26	26		1912586-00	DM 85S68N REGISTER,64BIT EDGE	4	E36-E39

REVISION HISTORY			BASIC PART NO: L0002		DRN: D.SIREEN		DATE: 19-OCT-79		D I G I T A L			
ENG	ECO NUMBER	REV	SECTION A OF A									
---	INITIAL	B	SECTION VARIATION INDEX		CHK'D: E.T. GERRY		DATE: 19-OCT-79		TITLE PARTS LIST			
D.L	L0002-TW001	C	[A] 00						D.F.M.			
			[B]									
			[C]		DES.ENG: D. LI		DATE: 19-OCT-79					
			[D]									
			[E]						DOCUMENT NUMBER			
			[F]		RESP.ENG.: D. LI		DATE: 19-OCT-79					
			[H]						SIZE CODE NUMBER REV			
			[J]									
			[K]		MFG.ENG.: VANCE PARKER		DATE: 8-FEB-80		K PL L0002-0-DBP C			
			[L]									
			[M]		ASSEMBLY NUMBER:		TOP DOCUMENT NUMBER:		FILE NAME:			
			[N]		E-UA-L0002-0-0		B-DD-L0002-0-0		Z1257.PLS EDIT #14			

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TW)

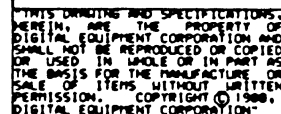




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REVISIONS		
CHK	CHANGE NO.	REV

digital	DRN. <i>A. Berman</i>	DATE 26-MAR-80	ENG.	DATE	TITLE: DPM02		
	CHK'D.	DATE	BOARD LOCATION: AC2	SHEET	DATA PATH (07:04)		
FIRST USED ON OPTION/MODEL: 11/750		NEXT HIGHER ASSEMBLY: B-DD-L0002-0-0		SIZE CODE	NUMBER	REV.	
				D CS	L0002-0-2	B	



digital

	DATE
-	26-MAR-
	DATE

LOCATION: _____

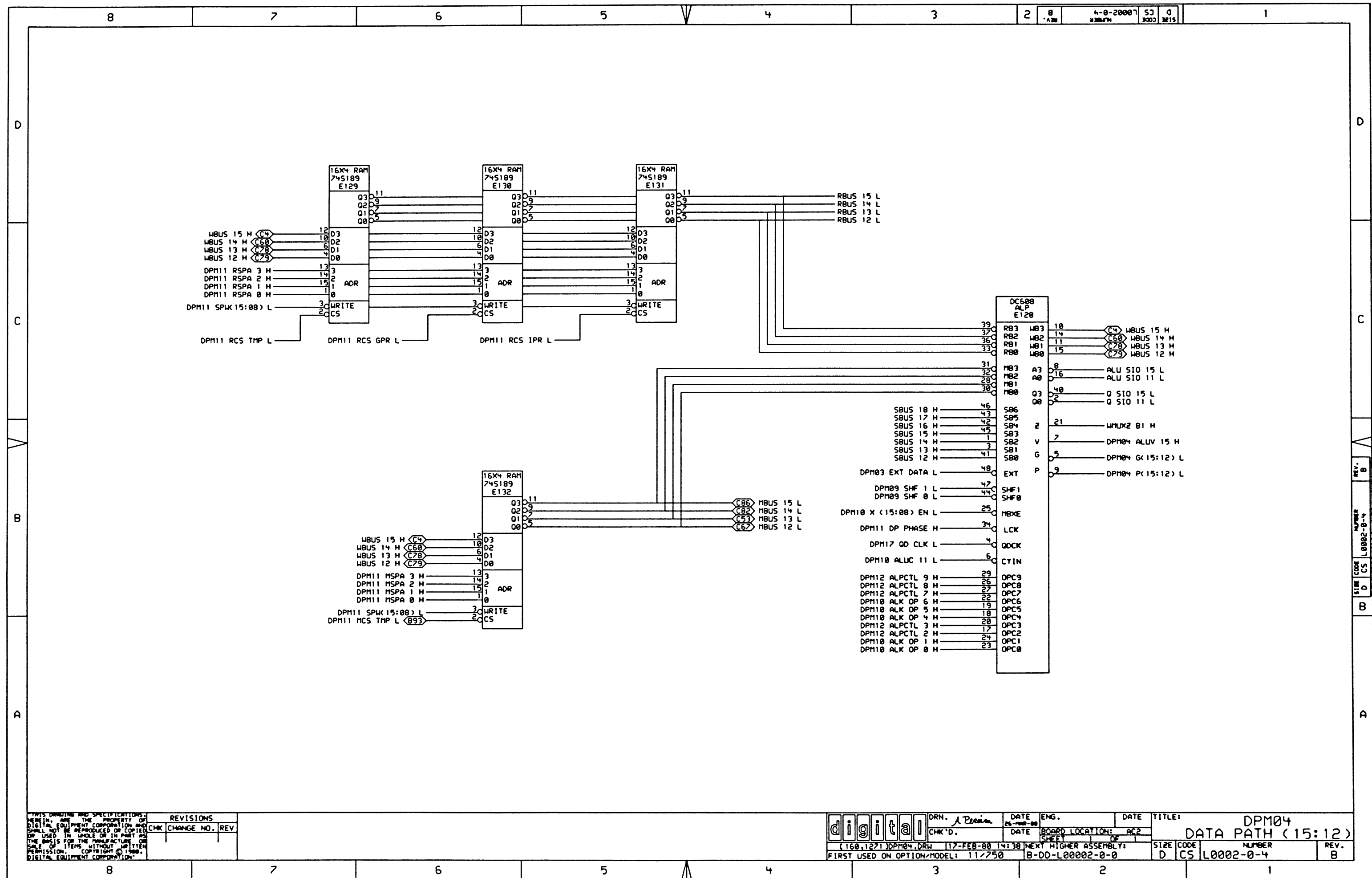
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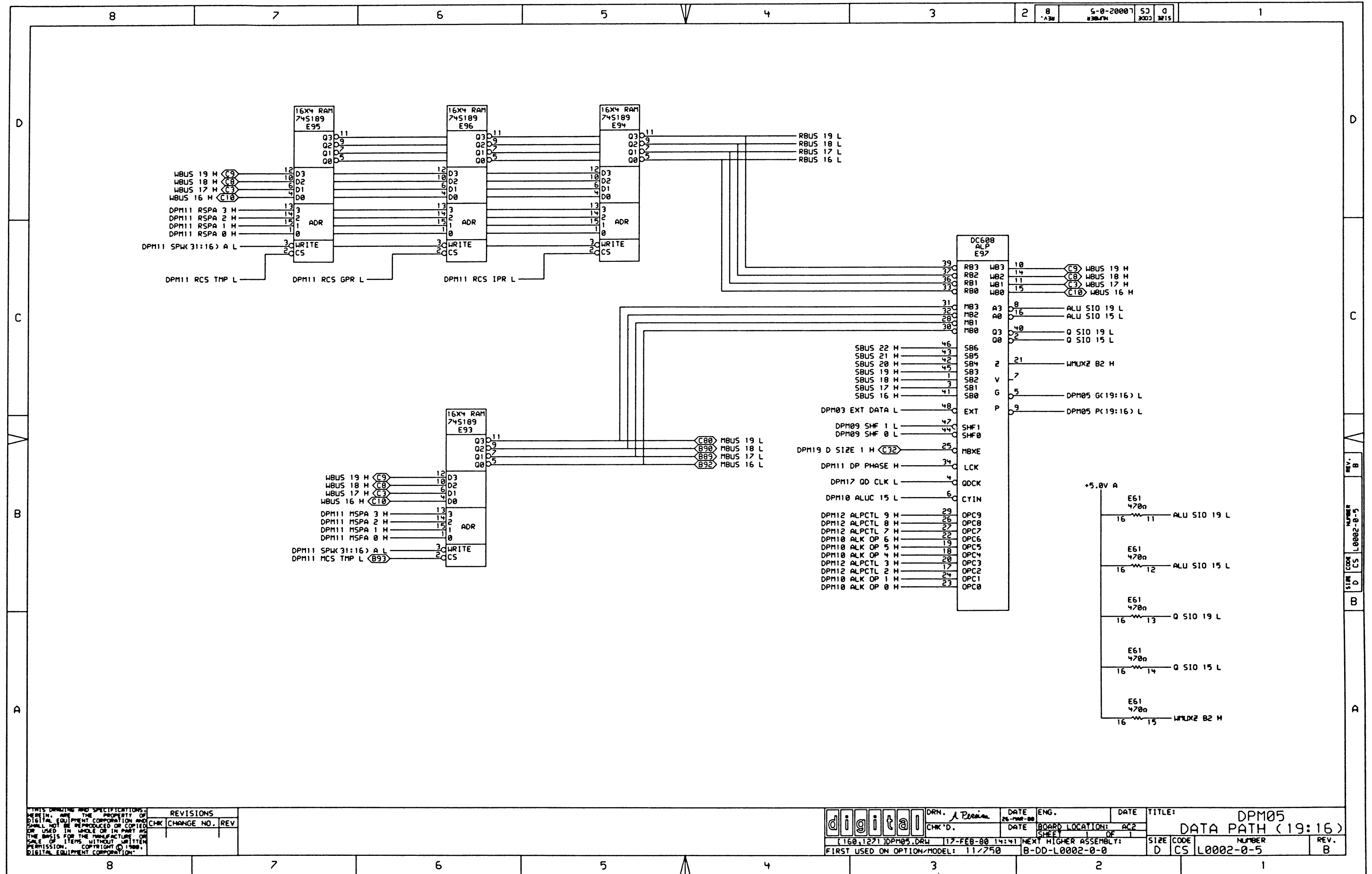
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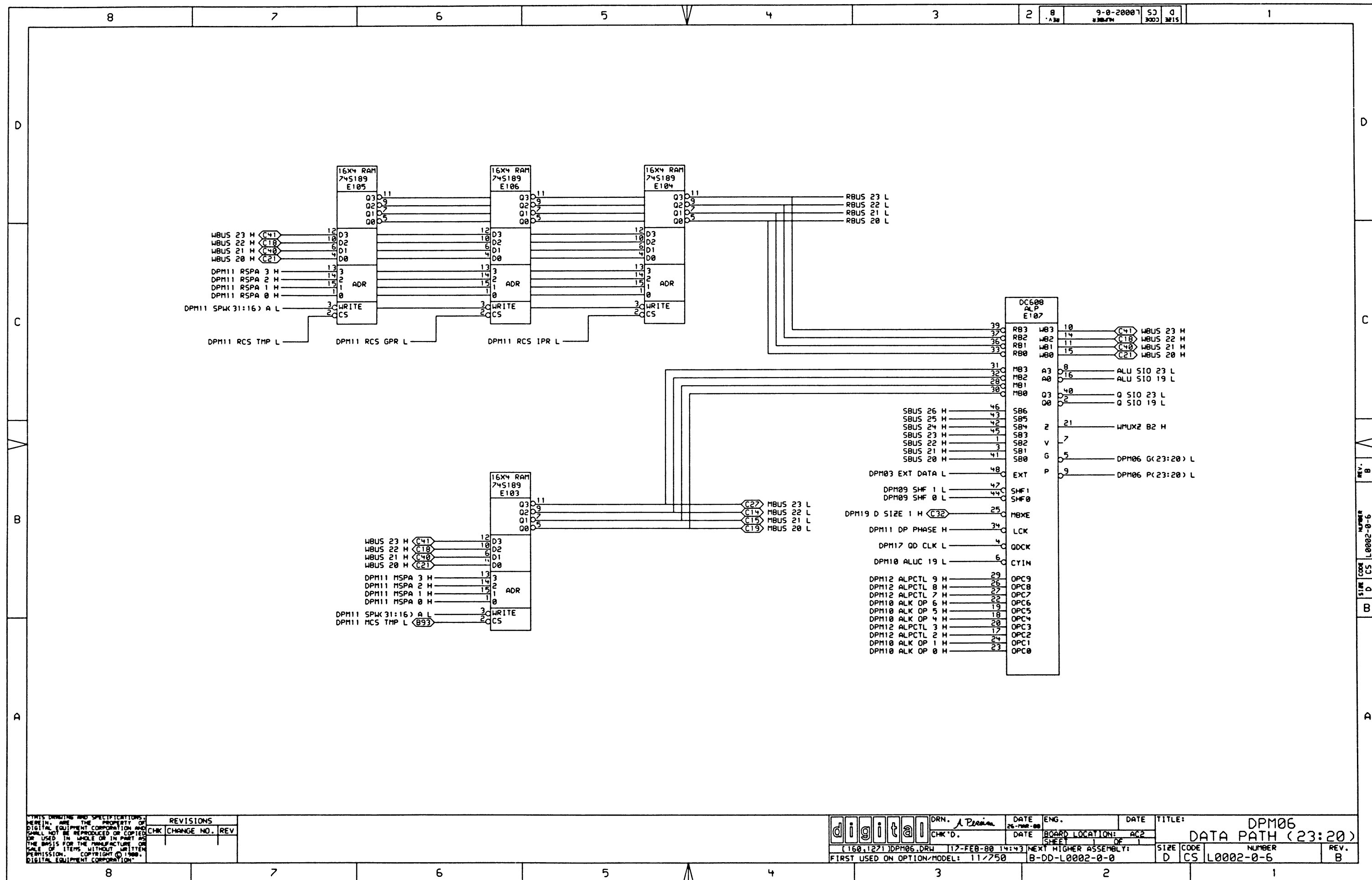
PM03
TH (

08

2	
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REVISIONS		
CHK	CHANGE NO.	REV

digital
[160,1271]DPM06.DRW 117-FEB-80 14:43
FIRST USED ON OPTION/MODEL: 11/750

DRN. A. P. P. P.
CHK'D.
DATE 26-MAR-80

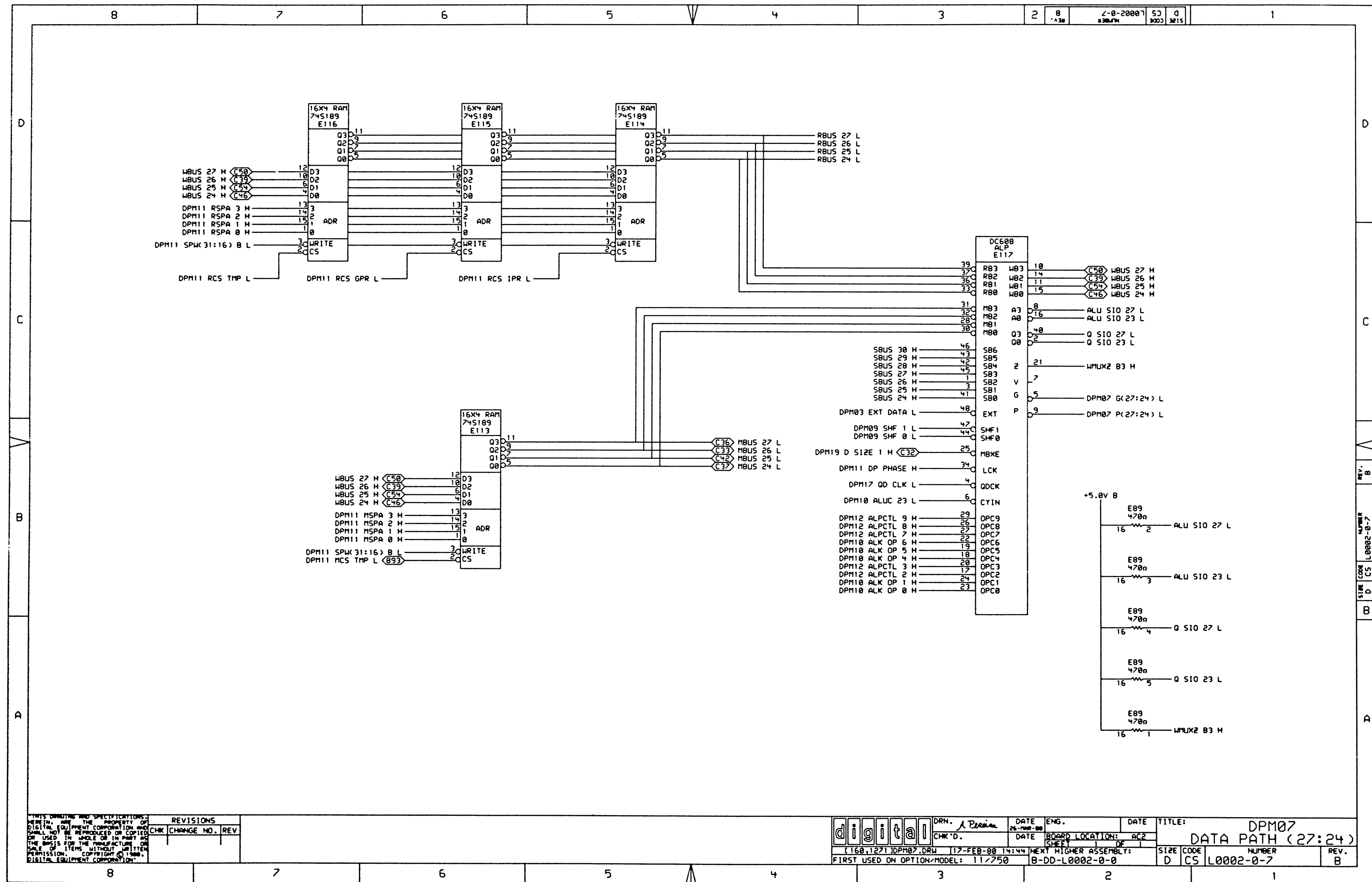
ENG.
DATE
BOARD LOCATION: AC2
SHEET 1 OF 1

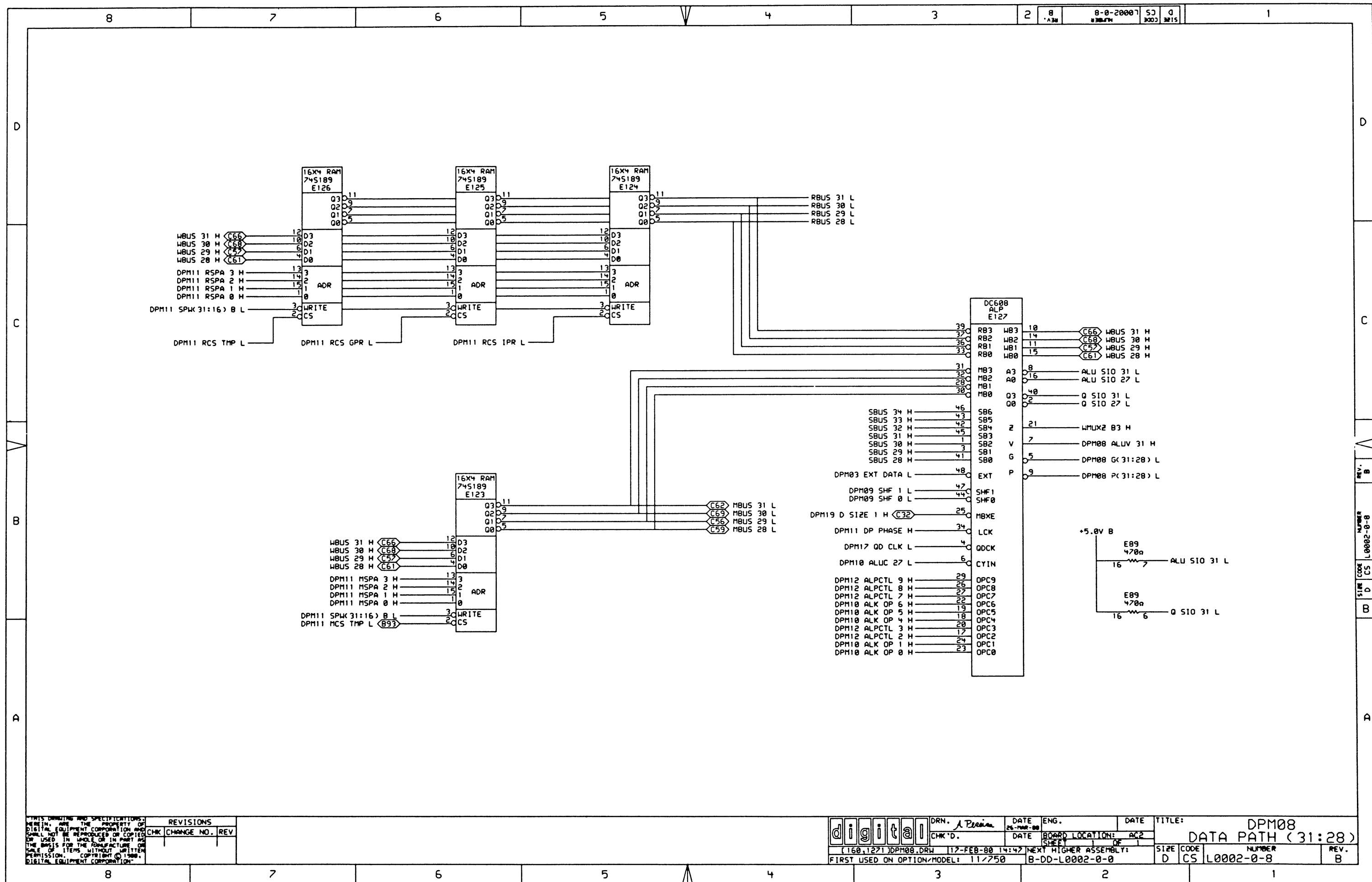
TITLE: DPM06
DATA PATH (23:20)

SIZE CODE
D CS

NUMBER
L0002-0-6

REV.
B





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REVISIONS		
CHK	CHANGE NO.	REV

digital

DRN. *A. P. P. P.*
CHK'D.

DATE 25-MAR-80
DATE

ENG.
SHEET 1 OF 1

DATE

TITLE:

DPM08
DATA PATH (31:28)

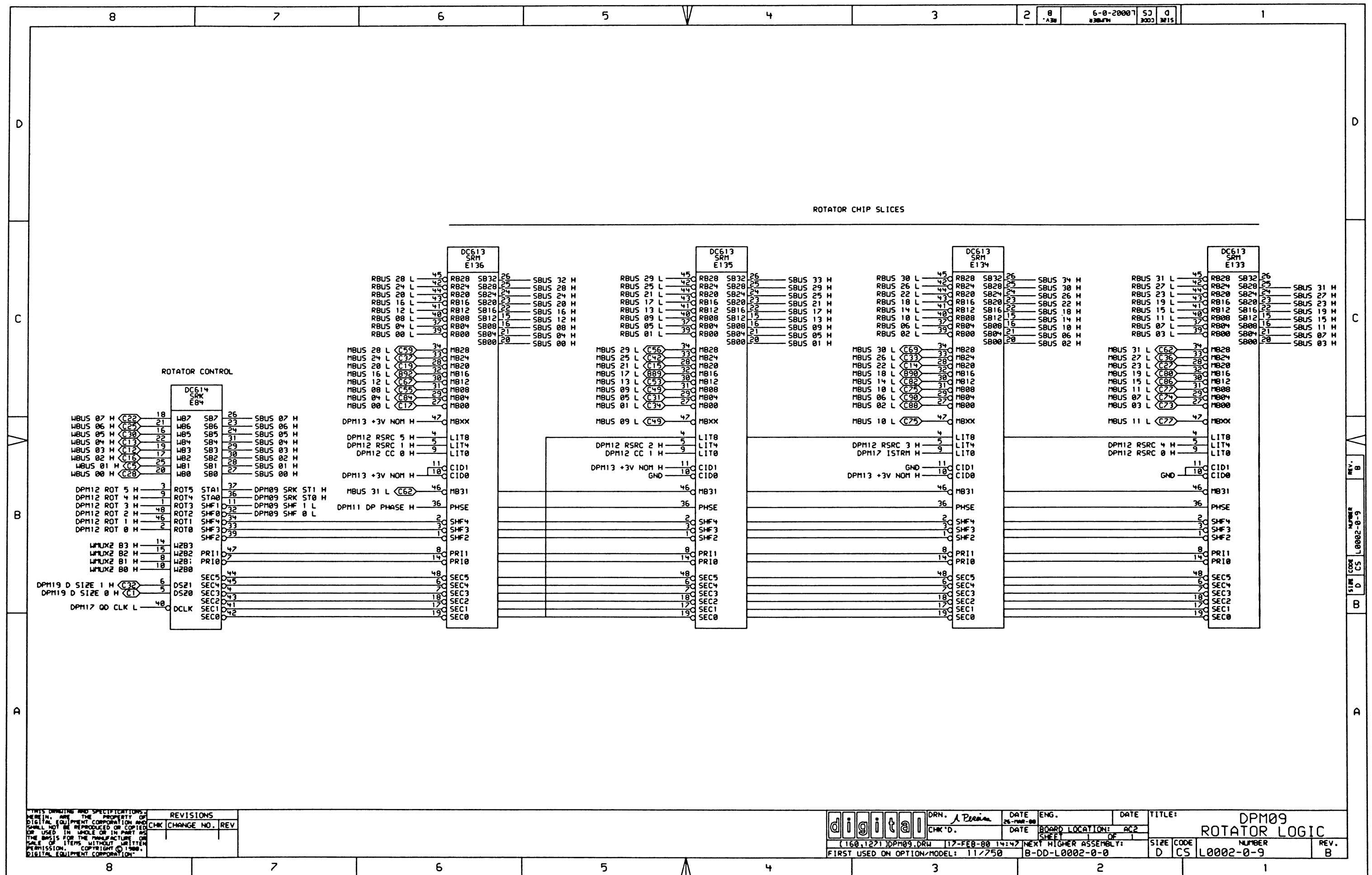
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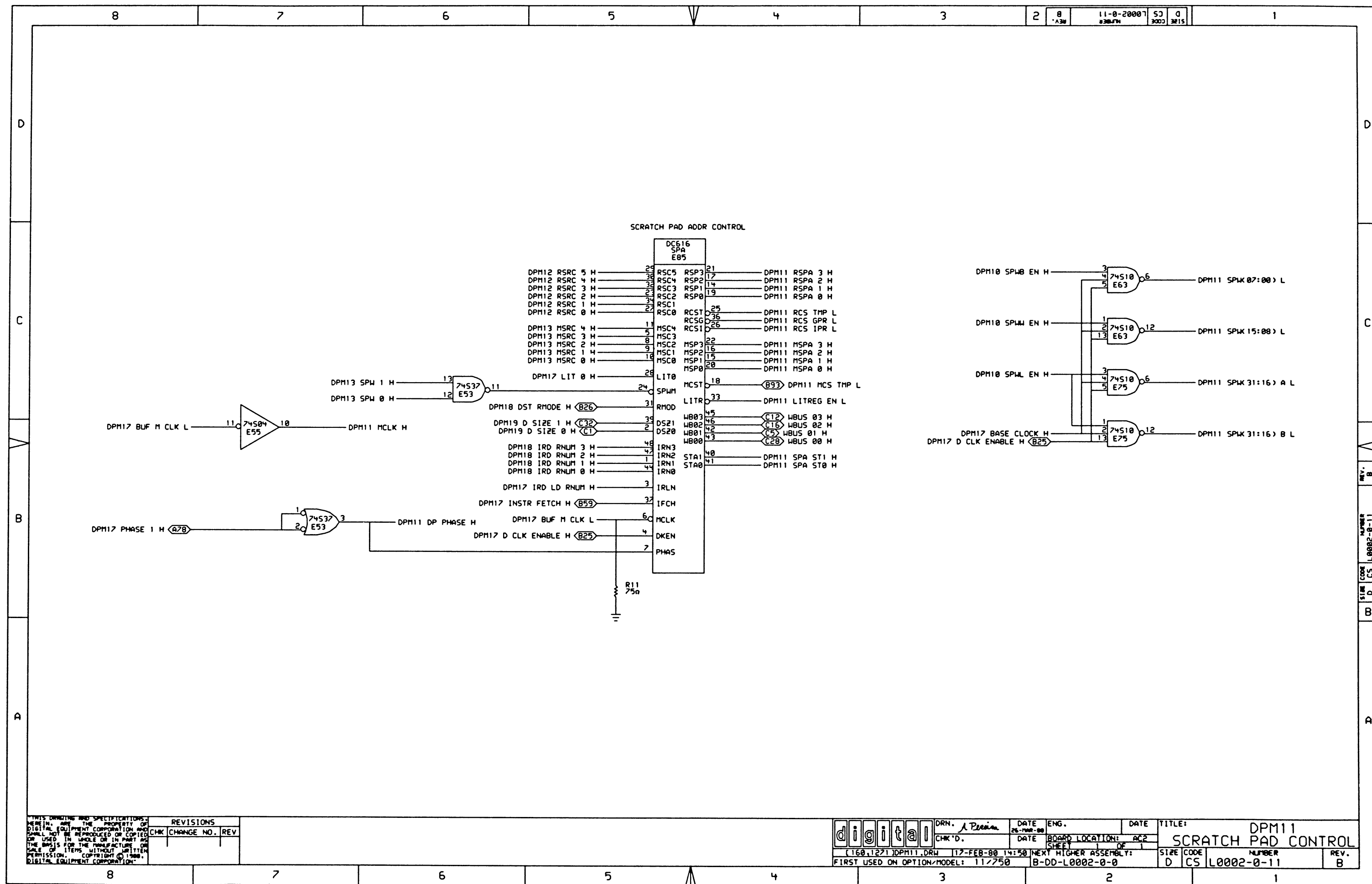
NEXT HIGHER ASSEMBLY:
B-DD-L0002-0-8

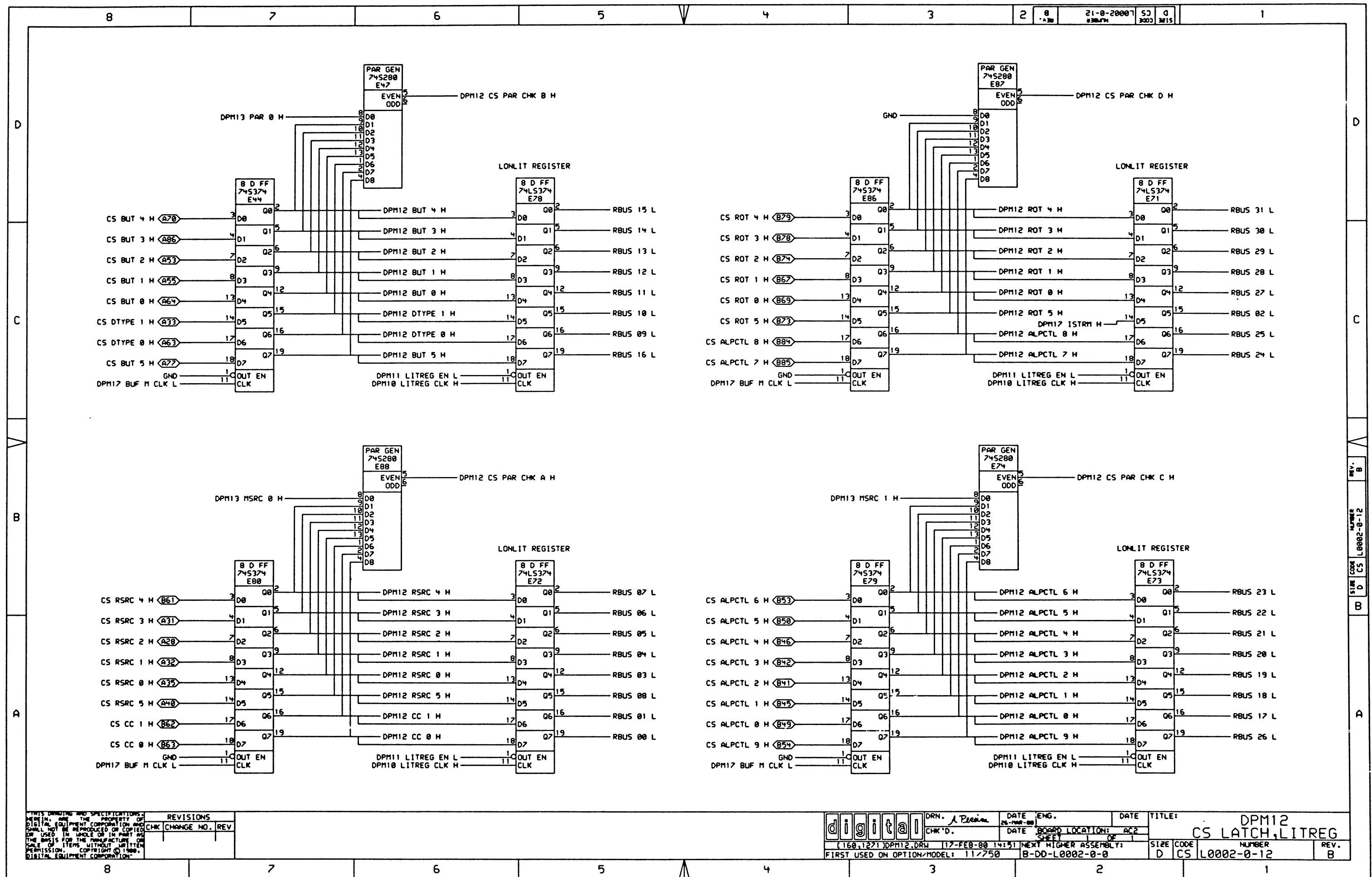
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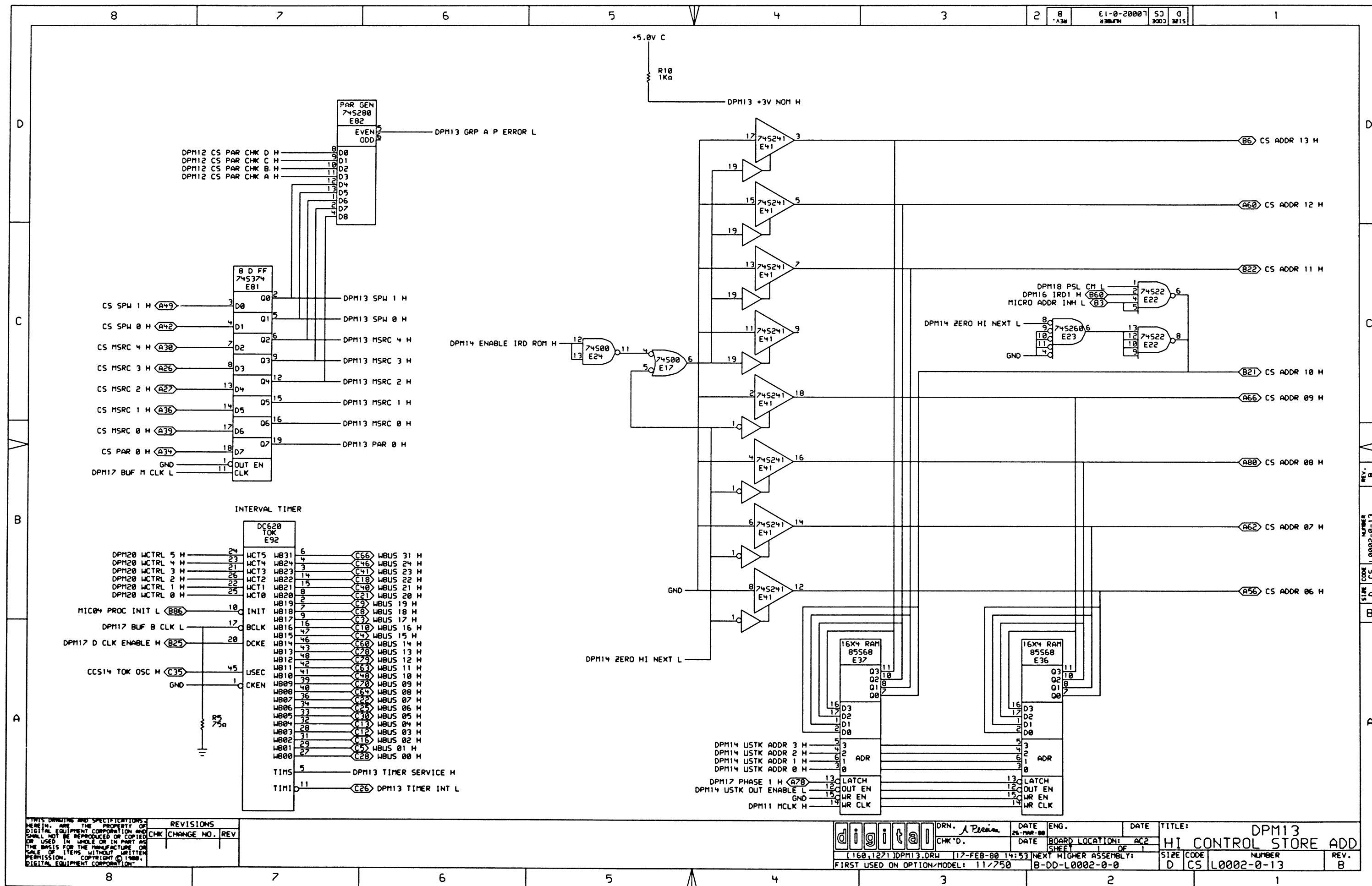
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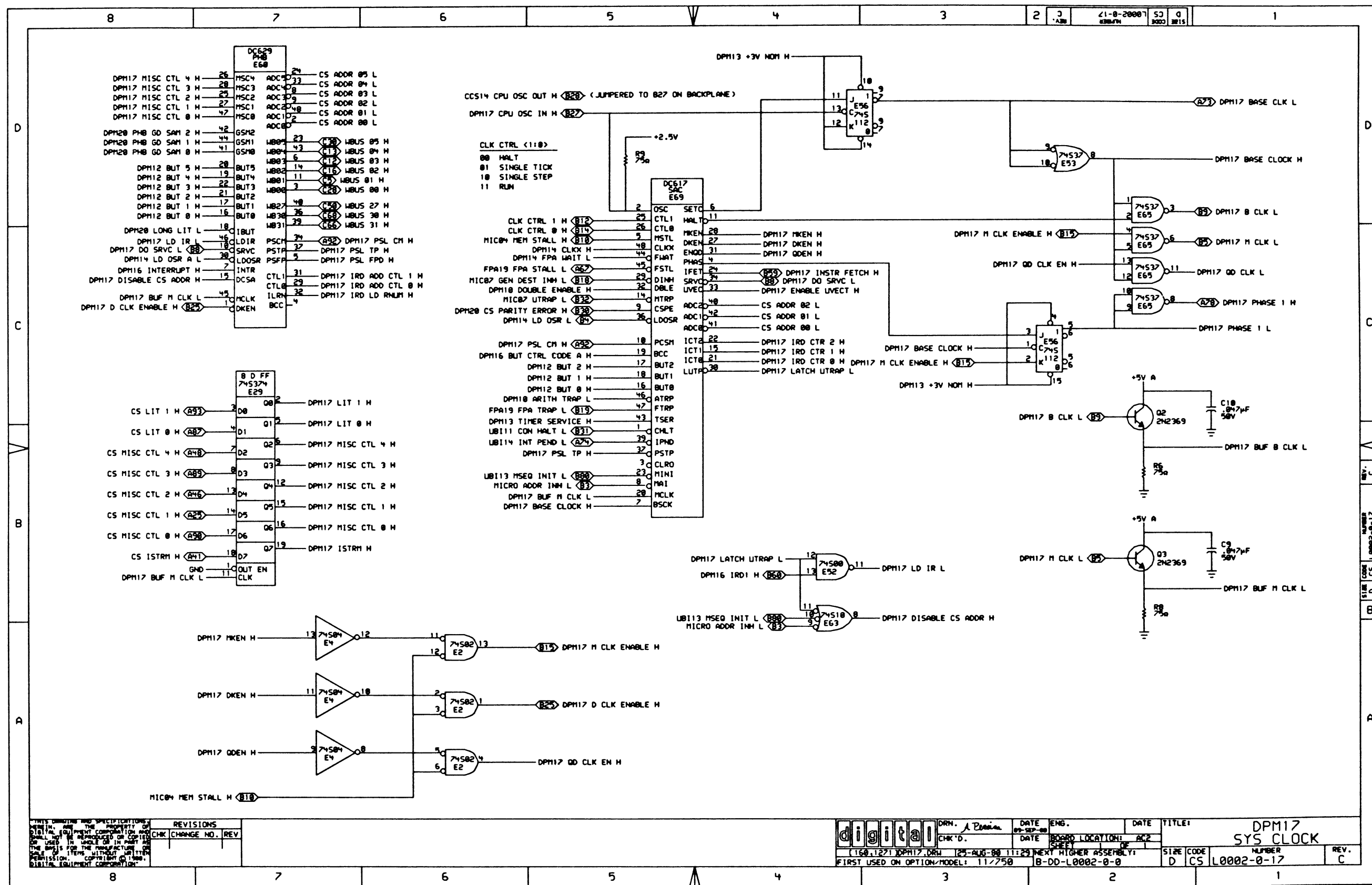
REV.
B

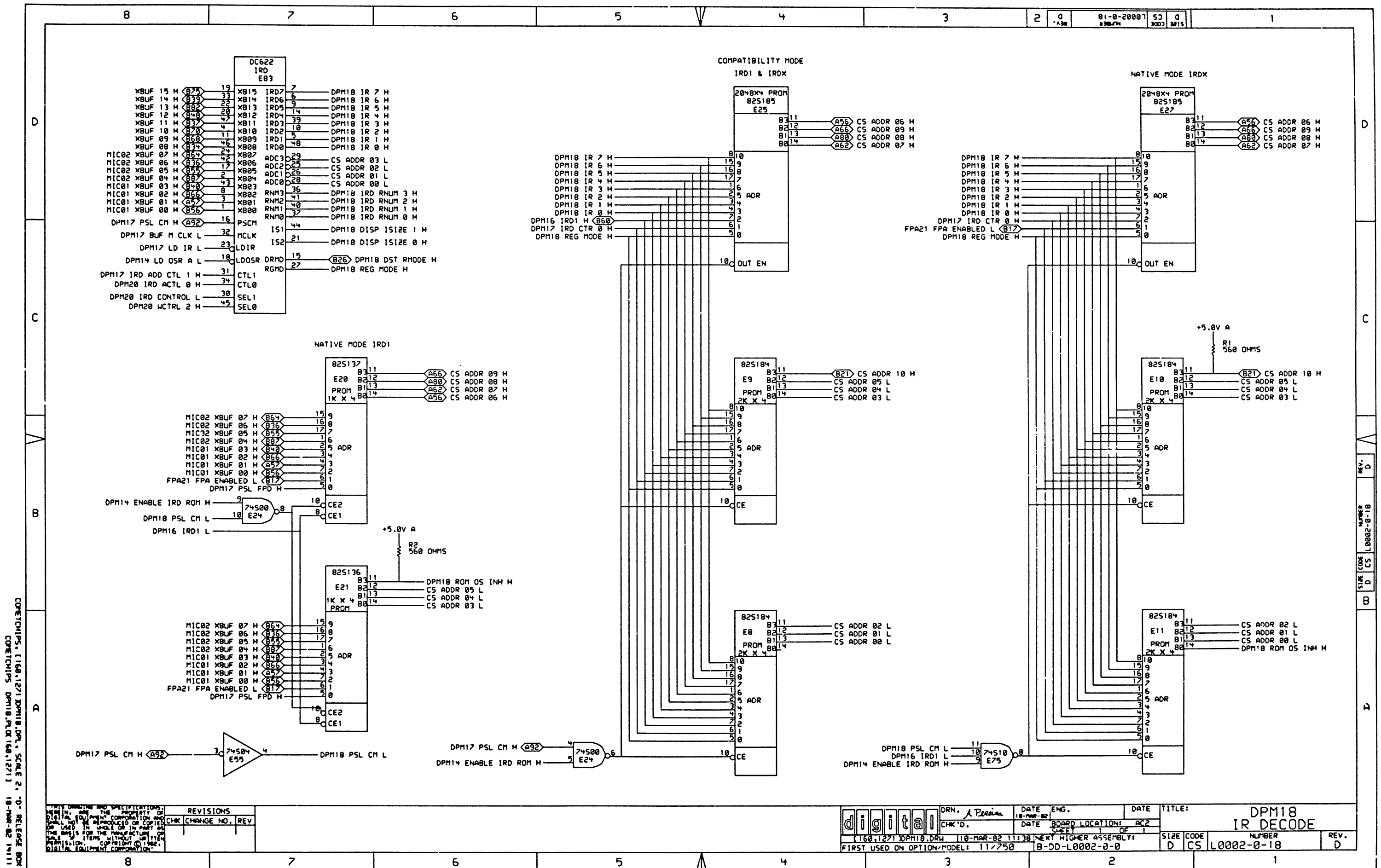


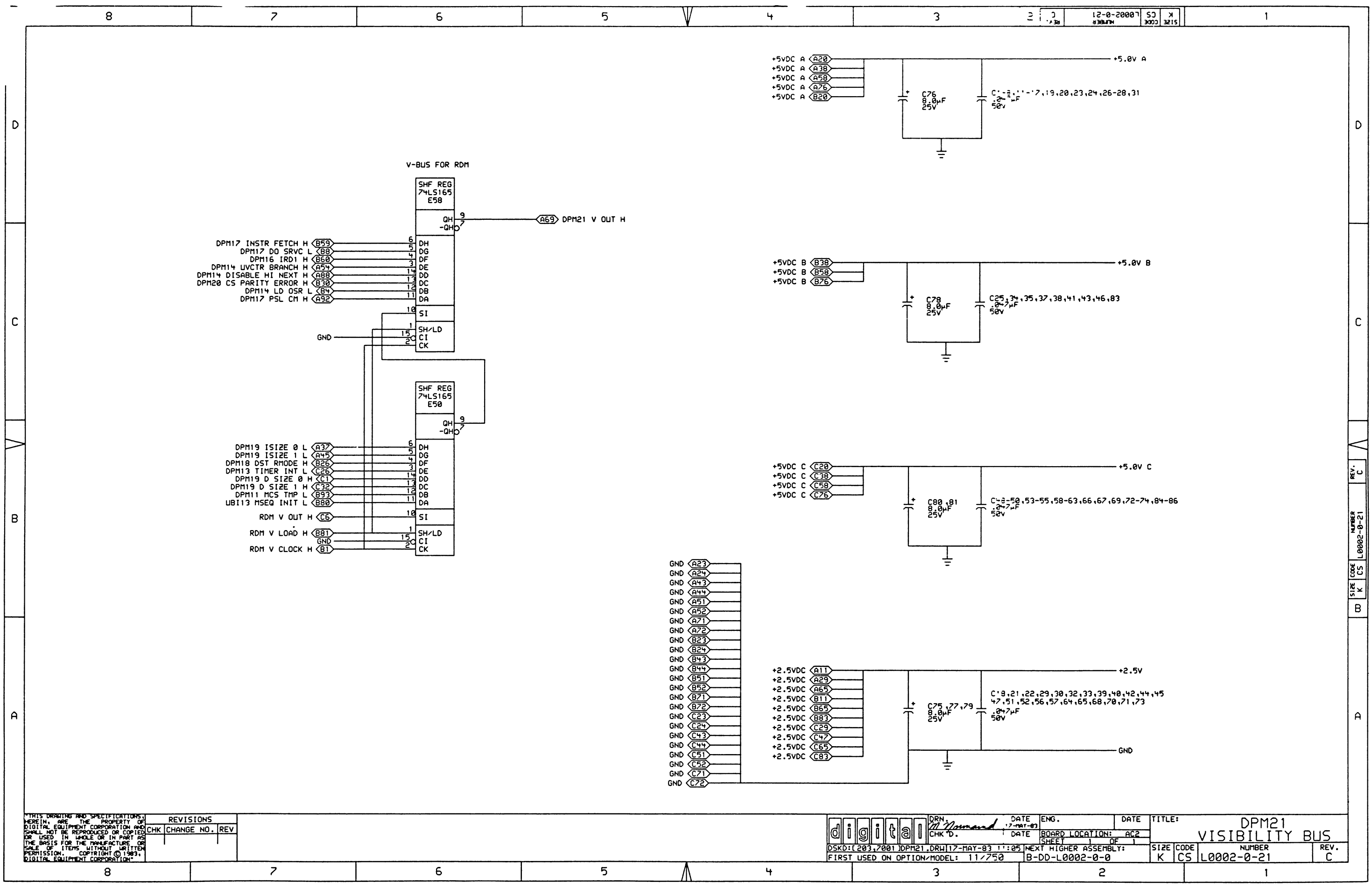












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REVISIONS		
CHK	CHANGE NO.	REV

digital	DRN.	DATE	ENG.	DATE	TITLE: DPM21 VISIBILITY BUS		
	CHK'D.	DATE	BOARD LOCATION: AC2	SHEET 1 OF 1	SIZE	CODE	NUMBER
DSKD: (203,2001) DPM21.DRW 17-MAY-83 11:05					K	C5	L0002-0-21
FIRST USED ON OPTION/MODEL: 11/750					REV. C		

8	7	6	5	4	3	2	1																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																				
<table><tr><td colspan="2">SIGNAL NAME</td><td colspan="2">PAGE NUMBER(S)</td><td colspan="2">SIGNAL NAME</td><td colspan="2">PAGE NUMBER(S)</td><td colspan="2">SIGNAL NAME</td><td colspan="2">PAGE NUMBER(S)</td></tr><tr><td colspan="2">ALU SIO 00 L</td><td colspan="2">01,10</td><td colspan="2">CS CC 1 H</td><td colspan="2">12</td><td colspan="2">DPM02 G(07:04) L</td><td colspan="2">10,02</td></tr><tr><td colspan="2">ALU SIO 03 L</td><td colspan="2">01,02</td><td colspan="2">CS CLKX H</td><td colspan="2">14</td><td colspan="2">DPM02 P(07:04) L</td><td colspan="2">10,02</td></tr><tr><td colspan="2">ALU SIO 07 L</td><td colspan="2">03,02</td><td colspan="2">CS DTYPE 0 H</td><td colspan="2">12</td><td colspan="2">DPM03 EXT DATA L</td><td colspan="2">03,05,06,07,08,04</td></tr><tr><td colspan="2">ALU SIO 11 L</td><td colspan="2">03,04</td><td colspan="2">CS DTYPE 1 H</td><td colspan="2">12</td><td colspan="2">DPM03 G(11:08) L</td><td colspan="2">10,03</td></tr><tr><td colspan="2">ALU SIO 15 L</td><td colspan="2">05,04</td><td colspan="2">CS HNEXT PAR H</td><td colspan="2">20</td><td colspan="2">DPM03 P(11:08) L</td><td colspan="2">10,03</td></tr><tr><td colspan="2">ALU SIO 19 L</td><td colspan="2">05,06</td><td colspan="2">CS ISTRM H</td><td colspan="2">17</td><td colspan="2">DPM04 ALUV 15 H</td><td colspan="2">10,04</td></tr><tr><td colspan="2">ALU SIO 23 L</td><td colspan="2">07,06</td><td colspan="2">CS JSR H</td><td colspan="2">14</td><td colspan="2">DPM04 G(15:12) L</td><td colspan="2">10,04</td></tr><tr><td colspan="2">ALU SIO 27 L</td><td colspan="2">07,08</td><td colspan="2">CS LIT 0 H</td><td colspan="2">17,20</td><td colspan="2">DPM04 P(15:12) L</td><td colspan="2">10,04</td></tr><tr><td colspan="2">ALU SIO 31 L</td><td colspan="2">08,10</td><td colspan="2">CS LIT 1 H</td><td colspan="2">17,20</td><td colspan="2">DPM05 G(19:16) L</td><td colspan="2">10,05</td></tr><tr><td colspan="2">CCS14 CPU OSC OUT H</td><td colspan="2">17</td><td colspan="2">CS MISC CTL 0 H</td><td colspan="2">17</td><td colspan="2">DPM05 P(19:16) L</td><td colspan="2">10,05</td></tr><tr><td colspan="2"> </td><td colspan="2"> </td><td colspan="2"> </td><td colspan="2"> </td><td colspan="2"> </td><td colspan="2"> </td></tr><tr><td colspan="2">CCS14 TOK OSC H</td><td colspan="2">13</td><td colspan="2">CS MISC CTL 1 H</td><td colspan="2">17</td><td colspan="2">DPM06 G(23:20) L</td><td colspan="2">10,06</td></tr><tr><td colspan="2">CLK CTRL 0 H</td><td colspan="2">17</td><td colspan="2">CS MISC CTL 2 H</td><td colspan="2">17</td><td colspan="2">DPM06 P(23:20) L</td><td colspan="2">10,06</td></tr><tr><td colspan="2">CLK CTRL 1 H</td><td colspan="2">17</td><td colspan="2">CS MISC CTL 3 H</td><td colspan="2">17</td><td colspan="2">DPM07 G(27:24) L</td><td colspan="2">10,07</td></tr><tr><td colspan="2">CS ADDR 00 H</td><td colspan="2">14</td><td colspan="2">CS MISC CTL 4 H</td><td colspan="2">17</td><td colspan="2">DPM07 P(27:24) L</td><td colspan="2">10,07</td></tr><tr><td colspan="2">CS ADDR 00 L</td><td colspan="2">18,14,17,16</td><td colspan="2">CS MSRC 0 H</td><td colspan="2">13</td><td colspan="2">DPM08 ALUV 31 H</td><td colspan="2">10,08</td></tr><tr><td colspan="2">CS ADDR 01 H</td><td colspan="2">14</td><td colspan="2">CS MSRC 1 H</td><td colspan="2">13</td><td colspan="2">DPM08 G(31:28) L</td><td colspan="2">10,08</td></tr><tr><td colspan="2">CS ADDR 01 L</td><td colspan="2">18,14,17,15</td><td colspan="2">CS MSRC 2 H</td><td colspan="2">13</td><td colspan="2">DPM08 P(31:28) L</td><td colspan="2">10,08</td></tr><tr><td colspan="2">CS ADDR 02 H</td><td colspan="2">14</td><td colspan="2">CS MSRC 3 H</td><td colspan="2">13</td><td colspan="2">DPM09 SHF 0 L</td><td colspan="2">09,05,01,06,02,07,03,08,04</td></tr><tr><td colspan="2">CS ADDR 02 L</td><td colspan="2">18,14,17,15</td><td colspan="2">CS MSRC 4 H</td><td colspan="2">13</td><td colspan="2">DPM09 SHF 1 L</td><td colspan="2">09,05,01,06,02,07,03,08,04</td></tr><tr><td colspan="2">CS ADDR 03 H</td><td colspan="2">14</td><td colspan="2">CS NEXT 00 H</td><td colspan="2">14</td><td colspan="2">DPM09 SRK ST0 H</td><td colspan="2">16,09</td></tr><tr><td colspan="2"> </td><td colspan="2"> </td><td colspan="2"> </td><td colspan="2"> </td><td colspan="2"> </td><td colspan="2"> </td></tr><tr><td colspan="2">CS ADDR 03 L</td><td colspan="2">18,14,17</td><td colspan="2">CS NEXT 01 H</td><td colspan="2">14</td><td colspan="2">DPM09 SRK ST1 H</td><td colspan="2">15,09</td></tr><tr><td colspan="2">CS ADDR 04 H</td><td colspan="2">14</td><td colspan="2">CS NEXT 02 H</td><td colspan="2">14</td><td colspan="2">DPM10 ALK OP 0 H</td><td colspan="2">10,05,01,06,02,07,03,08,04</td></tr><tr><td colspan="2">CS ADDR 04 L</td><td colspan="2">18,14,17</td><td colspan="2">CS NEXT 03 H</td><td colspan="2">14</td><td colspan="2">DPM10 ALK OP 1 H</td><td colspan="2">10,05,01,06,02,07,03,08,04</td></tr><tr><td colspan="2">CS ADDR 05 H</td><td colspan="2">14</td><td colspan="2">CS NEXT 04 H</td><td colspan="2">14</td><td colspan="2">DPM10 ALK OP 4 H</td><td colspan="2">10,05,01,06,02,07,03,08,04</td></tr><tr><td colspan="2">CS ADDR 05 L</td><td colspan="2">18,14,17</td><td colspan="2">CS NEXT 05 H</td><td colspan="2">14</td><td colspan="2">DPM10 ALK OP 5 H</td><td colspan="2">10,05,01,06,02,07,03,08,04</td></tr><tr><td colspan="2">CS ADDR 06 H</td><td colspan="2">18,13</td><td colspan="2">CS PAR 0 H</td><td colspan="2">13</td><td colspan="2">DPM10 ALK OP 6 H</td><td colspan="2">10,05,01,06,02,07,03,08,04</td></tr><tr><td colspan="2">CS ADDR 07 H</td><td colspan="2">18,13</td><td colspan="2">CS PAR 1 H</td><td colspan="2">20</td><td colspan="2">DPM10 ALUC 00 L</td><td colspan="2">10,01</td></tr><tr><td colspan="2">CS ADDR 08 H</td><td colspan="2">18,13</td><td colspan="2">CS ROT 0 H</td><td colspan="2">12</td><td colspan="2">DPM10 ALUC 03 L</td><td colspan="2">10,02</td></tr><tr><td colspan="2">CS ADDR 09 H</td><td colspan="2">18,13</td><td colspan="2">CS ROT 1 H</td><td colspan="2">12</td><td colspan="2">DPM10 ALUC 07 L</td><td colspan="2">10,03</td></tr><tr><td colspan="2">CS ADDR 10 H</td><td colspan="2">18,13</td><td colspan="2">CS ROT 2 H</td><td colspan="2">12</td><td colspan="2">DPM10 ALUC 11 L</td><td colspan="2">10,04</td></tr><tr><td colspan="2"> </td><td colspan="2"> </td><td colspan="2"> </td><td colspan="2"> </td><td colspan="2"> </td><td colspan="2"> </td></tr><tr><td colspan="2">CS ADDR 11 H</td><td colspan="2">13</td><td colspan="2">CS ROT 3 H</td><td colspan="2">12</td><td colspan="2">DPM10 ALUC 15 L</td><td colspan="2">10,05</td></tr><tr><td colspan="2">CS ADDR 12 H</td><td colspan="2">13</td><td colspan="2">CS ROT 4 H</td><td colspan="2">12</td><td colspan="2">DPM10 ALUC 19 L</td><td colspan="2">10,06</td></tr><tr><td colspan="2">CS ADDR 13 H</td><td colspan="2">13</td><td colspan="2">CS ROT 5 H</td><td colspan="2">12</td><td colspan="2">DPM10 ALUC 23 L</td><td colspan="2">10,07</td></tr><tr><td colspan="2">CS ALPCTL 0 H</td><td colspan="2">12</td><td colspan="2">CS RSRC 0 H</td><td colspan="2">12</td><td colspan="2">DPM10 ALUC 27 L</td><td colspan="2">10,08</td></tr><tr><td colspan="2">CS ALPCTL 1 H</td><td colspan="2">12</td><td colspan="2">CS RSRC 1 H</td><td colspan="2">12</td><td colspan="2">DPM10 ALUC 31 L</td><td colspan="2">10</td></tr><tr><td colspan="2">CS ALPCTL 2 H</td><td colspan="2">12</td><td colspan="2">CS RSRC 2 H</td><td colspan="2">12</td><td colspan="2">DPM10 ARITH TRAP L</td><td colspan="2">17,10</td></tr><tr><td colspan="2">CS ALPCTL 3 H</td><td colspan="2">12</td><td colspan="2">CS RSRC 3 H</td><td colspan="2">12</td><td colspan="2">DPM10 CCBR 0 H</td><td colspan="2">16,10,15</td></tr><tr><td colspan="2">CS ALPCTL 4 H</td><td colspan="2">12</td><td colspan="2">CS RSRC 4 H</td><td colspan="2">12</td><td colspan="2">DPM10 CCBR 1 H</td><td colspan="2">15,16,10</td></tr><tr><td colspan="2">CS ALPCTL 5 H</td><td colspan="2">12</td><td colspan="2">CS RSRC 5 H</td><td colspan="2">12</td><td colspan="2">DPM10 DOUBLE ENABLE H</td><td colspan="2">10,17</td></tr><tr><td colspan="2">CS ALPCTL 6 H</td><td colspan="2">12</td><td colspan="2">CS SPW 0 H</td><td colspan="2">13</td><td colspan="2">DPM10 LITREG CLK H</td><td colspan="2">12,10</td></tr><tr><td colspan="2"> </td><td colspan="2"> </td><td colspan="2"> </td><td colspan="2"> </td><td colspan="2"> </td><td colspan="2"> </td></tr><tr><td colspan="2">CS ALPCTL 7 H</td><td colspan="2">12</td><td colspan="2">CS SPW 1 H</td><td colspan="2">13</td><td colspan="2">DPM10 NON BCD H</td><td colspan="2">16,10</td></tr><tr><td colspan="2">CS ALPCTL 8 H</td><td colspan="2">12</td><td colspan="2">CS WCTRL 0 H</td><td colspan="2">20</td><td colspan="2">DPM10 PSLC H</td><td colspan="2">16,10</td></tr><tr><td colspan="2">CS ALPCTL 9 H</td><td colspan="2">12</td><td colspan="2">CS WCTRL 1 H</td><td colspan="2">20</td><td colspan="2">DPM10 SPWB EN H</td><td colspan="2">11,10</td></tr><tr><td colspan="2">CS BUT 0 H</td><td colspan="2">12</td><td colspan="2">CS WCTRL 2 H</td><td colspan="2">20</td><td colspan="2">DPM10 SPWL EN H</td><td colspan="2">11,10</td></tr><tr><td colspan="2">CS BUT 1 H</td><td colspan="2">12</td><td colspan="2">CS WCTRL 3 H</td><td colspan="2">20</td><td colspan="2">DPM10 SPWH EN H</td><td colspan="2">11,10</td></tr><tr><td colspan="2">CS BUT 2 H</td><td colspan="2">12</td><td colspan="2">CS WCTRL 4 H</td><td colspan="2">20</td><td colspan="2">DPM10 X (15:08) EN L</td><td colspan="2">10,03,04</td></tr><tr><td colspan="2">CS BUT 3 H</td><td colspan="2">12</td><td colspan="2">CS WCTRL 5 H</td><td colspan="2">20</td><td colspan="2">DPM11 DP PHASE H</td><td colspan="2">11,05,01,06,02,07,03,08,04,09</td></tr><tr><td colspan="2">CS BUT 4 H</td><td colspan="2">12</td><td colspan="2">DPM01 G(03:00) L</td><td colspan="2">10,01</td><td colspan="2">DPM11 LITREG EN L</td><td colspan="2">12,11</td></tr><tr><td colspan="2">CS BUT 5 H</td><td colspan="2">12</td><td colspan="2">DPM01 P(03:00) L</td><td colspan="2">10,01</td><td colspan="2">DPM11 MCLK H</td><td colspan="2">13,14,11</td></tr><tr><td colspan="2">CS CC 0 H</td><td colspan="2">12</td><td colspan="2">DPM02 ALUV 07 H</td><td colspan="2">10,02</td><td colspan="2">DPM11 MCS TMP L</td><td colspan="2">21,11,05,01,06,02,07,03,08,04</td></tr></table>								SIGNAL NAME		PAGE NUMBER(S)		SIGNAL NAME		PAGE NUMBER(S)		SIGNAL NAME		PAGE NUMBER(S)		ALU SIO 00 L		01,10		CS CC 1 H		12		DPM02 G(07:04) L		10,02		ALU SIO 03 L		01,02		CS CLKX H		14		DPM02 P(07:04) L		10,02		ALU SIO 07 L		03,02		CS DTYPE 0 H		12		DPM03 EXT DATA L		03,05,06,07,08,04		ALU SIO 11 L		03,04		CS DTYPE 1 H		12		DPM03 G(11:08) L		10,03		ALU SIO 15 L		05,04		CS HNEXT PAR H		20		DPM03 P(11:08) L		10,03		ALU SIO 19 L		05,06		CS ISTRM H		17		DPM04 ALUV 15 H		10,04		ALU SIO 23 L		07,06		CS JSR H		14		DPM04 G(15:12) L		10,04		ALU SIO 27 L		07,08		CS LIT 0 H		17,20		DPM04 P(15:12) L		10,04		ALU SIO 31 L		08,10		CS LIT 1 H		17,20		DPM05 G(19:16) L		10,05		CCS14 CPU OSC OUT H		17		CS MISC CTL 0 H		17		DPM05 P(19:16) L		10,05														CCS14 TOK OSC H		13		CS MISC CTL 1 H		17		DPM06 G(23:20) L		10,06		CLK CTRL 0 H		17		CS MISC CTL 2 H		17		DPM06 P(23:20) L		10,06		CLK CTRL 1 H		17		CS MISC CTL 3 H		17		DPM07 G(27:24) L		10,07		CS ADDR 00 H		14		CS MISC CTL 4 H		17		DPM07 P(27:24) L		10,07		CS ADDR 00 L		18,14,17,16		CS MSRC 0 H		13		DPM08 ALUV 31 H		10,08		CS ADDR 01 H		14		CS MSRC 1 H		13		DPM08 G(31:28) L		10,08		CS ADDR 01 L		18,14,17,15		CS MSRC 2 H		13		DPM08 P(31:28) L		10,08		CS ADDR 02 H		14		CS MSRC 3 H		13		DPM09 SHF 0 L		09,05,01,06,02,07,03,08,04		CS ADDR 02 L		18,14,17,15		CS MSRC 4 H		13		DPM09 SHF 1 L		09,05,01,06,02,07,03,08,04		CS ADDR 03 H		14		CS NEXT 00 H		14		DPM09 SRK ST0 H		16,09														CS ADDR 03 L		18,14,17		CS NEXT 01 H		14		DPM09 SRK ST1 H		15,09		CS ADDR 04 H		14		CS NEXT 02 H		14		DPM10 ALK OP 0 H		10,05,01,06,02,07,03,08,04		CS ADDR 04 L		18,14,17		CS NEXT 03 H		14		DPM10 ALK OP 1 H		10,05,01,06,02,07,03,08,04		CS ADDR 05 H		14		CS NEXT 04 H		14		DPM10 ALK OP 4 H		10,05,01,06,02,07,03,08,04		CS ADDR 05 L		18,14,17		CS NEXT 05 H		14		DPM10 ALK OP 5 H		10,05,01,06,02,07,03,08,04		CS ADDR 06 H		18,13		CS PAR 0 H		13		DPM10 ALK OP 6 H		10,05,01,06,02,07,03,08,04		CS ADDR 07 H		18,13		CS PAR 1 H		20		DPM10 ALUC 00 L		10,01		CS ADDR 08 H		18,13		CS ROT 0 H		12		DPM10 ALUC 03 L		10,02		CS ADDR 09 H		18,13		CS ROT 1 H		12		DPM10 ALUC 07 L		10,03		CS ADDR 10 H		18,13		CS ROT 2 H		12		DPM10 ALUC 11 L		10,04														CS ADDR 11 H		13		CS ROT 3 H		12		DPM10 ALUC 15 L		10,05		CS ADDR 12 H		13		CS ROT 4 H		12		DPM10 ALUC 19 L		10,06		CS ADDR 13 H		13		CS ROT 5 H		12		DPM10 ALUC 23 L		10,07		CS ALPCTL 0 H		12		CS RSRC 0 H		12		DPM10 ALUC 27 L		10,08		CS ALPCTL 1 H		12		CS RSRC 1 H		12		DPM10 ALUC 31 L		10		CS ALPCTL 2 H		12		CS RSRC 2 H		12		DPM10 ARITH TRAP L		17,10		CS ALPCTL 3 H		12		CS RSRC 3 H		12		DPM10 CCBR 0 H		16,10,15		CS ALPCTL 4 H		12		CS RSRC 4 H		12		DPM10 CCBR 1 H		15,16,10		CS ALPCTL 5 H		12		CS RSRC 5 H		12		DPM10 DOUBLE ENABLE H		10,17		CS ALPCTL 6 H		12		CS SPW 0 H		13		DPM10 LITREG CLK H		12,10														CS ALPCTL 7 H		12		CS SPW 1 H		13		DPM10 NON BCD H		16,10		CS ALPCTL 8 H		12		CS WCTRL 0 H		20		DPM10 PSLC H		16,10		CS ALPCTL 9 H		12		CS WCTRL 1 H		20		DPM10 SPWB EN H		11,10		CS BUT 0 H		12		CS WCTRL 2 H		20		DPM10 SPWL EN H		11,10		CS BUT 1 H		12		CS WCTRL 3 H		20		DPM10 SPWH EN H		11,10		CS BUT 2 H		12		CS WCTRL 4 H		20		DPM10 X (15:08) EN L		10,03,04		CS BUT 3 H		12		CS WCTRL 5 H		20		DPM11 DP PHASE H		11,05,01,06,02,07,03,08,04,09		CS BUT 4 H		12		DPM01 G(03:00) L		10,01		DPM11 LITREG EN L		12,11		CS BUT 5 H		12		DPM01 P(03:00) L		10,01		DPM11 MCLK H		13,14,11		CS CC 0 H		12		DPM02 ALUV 07 H		10,02		DPM11 MCS TMP L		21,11,05,01,06,02,07,03,08,04	
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REVISIONS		DATE		ENG.		DATE		TITLE:	
CHK	CHANGE NO.	REV							
		19-MAR-80 12:49		B-00		AC2		DPM22	
		11/250		B-DD-L0002-0-0		D		CS L0002-0-22	
								FORWARD REFERENCE	
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REVISIONS		
CHK	CHANGE NO.	REV

digital	DRN. <i>027000</i>	DATE <i>21 MAR 80</i>	ENG.	DATE	TITLE: <i>DPM23</i>			
	CHK'D.	DATE	BOARD LOCATION: <i>AC2</i>		FORWARD REFERENCE			
		SHEET <i>1</i> OF <i>1</i>			SIZE	CODE	NUMBER	REV.
<i>(160,1271)DPM23.DRW</i>		<i>19-MAR-80 12:54</i>	NEXT HIGHER ASSEMBLY:		<i>D</i>	<i>CS</i>	<i>L0002-0-23</i>	<i>B</i>
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<table><thead><tr><th>SIGNAL NAME</th><th>PAGE NUMBER(S)</th><th>SIGNAL NAME</th><th>PAGE NUMBER(S)</th><th>SIGNAL NAME</th><th>PAGE NUMBER(S)</th></tr></thead><tbody><tr><td>DPM18 REG MODE M</td><td>18</td><td>MBUS 09 L</td><td>03,09</td><td>Q SIO 31 L</td><td>08,10</td></tr><tr><td>DPM18 ROM OS INH M</td><td>14,18</td><td>MBUS 10 L</td><td>03,09</td><td>RBUS 00 L</td><td>12,01,09</td></tr><tr><td>DPM19 D SIZE 0 M</td><td>19,21,03,10,09,11</td><td>MBUS 11 L</td><td>03,09</td><td>RBUS 01 L</td><td>12,01,09</td></tr><tr><td>DPM19 D SIZE 1 M</td><td>19,21,10,09,11,05,06,07,08</td><td>MBUS 12 L</td><td>04,09</td><td>RBUS 02 L</td><td>12,01,09</td></tr><tr><td>DPM19 DSIZE LATCH BYTE L</td><td>19,16</td><td>MBUS 13 L</td><td>04,09</td><td>RBUS 03 L</td><td>12,01,09</td></tr><tr><td>DPM19 DSIZE LATCH 0 (0)M</td><td>19</td><td>MBUS 14 L</td><td>04,09</td><td>RBUS 04 L</td><td>12,02,09</td></tr><tr><td>DPM19 DSIZE LATCH 0 (1)M</td><td>19,16</td><td>MBUS 15 L</td><td>03,04,09</td><td>RBUS 05 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M</td><td>20,17</td><td>MBUS 31 L</td><td>08,09</td><td>RBUS 21 L</td><td>12,06,09</td></tr><tr><td>DPM20 PHB GD SAM 2 M</td><td>20,17</td><td>MIC01 XBUF 00 M</td><td>18</td><td>RBUS 22 L</td><td>12,06,09</td></tr><tr><td>DPM20 WCTRL 0 M</td><td>20,13</td><td>MIC01 XBUF 01 M</td><td>18</td><td>RBUS 23 L</td><td>12,06,09</td></tr><tr><td>DPM20 WCTRL 1 M</td><td>20,13</td><td>MIC01 XBUF 02 M</td><td>18</td><td>RBUS 24 L</td><td>12,07,09</td></tr><tr><td>DPM20 WCTRL 2 M</td><td>20,18,13</td><td>MIC01 XBUF 03 M</td><td>18</td><td>RBUS 25 L</td><td>12,07,09</td></tr><tr><td>DPM20 WCTRL 3 M</td><td>20,13</td><td>MIC02 XBUF 04 M</td><td>18</td><td>RBUS 26 L</td><td>12,07,09</td></tr><tr><td>DPM20 WCTRL 4 M</td><td>20,13</td><td>MIC02 XBUF 05 M</td><td>18</td><td>RBUS 27 L</td><td>12,07,09</td></tr><tr><td>DPM20 WCTRL 5 M</td><td>20,13</td><td>MIC02 XBUF 06 M</td><td>18</td><td>RBUS 28 L</td><td>12,08,09</td></tr><tr><td></td><td></td><td></td><td></td><td></td><td></td></tr><tr><td>DPM21 V OUT M</td><td>21</td><td>MIC02 XBUF 07 M</td><td>18</td><td>RBUS 29 L</td><td>12,08,09</td></tr><tr><td>FP BOOT 0 M</td><td>16</td><td>MIC04 LATCHED MBUS 15 L</td><td>15</td><td>RBUS 30 L</td><td>12,08,09</td></tr><tr><td>FP BOOT 1 M</td><td>15</td><td>MIC04 MEM STALL M</td><td>17,20</td><td>RBUS 31 L</td><td>12,08,09</td></tr><tr><td>FP START 0 M</td><td>16</td><td>MIC04 MSRC XB M</td><td>19</td><td>RDM V CLOCK M</td><td>21</td></tr><tr><td>FP START 1 M</td><td>15</td><td>MIC04 PROC INIT L</td><td>10,13</td><td>RDM V LOAD M</td><td>21</td></tr><tr><td>FPA19 FPA TRAP L</td><td>17</td><td>MIC07 GEN DEST INH L</td><td>17</td><td>RDM V OUT M</td><td>21</td></tr><tr><td>FPA21 FPA ENABLED L</td><td>18,16,10</td><td>MIC07 UTRAP L</td><td>17</td><td>SBUS 00 M</td><td>09,01</td></tr><tr><td>FPA21 FPA PRESENT L</td><td>15</td><td>MICRO ADDR INH L</td><td>13,14,17</td><td>SBUS 01 M</td><td>09,01</td></tr><tr><td>FPA19 FPA STALL L</td><td>17</td><td>MICRO VECTOR 0 M</td><td>14</td><td>SBUS 02 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L	05,09	RBUS 06 L	12,02,09	DPM19 DSIZE LATCH 1 (1)M	19,15	MBUS 17 L	05,09	RBUS 07 L	12,02,09	DPM19 FORCE DTYPE L	19	MBUS 18 L	05,09	RBUS 08 L	12,03,09	DPM19 ISIZE 0 L	19,21					DPM19 ISIZE 1 L	19,21	MBUS 19 L	05,09	RBUS 09 L	12,03,09	DPM19 LD OSR A M	14,19	MBUS 20 L	06,09	RBUS 10 L	12,03,09	DPM20 CC CTRL 0 M	20,10	MBUS 21 L	06,09	RBUS 11 L	12,03,09	DPM20 CC CTRL 1 M	20,10	MBUS 22 L	06,09	RBUS 12 L	12,04,09	DPM20 CC CTRL 2 M	20,10	MBUS 23 L	06,09	RBUS 13 L	12,04,09	DPM20 CC CTRL 3 M	20,10	MBUS 24 L	07,09	RBUS 14 L	12,04,09	DPM20 CS PARITY ERROR M	20,21,17	MBUS 25 L	07,09	RBUS 15 L	12,04,09	DPM20 IRD ACTL 0 M	20,18	MBUS 26 L	07,09	RBUS 16 L	12,05,09	DPM20 IRD CONTROL L	20,18	MBUS 27 L	07,09	RBUS 17 L	12,05,09	DPM20 LONG LIT L	16,20,19,17,10	MBUS 28 L	08,09	RBUS 18 L	12,05,09							DPM20 PAR 1 M	20	MBUS 29 L	08,09	RBUS 19 L	12,05,09	DPM20 PHB GD SAM 0 M	20,17	MBUS 30 L	08,09	RBUS 20 L	12,06,09	DPM20 PHB GD SAM 1 M	20,17	MBUS 31 L	08,09	RBUS 21 L	12,06,09	DPM20 PHB GD SAM 2 M	20,17	MIC01 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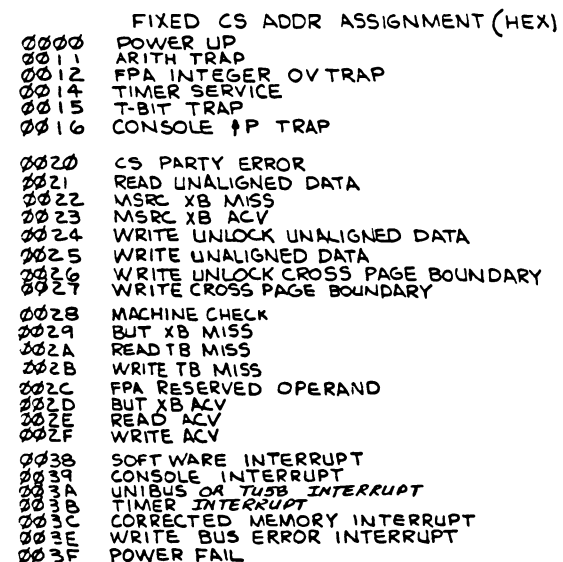
CS LATCH

Q

D

CS NEXT<13:06> L

LOGIC LOCATED ON CCS MODULE, L0005



MICRO SEQ. PORTION

OPN <i>Brian House</i>	DATE <i>16 Jan 80</i>	TITLE <i>DPM</i> BLOCK DIAGRAM		digital	
CHG TO <i>J.F. Sullivan</i>	DATE <i>JAN 24 80</i>				
DES. ENG. <i>Dominic Li</i>	DATE <i>MAR 21, 80</i>				
APP. ENG. <i>Dominic Li</i>	DATE <i>MAR 21, 80</i>				
WFO. ENG. <i>W. Parker</i>	DATE <i>3/20/80</i>	DOCUMENT NUMBER			
NEXT HIGH-DOC <i>R-DD-1-0002-0</i>		SIZE D	CODE BD	NUMBER L0002-0-26	REV B
		SCALE <i>1/4"</i>	SHEET <i>1</i> OF <i>2</i>		

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DRN RWCauten	26 JUN 79	FIRST USED ON		digital	
CHK'D J.F.S.	24 JUN 79	TITLE DPM BLOCK DIAGRAM			
ENG D. G.	3/2/80				
PROJ. ENG. D. Case	3/21/80				
PROD AFK/ken	3/20/80				
NEXT HIGHER ASSY.					
B-DD-LOO02-0		SIZE	CODE BD	NUMBER LOO02-0-26	REV. B
SCALE 1 1					
SHEET 2 OF 2		DIST.			

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MICRO ORDER →		0		1		2		3		4		5		6		7		8		9		A		B		C		D		E		F	
ALU	AMUX, BMUX	M, R1		M, R2		M, Q1		M, Q2		M, S		XM, R		XM, Q		XM, S		D, R1		D, R2		D, Q1		D, Q2		D, S		0, S		R, Q		R, S	
0	A-B-CI	NOP	D=MX	SQ	SQ	NOP	D=MX	SQ	SQ	NOP	D=MX	NOP	D=MX	NOP	D=MX	NOP	D=MX	NOP	D=MX	SQ	RESVRD	NOP	D=MX	SQ	SQ	NOP	D=MX	NOP	D=MX	NOP	D=MX	NOP	D=MX
		Q=MX	D=MX	SQR	SQR	Q=MX	D=MX	SQR	SQR	Q=MX	D=MX	Q=MX	D=MX	Q=MX	D=MX	Q=MX	D=MX	Q=MX	D=MX	SQR	RESVRD	Q=MX	D=MX	SQR	SQR	Q=MX	D=MX	Q=MX	D=MX	Q=MX	D=MX	Q=MX	D=MX
1	A-B-CI, BCD	NOP	D=MX	SQ	SQ	NOP	D=MX	SQ	SQ	NOP	D=MX	NOP	D=MX	NOP	D=MX	NOP	D=MX	NOP	D=MX	SQ	RESVRD	NOP	D=MX	SQ	SQ	NOP	D=MX	NOP	D=MX	NOP	D=MX	NOP	D=MX
		Q=MX	D=MX	SQR	SQR	Q=MX	D=MX	SQR	SQR	Q=MX	D=MX	Q=MX	D=MX	Q=MX	D=MX	Q=MX	D=MX	Q=MX	D=MX	SQR	RESVRD	Q=MX	D=MX	SQR	SQR	Q=MX	D=MX	Q=MX	D=MX	Q=MX	D=MX	Q=MX	D=MX
2	(A-B-CI).SR	NOP	D=MX	SQ	SQ	NOP	D=MX	SQ	SQ	NOP	D=MX	NOP	D=MX	NOP	D=MX	NOP	D=MX	NOP	D=MX	SQ	RESVRD	NOP	D=MX	SQ	SQ	NOP	D=MX	NOP	D=MX	NOP	D=MX	NOP	D=MX
		Q=MX	D=MX	SQR	SQR	Q=MX	D=MX	SQR	SQR	Q=MX	D=MX	Q=MX	D=MX	Q=MX	D=MX	Q=MX	D=MX	Q=MX	D=MX	SQR	RESVRD	Q=MX	D=MX	SQR	SQR	Q=MX	D=MX	Q=MX	D=MX	Q=MX	D=MX	Q=MX	D=MX
3	(A-B-CI).SL	NOP	D=MX	SQ	SQ	NOP	D=MX	SQ	SQ	NOP	D=MX	NOP	D=MX	NOP	D=MX	NOP	D=MX	NOP	D=MX	SQ	RESVRD	NOP	D=MX	SQ	SQ	NOP	D=MX	NOP	D=MX	NOP	D=MX	NOP	D=MX
		Q=MX	D=MX	SQR	SQR	Q=MX	D=MX	SQR	SQR	Q=MX	D=MX	Q=MX	D=MX	Q=MX	D=MX	Q=MX	D=MX	Q=MX	D=MX	SQR	RESVRD	Q=MX	D=MX	SQR	SQR	Q=MX	D=MX	Q=MX	D=MX	Q=MX	D=MX	Q=MX	D=MX
4	A+B+CI	NOP	D=MX	SQ	SQ	NOP	D=MX	SQ	SQ	NOP	D=MX	NOP	D=MX	NOP	D=MX	NOP	D=MX	NOP	D=MX	SQ	RESVRD	NOP	D=MX	SQ	SQ	NOP	D=MX	NOP	D=MX	NOP	D=MX	NOP	D=MX
		Q=MX	D=MX	SQR	SQR	Q=MX	D=MX	SQR	SQR	Q=MX	D=MX	Q=MX	D=MX	Q=MX	D=MX	Q=MX	D=MX	Q=MX	D=MX	SQR	RESVRD	Q=MX	D=MX	SQR	SQR	Q=MX	D=MX	Q=MX	D=MX	Q=MX	D=MX	Q=MX	D=MX
5	A+B+CI, BCD	NOP	D=MX	SQ	SQ	NOP	D=MX	SQ	SQ	NOP	D=MX	NOP	D=MX	NOP	D=MX	NOP	D=MX	NOP	D=MX	SQ	RESVRD	NOP	D=MX	SQ	SQ	NOP	D=MX	NOP	D=MX	NOP	D=MX	NOP	D=MX
		Q=MX	D=MX	SQR	SQR	Q=MX	D=MX	SQR	SQR	Q=MX	D=MX	Q=MX	D=MX	Q=MX	D=MX	Q=MX	D=MX	Q=MX	D=MX	SQR	RESVRD	Q=MX	D=MX	SQR	SQR	Q=MX	D=MX	Q=MX	D=MX	Q=MX	D=MX	Q=MX	D=MX
6	(A+B+CI).SR	NOP	D=MX	SQ	SQ	NOP	D=MX	SQ	SQ	NOP	D=MX	NOP	D=MX	NOP	D=MX	NOP	D=MX	NOP	D=MX	SQ	RESVRD	NOP	D=MX	SQ	SQ	NOP	D=MX	NOP	D=MX	NOP	D=MX	NOP	D=MX
		Q=MX	D=MX	SQR	SQR	Q=MX	D=MX	SQR	SQR	Q=MX	D=MX	Q=MX	D=MX	Q=MX	D=MX	Q=MX	D=MX	Q=MX	D=MX	SQR	RESVRD	Q=MX	D=MX	SQR	SQR	Q=MX	D=MX	Q=MX	D=MX	Q=MX	D=MX	Q=MX	D=MX
7	(A+B+CI).SL	NOP	D=MX	SQ	SQ	NOP	D=MX	SQ	SQ	NOP	D=MX	NOP	D=MX	NOP	D=MX	NOP	D=MX	NOP	D=MX	SQ	RESVRD	NOP	D=MX	SQ	SQ	NOP	D=MX	NOP	D=MX	NOP	D=MX	NOP	D=MX
		Q=MX	D=MX	SQR	SQR	Q=MX	D=MX	SQR	SQR	Q=MX	D=MX	Q=MX	D=MX	Q=MX	D=MX	Q=MX	D=MX	Q=MX	D=MX	SQR	RESVRD	Q=MX	D=MX	SQR	SQR	Q=MX	D=MX	Q=MX	D=MX	Q=MX	D=MX	Q=MX	D=MX
8	A.AND.B	NOP	D=MX	SQ	SQ	NOP	D=MX	SQ	SQ	NOP	D=MX	NOP	D=MX	NOP	D=MX	NOP	D=MX	NOP	D=MX	SQ	RESVRD	NOP	D=MX	SQ	SQ	NOP	D=MX	NOP	D=MX	NOP	D=MX	NOP	D=MX
		Q=MX	D=MX	SQR	SQR	Q=MX	D=MX	SQR	SQR	Q=MX	D=MX	Q=MX	D=MX	Q=MX	D=MX	Q=MX	D=MX	Q=MX	D=MX	SQR	RESVRD	Q=MX	D=MX	SQR	SQR	Q=MX	D=MX	Q=MX	D=MX	Q=MX	D=MX	Q=MX	D=MX
9	A.OR.B	NOP	D=MX	SQ	SQ	NOP	D=MX	SQ	SQ	NOP	D=MX	NOP	D=MX	NOP	D=MX	NOP	D=MX	NOP	D=MX	SQ	RESVRD	NOP	D=MX	SQ	SQ	NOP	D=MX	NOP	D=MX	NOP	D=MX	NOP	D=MX
		Q=MX	D=MX	SQR	SQR	Q=MX	D=MX	SQR	SQR	Q=MX	D=MX	Q=MX	D=MX	Q=MX	D=MX	Q=MX	D=MX	Q=MX	D=MX	SQR	RESVRD	Q=MX	D=MX	SQR	SQR	Q=MX	D=MX	Q=MX	D=MX	Q=MX	D=MX	Q=MX	D=MX
A	(A.AND.B).SR	NOP	D=MX	SQ	SQ	NOP	D=MX	SQ	SQ	NOP	D=MX	NOP	D=MX	NOP	D=MX	NOP	D=MX	NOP	D=MX	SQ	RESVRD	NOP	D=MX	SQ	SQ	NOP	D=MX	NOP	D=MX	NOP	D=MX	NOP	D=MX
		Q=MX	D=MX	SQR	SQR	Q=MX	D=MX	SQR	SQR	Q=MX	D=MX	Q=MX	D=MX	Q=MX	D=MX	Q=MX	D=MX	Q=MX	D=MX	SQR	RESVRD	Q=MX	D=MX	SQR	SQR	Q=MX	D=MX	Q=MX	D=MX	Q=MX	D=MX	Q=MX	D=MX
B	(A.AND.B).SL	NOP	D=MX	SQ	SQ	NOP	D=MX	SQ	SQ	NOP	D=MX	NOP	D=MX	NOP	D=MX	NOP	D=MX	NOP	D=MX	SQ	RESVRD	NOP	D=MX	SQ	SQ	NOP	D=MX	NOP	D=MX	NOP	D=MX	NOP	D=MX
		Q=MX	D=MX	SQR	SQR	Q=MX	D=MX	SQR	SQR	Q=MX	D=MX	Q=MX	D=MX	Q=MX	D=MX	Q=MX	D=MX	Q=MX	D=MX	SQR	RESVRD	Q=MX	D=MX	SQR	SQR	Q=MX	D=MX	Q=MX	D=MX	Q=MX	D=MX	Q=MX	D=MX
C	B-A-CI	NOP	D=MX	SQ	SQ	NOP	D=MX	SQ	SQ	NOP	D=MX	NOP	D=MX	NOP	D=MX	NOP	D=MX	NOP	D=MX	SQ	RESVRD	NOP	D=MX	SQ	SQ	NOP	D=MX	NOP	D=MX	NOP	D=MX	NOP	D=MX
		Q=MX	D=MX	SQR	SQR	Q=MX	D=MX	SQR	SQR	Q=MX	D=MX	Q=MX	D=MX	Q=MX	D=MX	Q=MX	D=MX	Q=MX	D=MX	SQR	RESVRD	Q=MX	D=MX	SQR	SQR	Q=MX	D=MX	Q=MX	D=MX	Q=MX	D=MX	Q=MX	D=MX
D	A.XOR.B	NOP	D=MX	SQ	SQ	NOP	D=MX	SQ	SQ	NOP	D=MX	NOP	D=MX	NOP	D=MX	NOP	D=MX	NOP	D=MX	SQ	RESVRD	NOP	D=MX	SQ	SQ	NOP	D=MX	NOP	D=MX	NOP	D=MX	NOP	D=MX
		Q=MX	D=MX	SQR	SQR	Q=MX	D=MX	SQR	SQR	Q=MX	D=MX	Q=MX	D=MX	Q=MX	D=MX	Q=MX	D=MX	Q=MX	D=MX	SQR	RESVRD	Q=MX	D=MX	SQR	SQR	Q=MX	D=MX	Q=MX	D=MX	Q=MX	D=MX	Q=MX	D=MX
E	A.AND.(.NOT.B)	NOP	D=MX	SQ	SQ	NOP	D=MX	SQ	SQ	NOP	D=MX	NOP	D=MX	NOP	D=MX	NOP	D=MX	NOP	D=MX	SQ	RESVRD	NOP	D=MX	SQ	SQ	NOP	D=MX	NOP	D=MX	NOP	D=MX	NOP	D=MX
		Q=MX	D=MX	SQR	SQR	Q=MX	D=MX	SQR	SQR	Q=MX	D=MX	Q=MX	D=MX	Q=MX	D=MX	Q=MX	D=MX	Q=MX	D=MX	SQR	RESVRD	Q=MX	D=MX	SQR	SQR	Q=MX	D=MX	Q=MX	D=MX	Q=MX	D=MX	Q=MX	D=MX
F	(.NOT.A).AND.B	NOP	D=MX	SQ	SQ	NOP	D=MX	SQ	SQ	NOP	D=MX	NOP	D=MX	NOP	D=MX	NOP	D=MX	NOP	D=MX	SQ	RESVRD	NOP	D=MX	SQ	SQ	NOP	D=MX	NOP	D=MX	NOP	D=MX	NOP	D=MX
		Q=MX	D=MX	SQR	SQR	Q=MX	D=MX	SQR	SQR	Q=MX	D=MX	Q=MX	D=MX	Q=MX	D=MX	Q=MX	D=MX	Q=MX	D=MX	SQR	RESVRD	Q=MX	D=MX	SQR	SQR	Q=MX	D=MX	Q=MX	D=MX	Q=MX	D=MX	Q=MX	D=MX

A#AMUX
B#BMUX
C#CARRY IN
D#D REGISTER
DSL#D SHIFTED LEFT
DSR#D SHIFTED RIGHT
M#M BUS
Q#Q REGISTER
R#R BUS
RESVRD#RESERVED
SQL#SHIFT Q LEFT
SOR#SHIFT Q RIGHT
S#SUPER ROTATOR
WB#WBUS
WX#WMUX
XM#SIGN/ZERO EXTENDED MBUS

OUTPUT
DISABLE

SPECIAL
OPERATION

DQ
MICRO
ORDER

0 2
1 3

TITLE#

ALPCTL FUNCTION CHART

SIZE#CODE

D BD

NUMBER

LCCC2-0-27

REV.

B

FNG.

SHEET

OF

DIST.

1

2

3

4

5

6

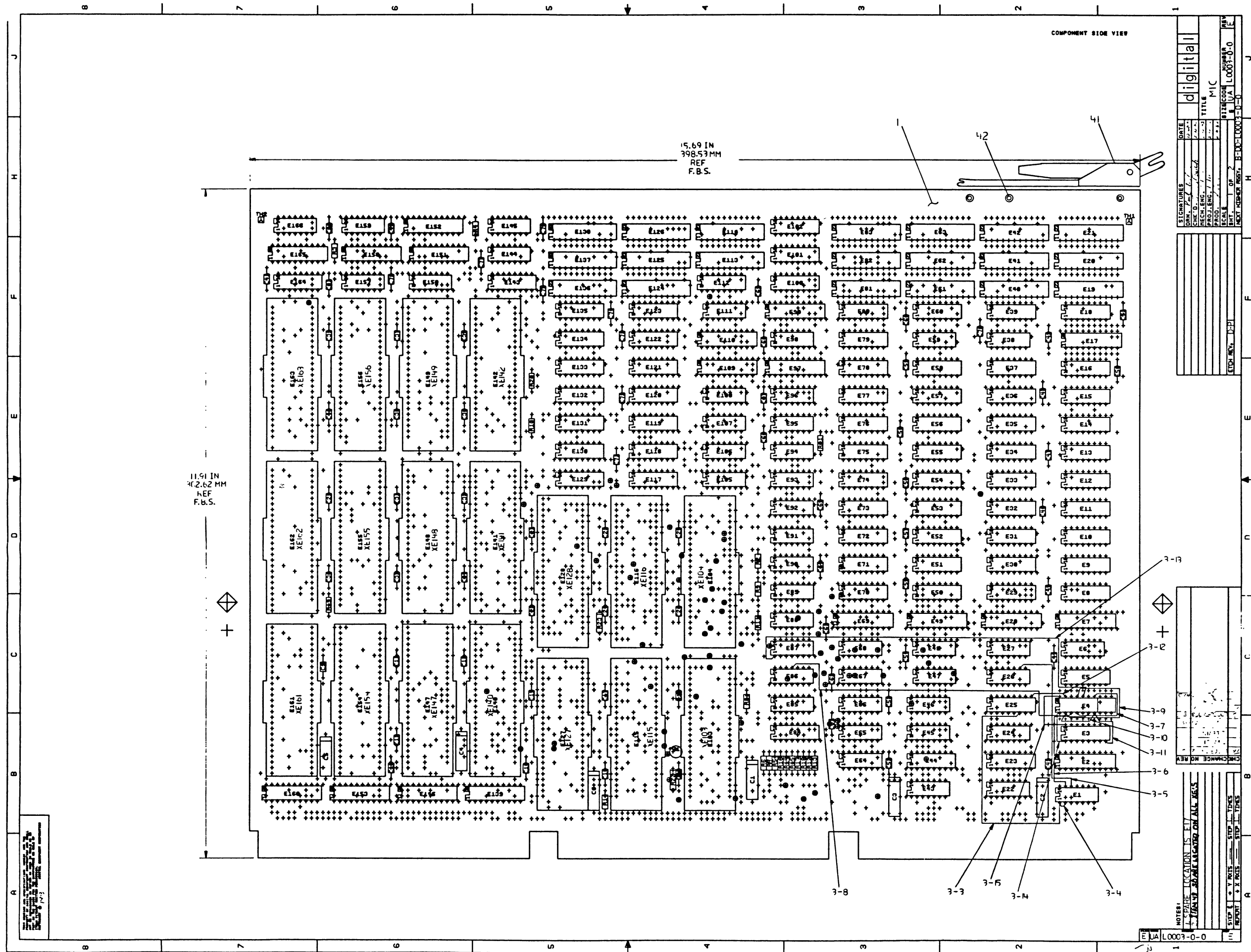
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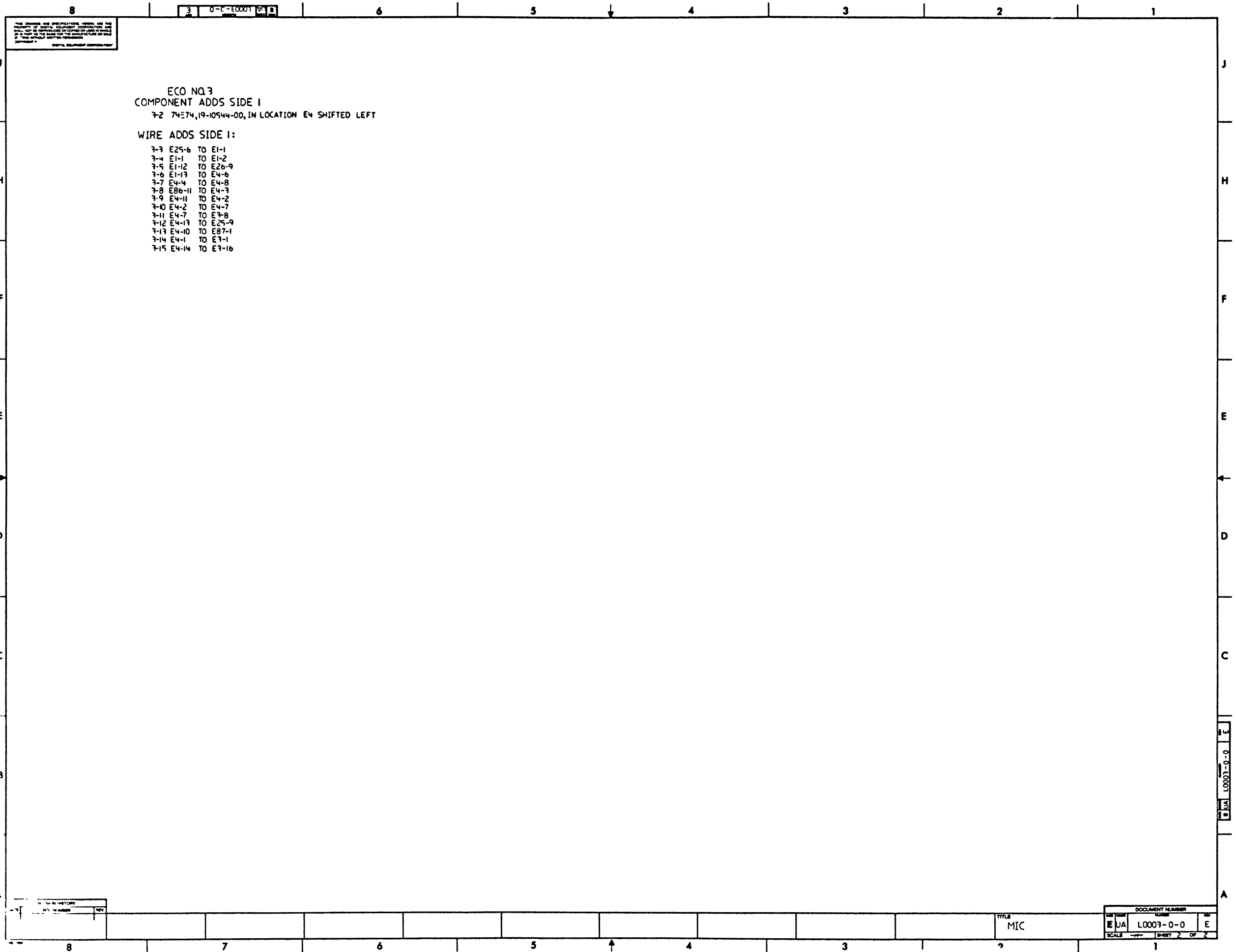
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[illegible]





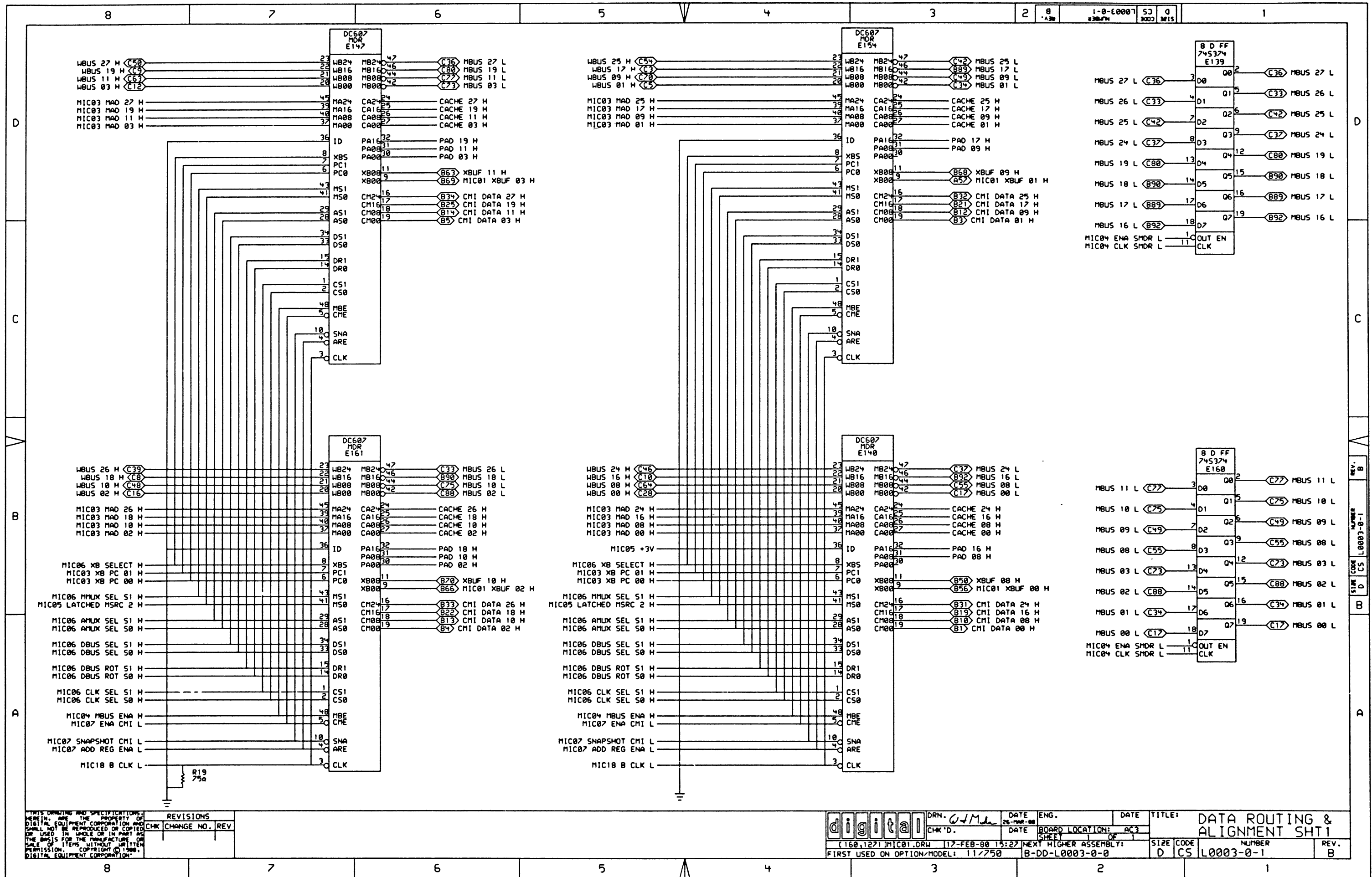
LINE	ITEM	DOCUMENT NUMBER	PART NUMBER	DESCRIPTION	QTY PER VARIATION	REFERENCE DESIGNATOR
					00	
1	1	E-MD-5013693-0-0	5013693-00	DRILL & ETCH DRAWING	1	
2	2		1012784-00	.047 MFD 50V +80-20% CER	69	C9-C23,C25,C26,C29-C41,C43-C81
3	3	SEE NOTES	1012084-01	8 MFD 25V +75-10% AL EL	6	C1-C6
4	4		1313580-00	360.0 .25 W 5.0 % CC	18	R1-R18
5	5		1910532-00	74500 NAND GATE-QUAD 2IN	1	E65
6	6		1910534-00	74504 INVERTER GATE-HEX 1I	2	E85,E87
7	7		1910536-00	74510 NAND GATE-TRIPLE 3IN	1	E91
8	8		1910542-00	74564 A-0-1 GATE 4-2-3-2	3	E24,E47,E89
9	9		1910547-00	745153 MUX 1 OF 4 (DUAL)	3	E100-E102
10	10		1910549-00	745158 MUX 1 OF 2 (QUAD)	2	E5,E79
11	11		1910541-00	74540 NAND GATE-DUAL 4IN,8	1	E38
12	12		1910957-00	745175 FF-D QUAD COMMON CLO	3	E3,E23,E25
13	13		1911573-00	745280 PARITY GEN/CHKR,9BIT	17	E6,E27,E48,E59,E68,E98, CONT CONT E105-E108,E111,E144,E145,E150, E157,E159,E166 E18,E39,E60,E80 E26,E92 E112,E143 E96 E164 E88 E90 E94,E95 E22,E93 E7,E28,E49,E69,E97 E8-E16,E29-E37,E50-E58,E70-E78, CONT E117-E123,E129-E135 E2,E139,E146,E153,E160 E44,E84 E109,E110,E151,E152,E158,E165
14	14		1911641-00	SN 745257 MUX,QUAD 2 TO 1	4	
15	15		1911712-00	74551 AND-OR GATE-INVERT D	2	
16	16		1916310-00	RAM,256X1 TRI STATE	2	
17	17		1912096-00	DEC 74586 XOR GATE,QUAD 2IN	1	
18	18		1912097-00	SN 745182 LOOK AND CARRY GEN	1	
19	19		1912388-00	74502 NOR GATE-QUAD 2IN,PO	1	
20	20		1912389-00	74508 AND GATE-QUAD 2IN,PO	1	
21	21		1912746-00	DEC 74537 NAND GATE-QUAD 2IN	2	
22	22		1913340-00	74532 OR GATE-QUAD 2IN	2	
23	23		1913493-00	745241 OCTAL BUFFER,TRI-STA	5	
24	24		1913662-00	RAM 1KX1 16 PIN TT	50	
25	25		1913671-00	745374 FF-D OCTAL TRISTATE	5	
26	26		1913839-00	74LS165 SHIFT REG. 8BIT	2	
27	27		1913888-00	DC 102A EQUALS CHECKER 8BIT	6	

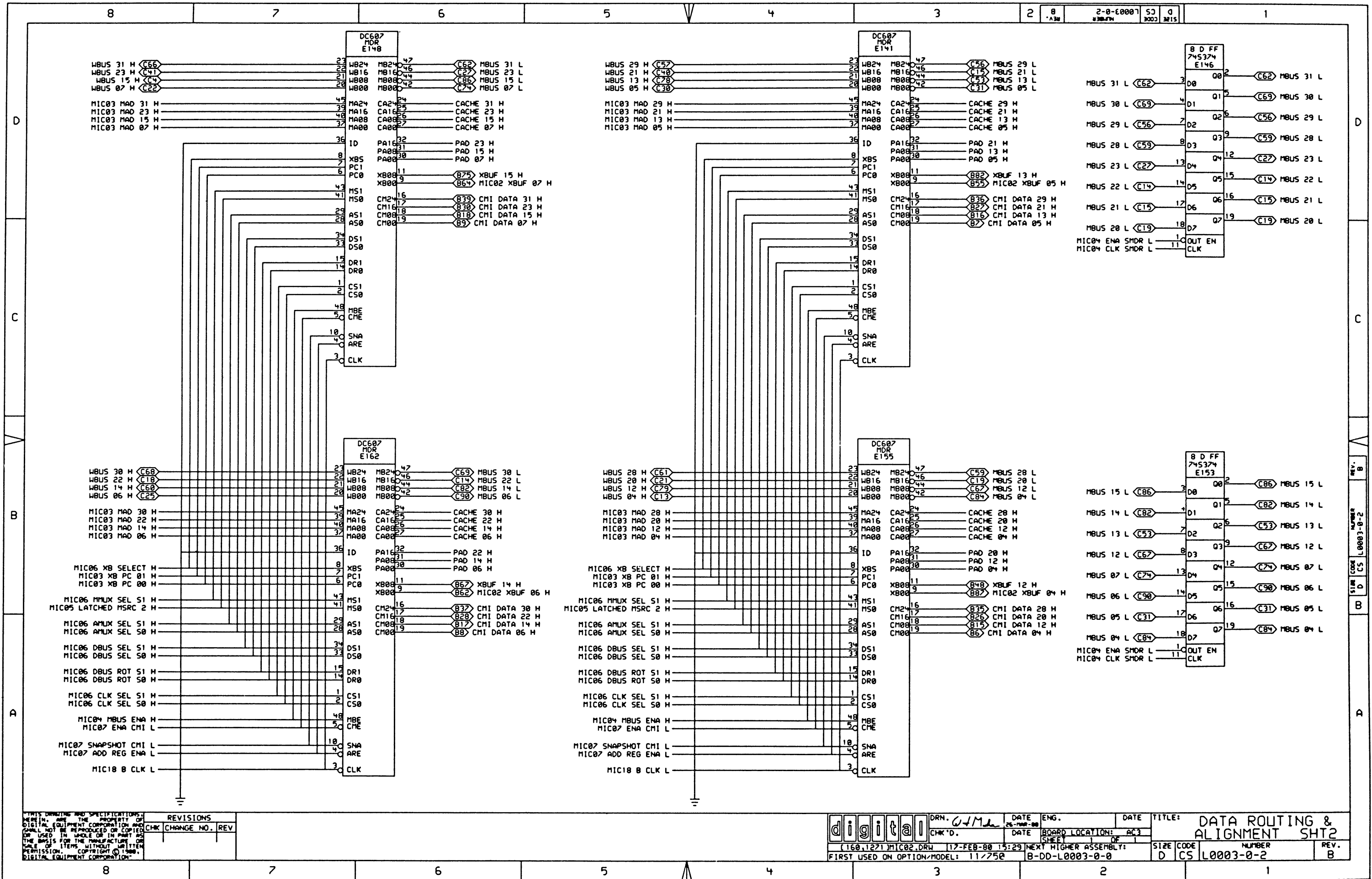
REVISION HISTORY		BASIC PART NO: L0003		DRN: D.SIREEN		DATE: 31-MAY-79		D I G I T A L	
ENG:	ECO NUMBER	REV	SECTION A OF A	CHK'D: F.CAROFALO		DATE: 31-MAY-79		TITLE PARTS LIST	
---	INITIAL	B	SECTION VARIATION INDEX	DES.ENG: P.BINDER		DATE: 31-MAY-79		MIC	
D.L	L0003-TW001	C	[A] 00	RESP.ENG.: P.BINDER		DATE: 31-MAY-79		DOCUMENT NUMBER	
			[B]	MFG.ENG.: VANCE PARKER		DATE: 8-FEB-80		K	PL L0003-0-DBP
			[C]	ASSEMBLY NUMBER:		TOP DOCUMENT NUMBER:			FILE NAME: Z1258.PLS
			[D]	E-UA-L0003-0-0		B-3D-L0003-0-0			EDIT # 11
			[E]						
			[F]						
			[G]						
			[H]						
			[I]						
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LINE	ITEM	DOCUMENT NUMBER	PART NUMBER	DESCRIPTION	QTY	PER VARIATION	REFERENCE DESIGNATOR
					00		
28	28		1914085-00	74S260 NOR GATE-DUAL,POS	3		E45,E67,E86
29	29		1914086-00	74S30 NAND GATE-POS,8IN	1		E64
30	30		1914681-00	DC 6078 BIPOLAR,LS,400-GATE	8	CONT	E140,E141,E147,E148,E154,E155, E161,E162
31	31		1914683-00	DC 609E BIPOLAR,LS,400-GATE	4		E142,E149,E156,E163
32	32		1914697-00	DC 623C BIPOLAR,LS,400-GATE	1		E103
33	33		1914698-00	DC 624E BIPOLAR,LS,400-GATE	1		E128
34	34		1914699-00	DC 625B BIPOLAR,LS,400-GATE	1		E127
35	35		1914700-00	DC 626B BIPOLAR,LS,400-GATE	1		E116
36	36		1914701-00	DC 627B BIPOLAR,LS,400-GATE	1		E104
37	37		1914702-00	DC 628B BIPOLAR,LS,400-GATE	1		E115
38	38		1915193-00	LS244 DRIVER,LINE OCTAL,T	1		E99
39	39		1915697-00	RAM 256X4 TRI-STATE	20	CONT	E19-E21,E40-E42,E61-E63,E81-E83, E113,E114,E124-E126,E136-E138 E66
40	40		1910537-00	74S11 AND GATE-TRIPLE 3INP	1		
41	41		1210711-02	/REPLACED BY 12-16988-02	1		
42	42		9000024-01	EYELET, ROLLED FLANGE .121 OD X	12		
43	43		1503121-00	2N 2369 NPN 350MW S1 N	1		Q1
44	44		1302379-00	75.0 .25 W 5.0 % CC	4		R19-R21,R23
45	45		9009185-00	JUMPER, WIRE, INSULATED, BLACK B	1		R22
46	46		1910544-00	74S74 FF-D DUAL EDGE TRIGG	1		E46
47	47		1910878-00	7427 NOR GATE-TRIPLE 3IN	2		E43,E1
48	48		1215924-00	SOCKET IC W/METAL CONT	18	CONT	XE103,XE104,XE115,XE116,XE127, XE128,XE140-XE142,XE147-XE149, XE154-XE156,XE161-XE163
49	49		1215935-00	GASKET, THERMAL .50"X.80"	18		
50	50		1215936-00	HEAT SINK, FORCED CONVECTION	18		

51 NOTE: SPARE I.C. LOCATIONS ARE E4,E17
52 NOTE: SOME MODULES WILL HAVE 10-05305 INSTEAD OF 10-12084-01

D	I	G	I	T	A	L	TITLE	SECTION A	OF A	SIZE	CODE	DOCUMENT NUMBER	REV
							MIC			K	PL	L0003-0-DBP	C

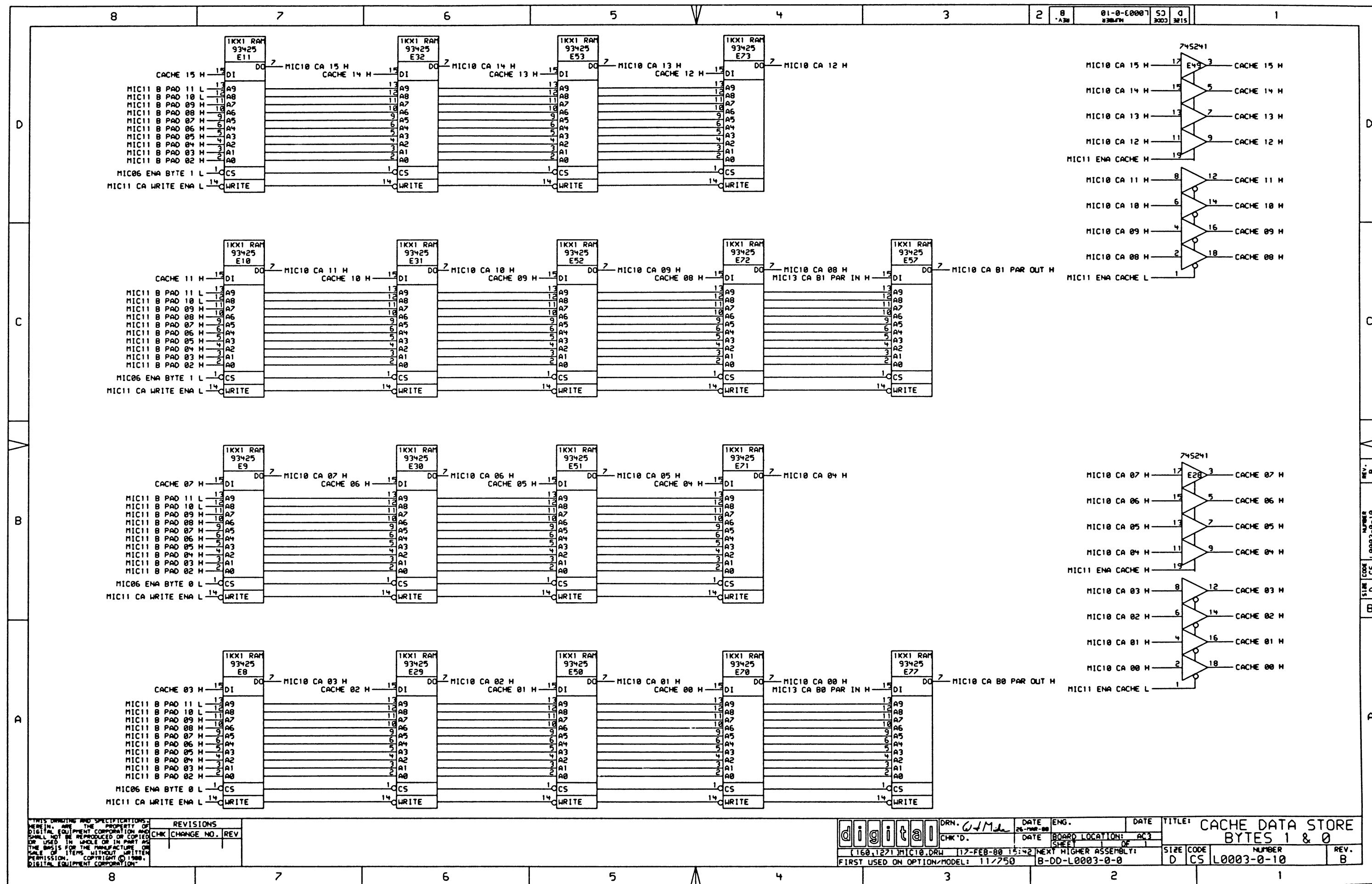




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REVISIONS		
CHK	CHANGE NO.	REV

digital	DRN. <i>W+M</i>	DATE 26-MAR-80	ENG.	DATE	TITLE: DATA ROUTING & ALIGNMENT SHT2
	CHK'D.	DATE 17-FEB-80	BOARD LOCATION: AC3	SHEET 1 OF 1	SIZE CODE D CS
FIRST USED ON OPTION/MODEL: 11/750			NEXT HIGHER ASSEMBLY: B-DD-L0003-0-0		NUMBER L0003-0-2
					REV. B



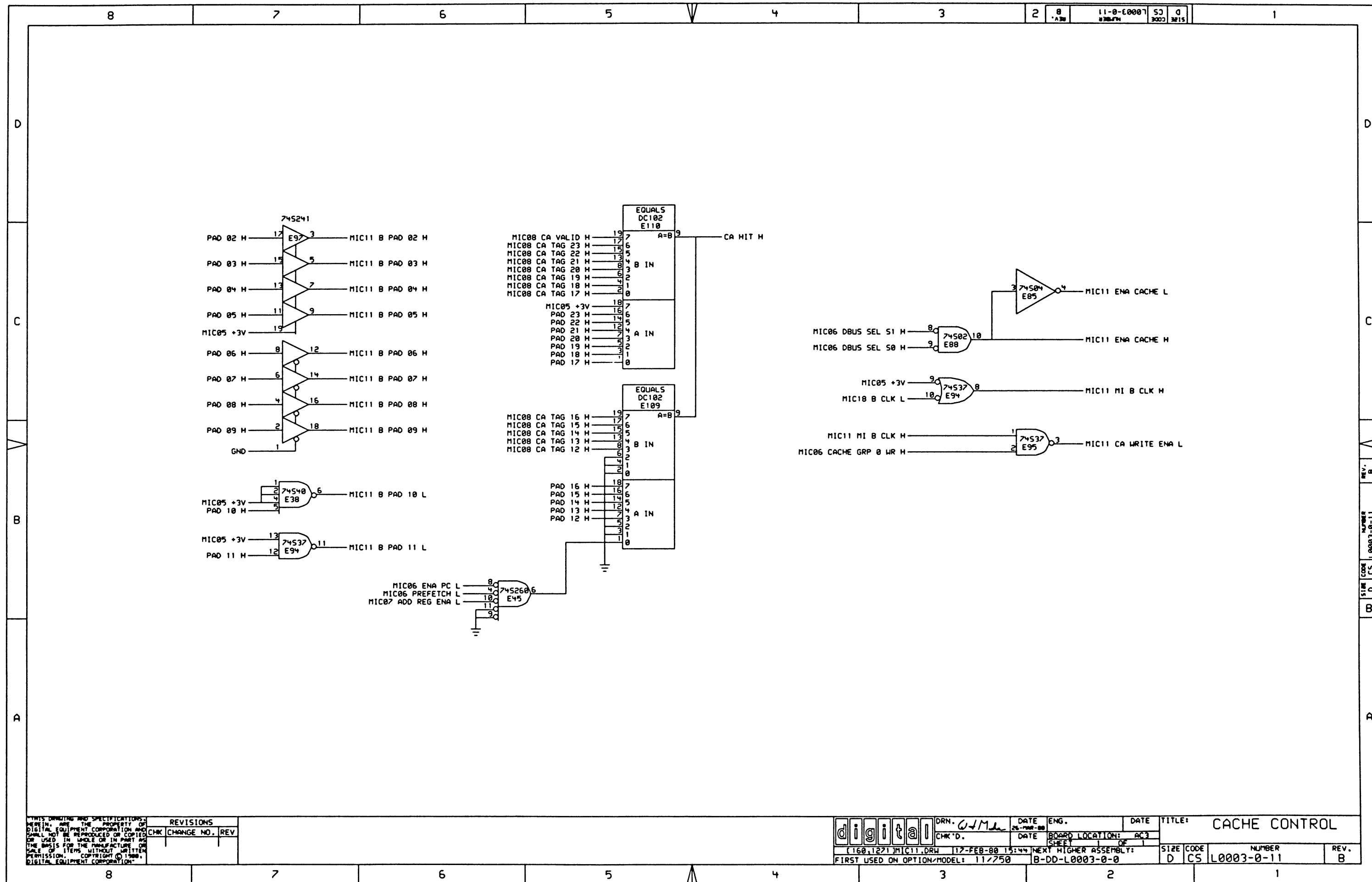
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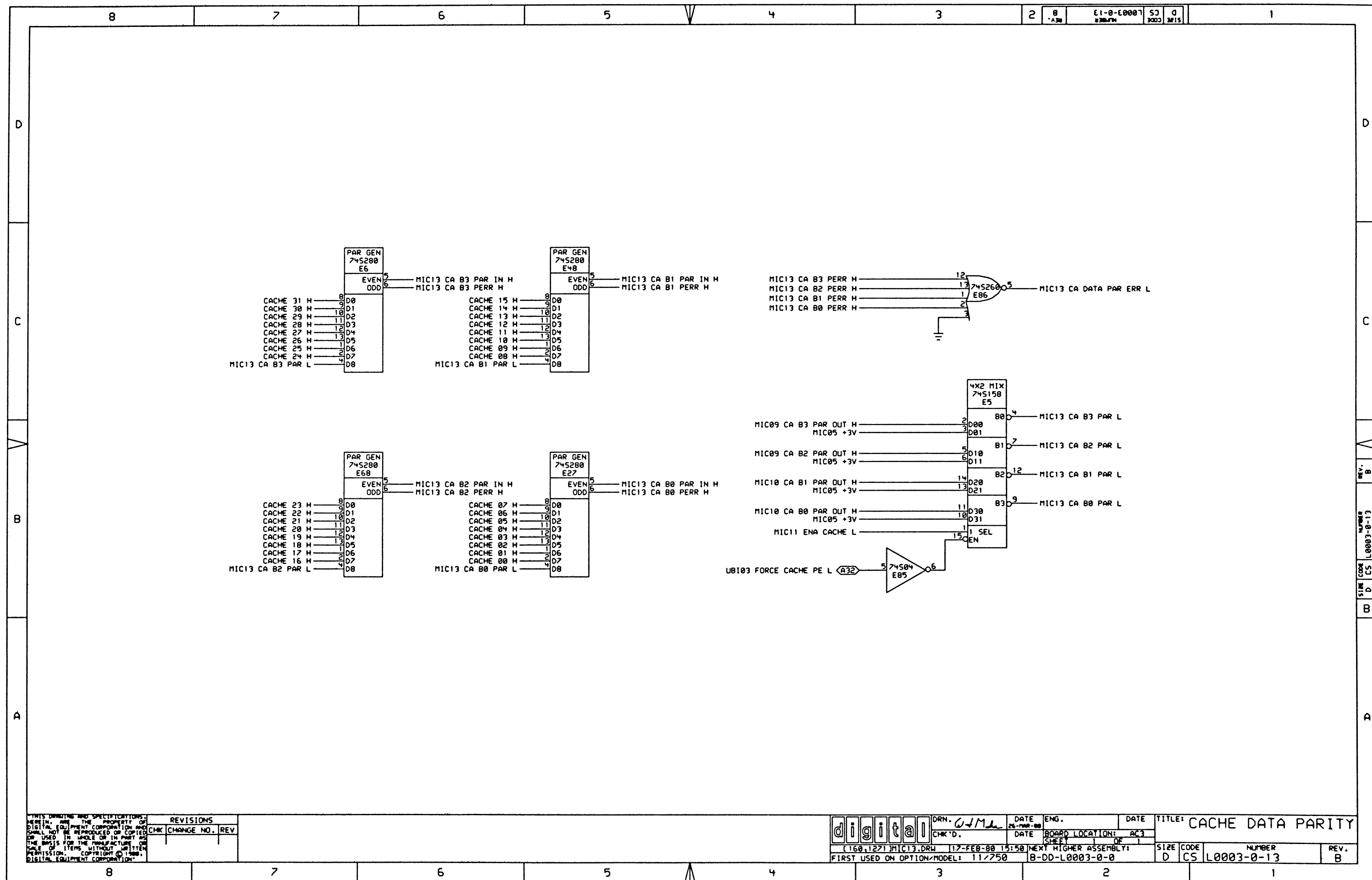
REVISIONS		
CHK	CHANGE NO.	REV

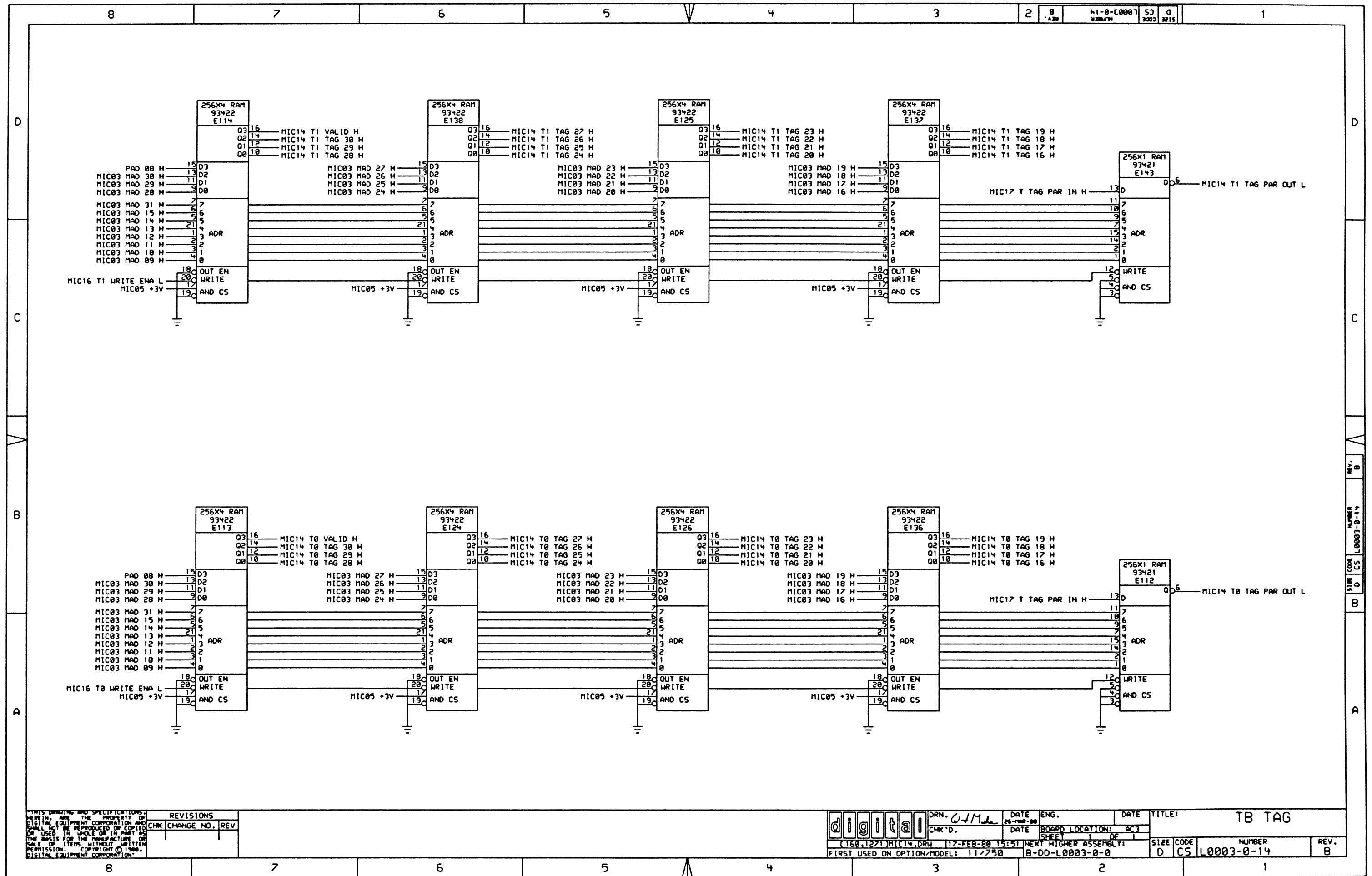
digital
DRN. W+M
CHK'D.
(160,127) MIC10.DRW
FIRST USED ON OPTION/MODEL: 11/750

DATE	ENG.	DATE	BOARD LOCATION
25-MAR-80			AC3

TITLE: CACHE DATA STORE BYTES 1 & 0			
SIZE	CODE	NUMBER	REV.
D	CS	L0003-0-10	B







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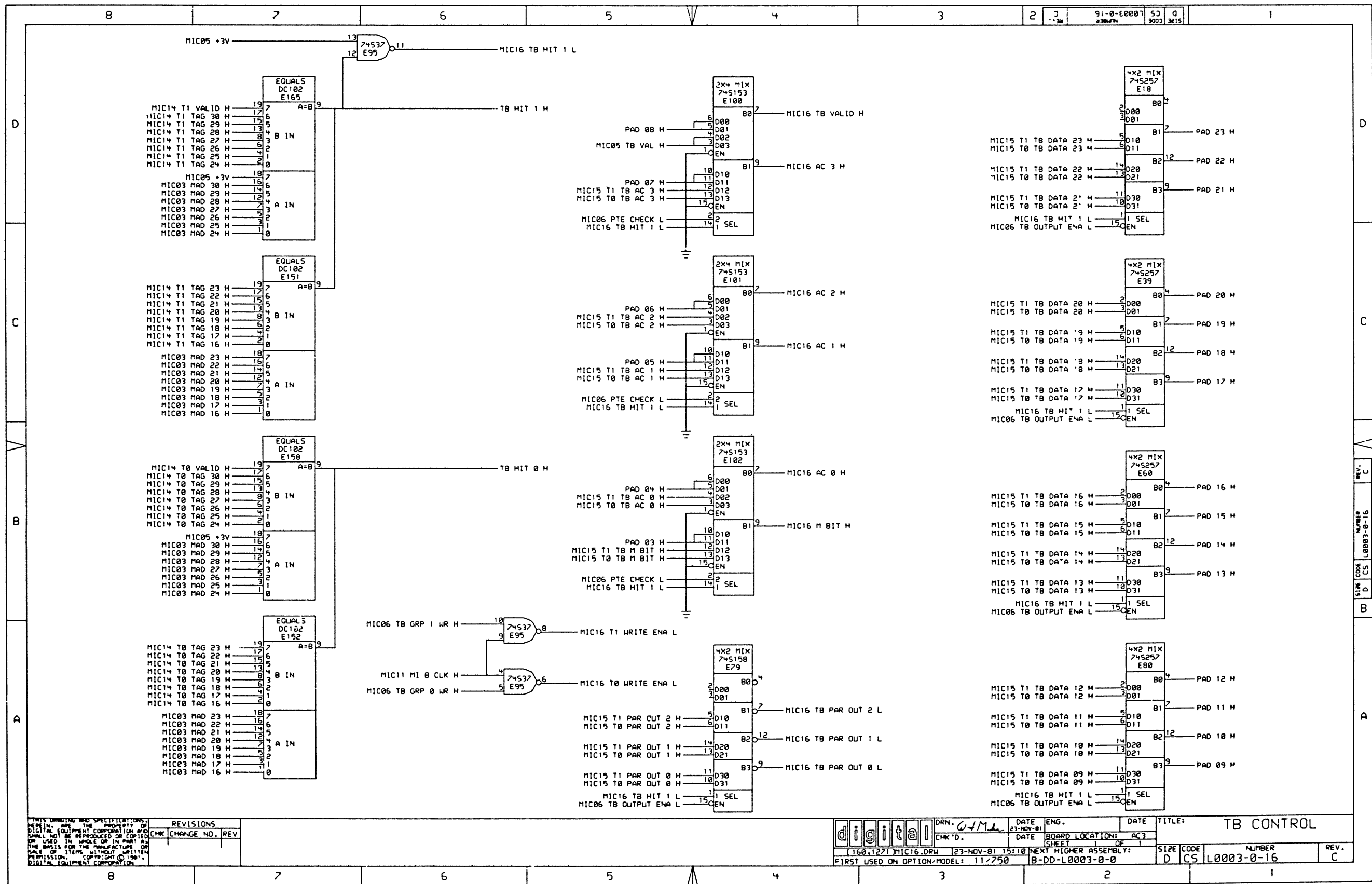
REVISIONS		
CHK	CHANGE NO.	REV

digital

DRN. *WIM*
CHK'D.
DATE 26-MAR-88
SHEET 1 OF 1

DATE 26-MAR-88
BOARD LOCATION: AC3
NEXT HIGHER ASSEMBLY:
B-DD-L0003-0-0

TITLE: TB TAG		SIZE	CODE	NUMBER	REV.
D	CS	L0003-0-14		B	



8	7	6	5	4	3	2	1
D		C		B		A	
SIGNAL NAME		PAGE NUMBER(S)		SIGNAL NAME		PAGE NUMBER(S)	
CA HIT M		07,06,11		CMI DATA 10 M		01	
CACHE 00 M		13,10,01		CMI DATA 11 M		01	
CACHE 01 M		13,10,01		CMI DATA 12 M		02	
CACHE 02 M		13,10,01		CMI DATA 13 M		02	
CACHE 03 M		10,13,01		CMI DATA 14 M		02	
CACHE 04 M		13,10,02		CMI DATA 15 M		02	
CACHE 05 M		13,10,02		CMI DATA 16 M		01	
CACHE 06 M		13,10,02		CMI DATA 17 M		01	
CACHE 07 M		10,13,02		CMI DATA 18 M		01	
CACHE 08 M		13,10,01		CMI DATA 19 M		01	
CACHE 09 M		13,10,01		CMI DATA 20 M		02	
CACHE 10 M		10,13,01		CMI DATA 21 M		02	
CACHE 11 M		10,13,01		CMI DATA 22 M		02	
CACHE 12 M		13,10,02		CMI DATA 23 M		02	
CACHE 13 M		13,10,02		CMI DATA 24 M		01	
CACHE 14 M		10,13,02		CMI DATA 25 M		07,01	
CACHE 15 M		10,13,02		CMI DATA 26 M		07,01	
CACHE 16 M		13,09,01		CMI DATA 27 M		07,01	
CACHE 17 M		09,13,01		CMI DATA 28 M		07,02	
CACHE 18 M		09,13,01		CMI DATA 29 M		07,02	
CACHE 19 M		09,13,01		CMI DATA 30 M		07,02	
CACHE 20 M		13,09,02		CMI DATA 31 M		07,02	
CACHE 21 M		09,13,02		CMI DBBZ L		07	
CACHE 22 M		09,13,02		CMI HOLD L		07	
CACHE 23 M		09,13,02		CMI STATUS 00 L		07	
CACHE 24 M		13,09,01		CMI STATUS 01 L		07	
CACHE 25 M		13,09,01		CMI WAIT L		04	
CACHE 26 M		13,09,01		CS BUS 0 M		05	
CACHE 27 M		13,09,01		CS BUS 1 M		05	
CACHE 28 M		13,09,02		CS BUS 2 M		05	
CACHE 29 M		13,09,02		CS BUS 3 M		05	
CACHE 30 M		13,09,02		CS BUS 4 M		05,07,06	
CACHE 31 M		13,09,02		CS MSRC 0 M		05	
CMI ARB 1 L		04		CS MSRC 1 M		05	
CMI ARB 2 L		04		CS MSRC 2 M		05	
CMI ARB 3 L		04		CS MSRC 3 M		05	
CMI ARB 4 L		04		CS MSRC 4 M		05	
CMI ARB 5 L		04		CS MCTRL 0 M		05	
CMI ARB 6 L		04		CS MCTRL 1 M		05	
CMI ARB 7 L		04		CS MCTRL 2 M		05	
CMI DATA 00 M		01		CS MCTRL 3 M		05	
CMI DATA 01 M		01		CS MCTRL 4 M		05	
CMI DATA 02 M		01		CS MCTRL 5 M		05	
CMI DATA 03 M		01		DPM11 MCS TNP L		04	
CMI DATA 04 M		02		DPM14 LD OSR L		18	
CMI DATA 05 M		02		DPM16 IRDI M		04,06	
CMI DATA 06 M		02		DPM17 B CLK L		18	
CMI DATA 07 M		02		DPM17 D CLK ENABLE M		07,06	
CMI DATA 08 M		01		DPM17 DO SRVC L		07	
CMI DATA 09 M		01		DPM17 INSTR FETCH M		04,18	
SIGNAL NAME		PAGE NUMBER(S)		SIGNAL NAME		PAGE NUMBER(S)	
DPM17 M CLK ENABLE M		18,04,05,07,06		DPM17 PHASE 1 M		06,04,07	
DPM17 PSL CM M		18,06		DPM18 DST RMODE M		07,06	
DPM18 D SIZE 0 M		07,06		DPM19 D SIZE 1 M		07,06	
DPM19 D SIZE 0 L		04,06		DPM19 ISIZE 0 L		04,06	
DPM19 ISIZE 1 L		04,06		DPM20 CS PARITY ERROR M		07	
DPM22 V OUT M		04		FPA21 FP RES OP L		07	
MBUS 00 L		01		MBUS 01 L		01	
MBUS 02 L		01		MBUS 03 L		01	
MBUS 04 L		02		MBUS 05 L		02	
MBUS 06 L		02		MBUS 07 L		02	
MBUS 08 L		01		MBUS 09 L		01	
MBUS 10 L		01		MBUS 11 L		01	
MBUS 12 L		02		MBUS 13 L		02	
MBUS 14 L		02		MBUS 15 L		04,02	
MBUS 16 L		01		MBUS 17 L		01	
MBUS 18 L		01		MBUS 19 L		01	
MBUS 20 L		02		MBUS 21 L		02	
MBUS 22 L		02		MBUS 23 L		02	
MBUS 24 L		01		MBUS 25 L		01	
MBUS 26 L		01		MBUS 27 L		01	
MBUS 28 L		02		MBUS 29 L		02	
MBUS 30 L		02		MBUS 31 L		02	
MIC01 XBUF 00 M		01		MIC01 XBUF 01 M		01	
MIC01 XBUF 02 M		01		MIC01 XBUF 03 M		01	
MIC02 XBUF 04 M		02		MIC02 XBUF 05 M		02	
MIC02 XBUF 06 M		02		MIC02 XBUF 07 M		02	

NOTES:
1. THIS PAGE LISTS THE SCHEMATIC PAGE NUMBER(S) WHERE A SIGNAL NAME IS REFERENCED.

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REVISIONS	
CHK	CHANGE NO. REV

digital

DRN. 0111	DATE ENG. 06-NOV-80	DATE	TITLE: MIC FORWARD REFERENCE
CHK'D.	DATE BOARD LOCATION: AC3	SHEET	
(160,1271)MIC19.DRW	06-NOV-80 10:24	NEW HIGHER ASSEMBLY:	SIZE CODE NUMBER REV.
FIRST USED ON OPTION/MODEL: 11/750	B-DD-L0003-0-0		D CS L0003-0-19 C

8	7	6	5	4	3	2	8 REV. A30	12-0-00007 NUMBER	53 CODE	0 SIZE	1																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																								
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H</td><td>11,12,08</td><td></td><td>MIC11</td><td>CA WRITE ENA L</td><td>10,09,11,08</td><td></td><td>MIC14</td><td>T1 TAG 30 H</td><td>14,16,17</td><td></td></tr><tr><td>MIC08</td><td>CA TAG 23 H</td><td>11,12,08</td><td></td><td>MIC11</td><td>ENA CACHE H</td><td>09,10,11</td><td></td><td>MIC14</td><td>T1 TAG PAR OUT L</td><td>14,17</td><td></td></tr><tr><td>MIC08</td><td>CA TAG PAR OUT H</td><td>12,08</td><td></td><td>MIC11</td><td>ENA CACHE L</td><td>13,09,10,11</td><td></td><td>MIC14</td><td>T1 VALID H</td><td>14,16,17</td><td></td></tr><tr><td>MIC08</td><td>CA VALID H</td><td>11,12,08</td><td></td><td>MIC11</td><td>MI B CLK H</td><td>11,16</td><td></td><td>MIC15</td><td>T0 PAR OUT 0 H</td><td>15,16</td><td></td></tr><tr><td>MIC09</td><td>CA 16 H</td><td>09</td><td></td><td>MIC12</td><td>CA TAG PAR ERR H</td><td>12,06</td><td></td><td>MIC15</td><td>T0 PAR OUT 1 H</td><td>15,16</td><td></td></tr><tr><td>MIC09</td><td>CA 17 H</td><td>09</td><td></td><td>MIC12</td><td>CA TAG PAR IN H</td><td>12,08</td><td></td><td>MIC15</td><td>T0 PAR OUT 2 H</td><td>15,16</td><td></td></tr><tr><td colspan="4"></td><td colspan="4"></td><td colspan="4"></td><td></td></tr><tr><td>MIC09</td><td>CA 18 H</td><td>09</td><td></td><td>MIC13</td><td>CA B0 PAR IN H</td><td>13,10</td><td></td><td>MIC15</td><td>T0 TB AC 0 H</td><td>15,16</td><td></td></tr><tr><td>MIC09</td><td>CA 19 H</td><td>09</td><td></td><td>MIC13</td><td>CA B0 PAR L</td><td>13</td><td></td><td>MIC15</td><td>T0 TB AC 1 H</td><td>15,16</td><td></td></tr><tr><td>MIC09</td><td>CA 20 H</td><td>09</td><td></td><td>MIC13</td><td>CA B0 PERR H</td><td>13</td><td></td><td>MIC15</td><td>T0 TB AC 2 H</td><td>15,16</td><td></td></tr><tr><td>MIC09</td><td>CA 21 H</td><td>09</td><td></td><td>MIC13</td><td>CA B1 PAR IN H</td><td>13,10</td><td></td><td>MIC15</td><td>T0 TB AC 3 H</td><td>15,16</td><td></td></tr><tr><td>MIC09</td><td>CA 22 H</td><td>09</td><td></td><td>MIC13</td><td>CA B1 PAR L</td><td>13</td><td></td><td>MIC15</td><td>T0 TB DATA 09 H</td><td>16,15</td><td></td></tr><tr><td>MIC09</td><td>CA 23 H</td><td>09</td><td></td><td>MIC13</td><td>CA B1 PERR H</td><td>13</td><td></td><td>MIC15</td><td>T0 TB DATA 10 H</td><td>16,15</td><td></td></tr><tr><td>MIC09</td><td>CA 24 H</td><td>09</td><td></td><td>MIC13</td><td>CA B2 PAR IN H</td><td>09,13</td><td></td><td>MIC15</td><td>T0 TB DATA 11 H</td><td>16,15</td><td></td></tr><tr><td>MIC09</td><td>CA 25 H</td><td>09</td><td></td><td>MIC13</td><td>CA B2 PAR L</td><td>13</td><td></td><td>MIC15</td><td>T0 TB DATA 12 H</td><td>15,16</td><td></td></tr><tr><td>MIC09</td><td>CA 26 H</td><td>09</td><td></td><td>MIC13</td><td>CA B2 PERR H</td><td>13</td><td></td><td>MIC15</td><td>T0 TB DATA 13 H</td><td>16,15</td><td></td></tr><tr><td>MIC09</td><td>CA 27 H</td><td>09</td><td></td><td>MIC13</td><td>CA B3 PAR IN H</td><td>13,09</td><td></td><td>MIC15</td><td>T0 TB DATA 14 H</td><td>16,15</td><td></td></tr><tr><td colspan="4"></td><td colspan="4"></td><td colspan="4"></td><td></td></tr><tr><td>MIC09</td><td>CA 28 H</td><td>09</td><td></td><td>MIC13</td><td>CA B3 PAR L</td><td>13</td><td></td><td>MIC15</td><td>T0 TB DATA 15 H</td><td>16,15</td><td></td></tr><tr><td>MIC09</td><td>CA 29 H</td><td>09</td><td></td><td>MIC13</td><td>CA B3 PERR H</td><td>13</td><td></td><td>MIC15</td><td>T0 TB DATA 16 H</td><td>15,16</td><td></td></tr><tr><td>MIC09</td><td>CA 30 H</td><td>09</td><td></td><td>MIC13</td><td>CA DATA PAR ERR L</td><td>13,06</td><td></td><td>MIC15</td><td>T0 TB DATA 17 H</td><td>16,15</td><td></td></tr><tr><td>MIC09</td><td>CA 31 H</td><td>09</td><td></td><td>MIC14</td><td>T0 TAG 16 H</td><td>14,17,16</td><td></td><td>MIC15</td><td>T0 TB DATA 18 H</td><td>16,15</td><td></td></tr><tr><td>MIC09</td><td>CA B2 PAR OUT H</td><td>13,09</td><td></td><td>MIC14</td><td>T0 TAG 17 H</td><td>14,17,16</td><td></td><td>MIC15</td><td>T0 TB DATA 19 H</td><td>16,15</td><td></td></tr><tr><td>MIC09</td><td>CA B3 PAR OUT H</td><td>13,09</td><td></td><td>MIC14</td><td>T0 TAG 18 H</td><td>14,17,16</td><td></td><td>MIC15</td><td>T0 TB DATA 20 H</td><td>15,16</td><td></td></tr><tr><td>MIC10</td><td>CA 00 H</td><td>10</td><td></td><td>MIC14</td><td>T0 TAG 19 H</td><td>14,17,16</td><td></td><td>MIC15</td><td>T0 TB DATA 21 H</td><td>16,15</td><td></td></tr><tr><td>MIC10</td><td>CA 01 H</td><td>10</td><td></td><td>MIC14</td><td>T0 TAG 20 H</td><td>14,17,16</td><td></td><td>MIC15</td><td>T0 TB DATA 22 H</td><td>16,15</td><td></td></tr><tr><td>MIC10</td><td>CA 02 H</td><td>10</td><td></td><td>MIC14</td><td>T0 TAG 21 H</td><td>14,17,16</td><td></td><td>MIC15</td><td>T0 TB DATA 23 H</td><td>16,15</td><td></td></tr><tr><td>MIC10</td><td>CA 03 H</td><td>10</td><td></td><td>MIC14</td><td>T0 TAG 22 H</td><td>14,17,16</td><td></td><td>MIC15</td><td>T0 TB M BIT H</td><td>15,16</td><td></td></tr><tr><td colspan="4"></td><td colspan="4"></td><td colspan="4"></td><td></td></tr><tr><td>MIC10</td><td>CA 04 H</td><td>10</td><td></td><td>MIC14</td><td>T0 TAG 23 H</td><td>14,17,16</td><td></td><td>MIC15</td><td>T1 PAR OUT 0 H</td><td>15,16</td><td></td></tr><tr><td>MIC10</td><td>CA 05 H</td><td>10</td><td></td><td>MIC14</td><td>T0 TAG 24 H</td><td>14,17,16</td><td></td><td>MIC15</td><td>T1 PAR OUT 1 H</td><td>15,16</td><td></td></tr><tr><td>MIC10</td><td>CA 06 H</td><td>10</td><td></td><td>MIC14</td><td>T0 TAG 25 H</td><td>14,16,17</td><td></td><td>MIC15</td><td>T1 PAR OUT 2 H</td><td>15,16</td><td></td></tr><tr><td>MIC10</td><td>CA 07 H</td><td>10</td><td></td><td>MIC14</td><td>T0 TAG 26 H</td><td>14,16,17</td><td></td><td>MIC15</td><td>T1 TB AC 0 H</td><td>15,16</td><td></td></tr><tr><td>MIC10</td><td>CA 08 H</td><td>10</td><td></td><td>MIC14</td><td>T0 TAG 27 H</td><td>14,16,17</td><td></td><td>MIC15</td><td>T1 TB AC 1 H</td><td>15,16</td><td></td></tr><tr><td>MIC10</td><td>CA 09 H</td><td>10</td><td></td><td>MIC14</td><td>T0 TAG 28 H</td><td>14,16,17</td><td></td><td>MIC15</td><td>T1 TB AC 2 H</td><td>15,16</td><td></td></tr><tr><td>MIC10</td><td>CA 10 H</td><td>10</td><td></td><td>MIC14</td><td>T0 TAG 29 H</td><td>14,16,17</td><td></td><td>MIC15</td><td>T1 TB AC 3 H</td><td>15,16</td><td></td></tr><tr><td>MIC10</td><td>CA 11 H</td><td>10</td><td></td><td>MIC14</td><td>T0 TAG 30 H</td><td>14,16,17</td><td></td><td>MIC15</td><td>T1 TB DATA 09 H</td><td>16,15</td><td></td></tr><tr><td>MIC10</td><td>CA 12 H</td><td>10</td><td></td><td>MIC14</td><td>T0 TAG PAR OUT L</td><td>14,17</td><td></td><td>MIC15</td><td>T1 TB DATA 10 H</td><td>16,15</td><td></td></tr><tr><td>MIC10</td><td>CA 13 H</td><td>10</td><td></td><td>MIC14</td><td>T0 VALID H</td><td>14,16,17</td><td></td><td>MIC15</td><td>T1 TB DATA 11 H</td><td>16,15</td><td></td></tr><tr><td colspan="4"></td><td colspan="4"></td><td colspan="4"></td><td></td></tr><tr><td>MIC10</td><td>CA 14 H</td><td>10</td><td></td><td>MIC14</td><td>T1 TAG 16 H</td><td>14,17,16</td><td></td><td>MIC15</td><td>T1 TB DATA 12 H</td><td>15,16</td><td></td></tr><tr><td>MIC10</td><td>CA 15 H</td><td>10</td><td></td><td>MIC14</td><td>T1 TAG 17 H</td><td>14,17,16</td><td></td><td>MIC15</td><td>T1 TB DATA 13 H</td><td>16,15</td><td></td></tr><tr><td>MIC10</td><td>CA B0 PAR OUT H</td><td>13,10</td><td></td><td>MIC14</td><td>T1 TAG 18 H</td><td>14,17,16</td><td></td><td>MIC15</td><td>T1 TB DATA 14 H</td><td>16,15</td><td></td></tr><tr><td>MIC10</td><td>CA B1 PAR OUT H</td><td>13,10</td><td></td><td>MIC14</td><td>T1 TAG 19 H</td><td>14,17,16</td><td></td><td>MIC15</td><td>T1 TB DATA 15 H</td><td>16,15</td><td></td></tr><tr><td>MIC11</td><td>B PAD 02 H</td><td>10,09,11</td><td></td><td>MIC14</td><td>T1 TAG 20 H</td><td>14,17,16</td><td></td><td>MIC15</td><td>T1 TB DATA 16 H</td><td>15,16</td><td></td></tr><tr><td>MIC11</td><td>B PAD 03 H</td><td>10,09,11</td><td></td><td>MIC14</td><td>T1 TAG 21 H</td><td>14,17,16</td><td></td><td>MIC15</td><td>T1 TB DATA 17 H</td><td>16,15</td><td></td></tr><tr><td>MIC11</td><td>B PAD 04 H</td><td>10,09,11</td><td></td><td>MIC14</td><td>T1 TAG 22 H</td><td>14,17,16</td><td></td><td>MIC15</td><td>T1 TB DATA 18 H</td><td>16,15</td><td></td></tr><tr><td>MIC11</td><td>B PAD 05 H</td><td>10,09,11</td><td></td><td>MIC14</td><td>T1 TAG 23 H</td><td>14,17,16</td><td></td><td>MIC15</td><td>T1 TB DATA 19 H</td><td>16,15</td><td></td></tr><tr><td>MIC11</td><td>B PAD 06 H</td><td>10,09,11</td><td></td><td>MIC14</td><td>T1 TAG 24 H</td><td>14,17,16</td><td></td><td>MIC15</td><td>T1 TB DATA 20 H</td><td>15,16</td><td></td></tr><tr><td>MIC11</td><td>B PAD 07 H</td><td>10,09,11</td><td></td><td>MIC14</td><td>T1 TAG 25 H</td><td>14,16,17</td><td></td><td>MIC15</td><td>T1 TB DATA 21 H</td><td>16,15</td><td></td></tr></table>												SIGNAL NAME		PAGE NUMBER(S)		SIGNAL NAME		PAGE NUMBER(S)		SIGNAL NAME		PAGE NUMBER(S)		MIC08	CA TAG 18 H	11,12,08		MIC11	B PAD 08 H	10,09,11		MIC14	T1 TAG 26 H	14,16,17		MIC08	CA TAG 19 H	11,12,08		MIC11	B PAD 09 H	10,09,11		MIC14	T1 TAG 27 H	14,16,17		MIC08	CA TAG 20 H	11,12,08		MIC11	B PAD 10 L	10,09,11		MIC14	T1 TAG 28 H	14,16,17		MIC08	CA TAG 21 H	11,12,08		MIC11	B PAD 11 L	10,09,11		MIC14	T1 TAG 29 H	14,16,17		MIC08	CA TAG 22 H	11,12,08		MIC11	CA WRITE ENA L	10,09,11,08		MIC14	T1 TAG 30 H	14,16,17		MIC08	CA TAG 23 H	11,12,08		MIC11	ENA CACHE H	09,10,11		MIC14	T1 TAG PAR OUT L	14,17		MIC08	CA TAG PAR OUT H	12,08		MIC11	ENA CACHE L	13,09,10,11		MIC14	T1 VALID H	14,16,17		MIC08	CA VALID H	11,12,08		MIC11	MI B CLK H	11,16		MIC15	T0 PAR OUT 0 H	15,16		MIC09	CA 16 H	09		MIC12	CA TAG PAR ERR H	12,06		MIC15	T0 PAR OUT 1 H	15,16		MIC09	CA 17 H	09		MIC12	CA TAG PAR IN H	12,08		MIC15	T0 PAR OUT 2 H	15,16															MIC09	CA 18 H	09		MIC13	CA B0 PAR IN H	13,10		MIC15	T0 TB AC 0 H	15,16		MIC09	CA 19 H	09		MIC13	CA B0 PAR L	13		MIC15	T0 TB AC 1 H	15,16		MIC09	CA 20 H	09		MIC13	CA B0 PERR H	13		MIC15	T0 TB AC 2 H	15,16		MIC09	CA 21 H	09		MIC13	CA B1 PAR IN H	13,10		MIC15	T0 TB AC 3 H	15,16		MIC09	CA 22 H	09		MIC13	CA B1 PAR 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DRN. *G+Mde*
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DATE 21-MAR-88
SHEET 1 OF 1

ENG.
BOARD LOCATION: AC3

DATE
NEXT HIGHER ASSEMBLY:

160,1271 MIC21.DRW
FIRST USED ON OPTION/MODEL: 11/750

19-MAR-88 18:17
B-DD-L0003-0-0

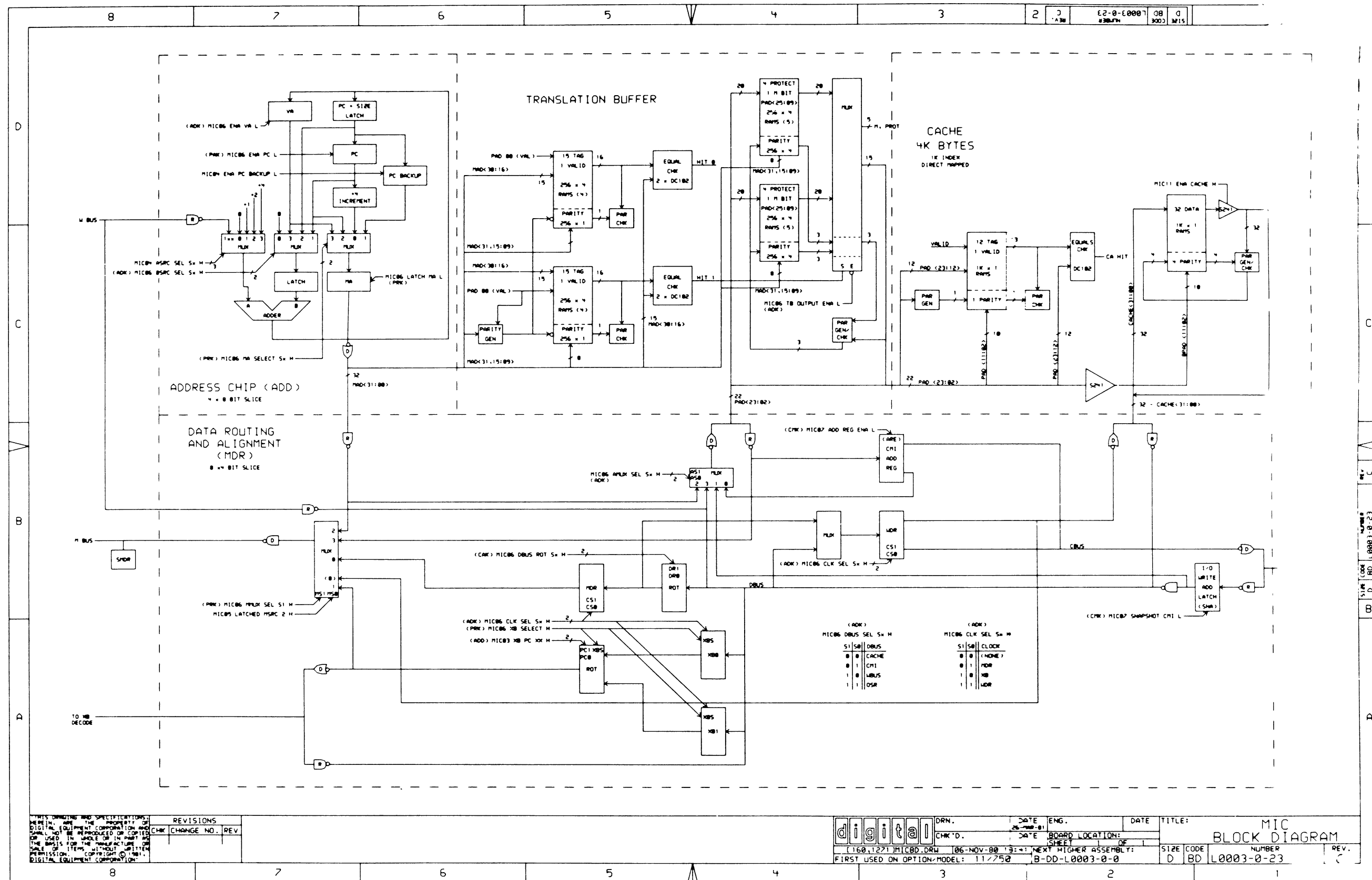
TITLE: MIC FORWARD REFERENCE

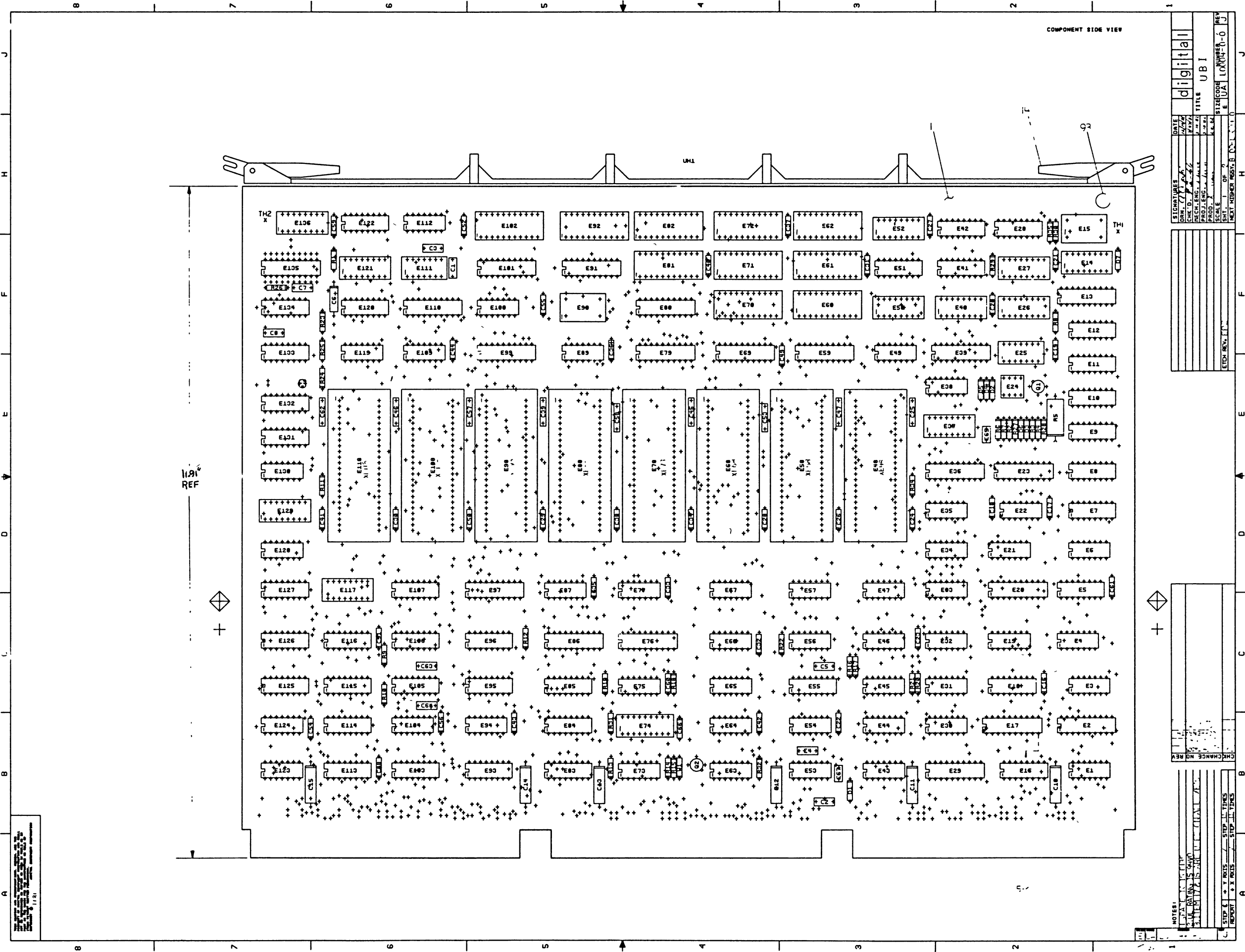
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INM L</td><td>04</td><td></td><td></td></tr><tr><td>MIC17 TB DATA PERR 0 M</td><td>15,04,17</td><td>UBI13 MSEQ INIT L</td><td>07,06</td><td></td><td></td></tr><tr><td>MIC17 TB DATA PERR 0 L</td><td>17</td><td>UBI14 UB INT GRANT M</td><td>07</td><td></td><td></td></tr><tr><td>MIC17 TB DATA PERR 1 M</td><td>15,04,17</td><td>UBUS 00 M</td><td>01,03</td><td></td><td></td></tr><tr><td>MIC17 TB DATA PERR 1 L</td><td>17</td><td>UBUS 01 M</td><td>03,01</td><td></td><td></td></tr><tr><td>MIC17 TB DATA PERR 2 M</td><td>15,04,17</td><td>UBUS 02 M</td><td>03,01</td><td></td><td></td></tr><tr><td>MIC17 TB DATA PERR 2 L</td><td>17</td><td>UBUS 03 M</td><td>03,01</td><td></td><td></td></tr><tr><td>MIC17 TB DATA PERR M</td><td>17,07</td><td>UBUS 04 M</td><td>03,02</td><td></td><td></td></tr><tr><td>MIC17 TB TAG 0 PERR M</td><td>07,17</td><td>UBUS 05 M</td><td>02,03</td><td></td><td></td></tr><tr><td>MIC17 TB TAG 1 PERR M</td><td>07,17</td><td>UBUS 06 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00 M	01,03			MIC17 TB DATA PERR 1 L	17	UBUS 01 M	03,01			MIC17 TB DATA PERR 2 M	15,04,17	UBUS 02 M	03,01			MIC17 TB DATA PERR 2 L	17	UBUS 03 M	03,01			MIC17 TB DATA PERR M	17,07	UBUS 04 M	03,02			MIC17 TB TAG 0 PERR M	07,17	UBUS 05 M	02,03			MIC17 TB TAG 1 PERR M	07,17	UBUS 06 M	03,02			MIC18 B CLK L	18,06,04,05,11,07,01,02,03	UBUS 07 M	03,02			MIC18 INM LATCH MA M	06,18	UBUS 08 M	01,03			MIC18 INTERRUPT INM M	18	UBUS 09 M	03,01			MIC18 LATCHED UTRAP M	18,04	UBUS 10 M	03,01			MIC18 MIC LD OSR L	18,04,06	UBUS 11 M	01,03			MIC18 X STALL M	18,04	UBUS 12 M	03,02			MIC18 X STALL L	18,04	UBUS 13 M	02,03			MICRO VECTOR 0 M	04,07	UBUS 14 M	03,02			MICRO VECTOR 1 M	04,07	UBUS 15 M	02,03			MICRO VECTOR 2 M	04,07	UBUS 16 M	01,03			MICRO VECTOR 3 M	04,07	UBUS 17 M	01,03			PAD 02 M	11,08,01	UBUS 18 M	03,01			PAD 03 M	15,11,05,16,08,01	UBUS 19 M	01,03			PAD 04 M	15,11,05,16,08,02	UBUS 20 M	02,03			PAD 05 M	15,11,05,16,08,02	UBUS 21 M	02,03			PAD 06 M	15,11,05,16,08,02	UBUS 22 M	03,02			PAD 07 M	15,11,05,16,08,02	UBUS 23 M	02,03			PAD 08 M	11,05,16,14,08,01,17	UBUS 24 M	06,07,01,03			PAD 09 M	16,15,11,17,08,01	UBUS 25 M	06,07,01,03			PAD 10 M	11,16,15,17,08,01	UBUS 26 M	06,07,01,03			PAD 11 M	16,15,11,17,08,01	UBUS 27 M	06,07,01,03			PAD 12 M	12,15,16,17,11,08,02	UBUS 28 M	02,03			PAD 13 M	12,16,15,17,11,08,02	UBUS 29 M	02,03			PAD 14 M	12,16,15,17,11,08,02	UBUS 30 M	02,03			PAD 15 M	12,16,15,17,11,08,02	UBUS 31 M	02,03			PAD 16 M	15,12,16,17,11,08,01				
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MIC17 TB DATA PERR M	17,07	UBUS 04 M	03,02																																																																																																																																																																																																																																																																																																																												
MIC17 TB TAG 0 PERR M	07,17	UBUS 05 M	02,03																																																																																																																																																																																																																																																																																																																												
MIC17 TB TAG 1 PERR M	07,17	UBUS 06 M	03,02																																																																																																																																																																																																																																																																																																																												
MIC18 B CLK L	18,06,04,05,11,07,01,02,03	UBUS 07 M	03,02																																																																																																																																																																																																																																																																																																																												
MIC18 INM LATCH MA M	06,18	UBUS 08 M	01,03																																																																																																																																																																																																																																																																																																																												
MIC18 INTERRUPT INM M	18	UBUS 09 M	03,01																																																																																																																																																																																																																																																																																																																												
MIC18 LATCHED UTRAP M	18,04	UBUS 10 M	03,01																																																																																																																																																																																																																																																																																																																												
MIC18 MIC LD OSR L	18,04,06	UBUS 11 M	01,03																																																																																																																																																																																																																																																																																																																												
MIC18 X STALL M	18,04	UBUS 12 M	03,02																																																																																																																																																																																																																																																																																																																												
MIC18 X STALL L	18,04	UBUS 13 M	02,03																																																																																																																																																																																																																																																																																																																												
MICRO VECTOR 0 M	04,07	UBUS 14 M	03,02																																																																																																																																																																																																																																																																																																																												
MICRO VECTOR 1 M	04,07	UBUS 15 M	02,03																																																																																																																																																																																																																																																																																																																												
MICRO VECTOR 2 M	04,07	UBUS 16 M	01,03																																																																																																																																																																																																																																																																																																																												
MICRO VECTOR 3 M	04,07	UBUS 17 M	01,03																																																																																																																																																																																																																																																																																																																												
PAD 02 M	11,08,01	UBUS 18 M	03,01																																																																																																																																																																																																																																																																																																																												
PAD 03 M	15,11,05,16,08,01	UBUS 19 M	01,03																																																																																																																																																																																																																																																																																																																												
PAD 04 M	15,11,05,16,08,02	UBUS 20 M	02,03																																																																																																																																																																																																																																																																																																																												
PAD 05 M	15,11,05,16,08,02	UBUS 21 M	02,03																																																																																																																																																																																																																																																																																																																												
PAD 06 M	15,11,05,16,08,02	UBUS 22 M	03,02																																																																																																																																																																																																																																																																																																																												
PAD 07 M	15,11,05,16,08,02	UBUS 23 M	02,03																																																																																																																																																																																																																																																																																																																												
PAD 08 M	11,05,16,14,08,01,17	UBUS 24 M	06,07,01,03																																																																																																																																																																																																																																																																																																																												
PAD 09 M	16,15,11,17,08,01	UBUS 25 M	06,07,01,03																																																																																																																																																																																																																																																																																																																												
PAD 10 M	11,16,15,17,08,01	UBUS 26 M	06,07,01,03																																																																																																																																																																																																																																																																																																																												
PAD 11 M	16,15,11,17,08,01	UBUS 27 M	06,07,01,03																																																																																																																																																																																																																																																																																																																												
PAD 12 M	12,15,16,17,11,08,02	UBUS 28 M	02,03																																																																																																																																																																																																																																																																																																																												
PAD 13 M	12,16,15,17,11,08,02	UBUS 29 M	02,03																																																																																																																																																																																																																																																																																																																												
PAD 14 M	12,16,15,17,11,08,02	UBUS 30 M	02,03																																																																																																																																																																																																																																																																																																																												
PAD 15 M	12,16,15,17,11,08,02	UBUS 31 M	02,03																																																																																																																																																																																																																																																																																																																												
PAD 16 M	15,12,16,17,11,08,01																																																																																																																																																																																																																																																																																																																														
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<div>NOTES:</div> <div>1. THIS PAGE LISTS THE SCHEMATIC PAGE NUMBER(S) WHERE A SIGNAL NAME IS REFERENCED.</div>																																																																																																																																																																																																																																																																																																																															
<div><div><div>THIS DRAWING AND SPECIFICATIONS ARE THE PROPERTY OF DIGITAL EQUIPMENT CORPORATION AND SHALL NOT BE REPRODUCED OR COPIED OR USED IN WHOLE OR IN PART AS THE BASIS FOR THE MANUFACTURE OR SALE OF ITEMS WITHOUT WRITTEN PERMISSION. COPYRIGHT © 1986, DIGITAL EQUIPMENT CORPORATION</div><div>REVISIONS</div><table><tr><th>CHK</th><th>CHANGE NO.</th><th>REV</th></tr><tr><td></td><td></td><td></td></tr></table></div><div><div>digital</div><div>DRN: 61M2</div><div>DATE: 06-NOV-88</div><div>ENG.</div><div>DATE:</div><div>TITLE: MIC FORWARD REFERENCE</div><div>160,1271 MIC22.DRW</div><div>06-NOV-88 18:32</div><div>NEXT HIGHER ASSEMBLY: B-DD-L0003-0-0</div><div>SHEET: 1</div><div>OF: 1</div><div>SIZE: D</div><div>CODE: CS</div><div>NUMBER: L0003-0-22</div><div>REV: C</div></div></div>								CHK	CHANGE NO.	REV																																																																																																																																																																																																																																																																																																																					
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11/91

NOTES:	
1. ALL WORK TO BE DONE IN ACCORDANCE WITH THE LATEST EDITIONS OF THE BIDDING SPECIFICATIONS AND THE STANDARD SPECIFICATIONS FOR CONSTRUCTION.	2. THE CONTRACTOR SHALL BE RESPONSIBLE FOR OBTAINING ALL NECESSARY PERMITS AND APPROVALS FROM THE APPROPRIATE AGENCIES.
SIGNATURES:	
DATE: 11/91	DATE: 11/91
DESIGNED BY: [Signature]	DESIGNED BY: [Signature]
CHECKED BY: [Signature]	CHECKED BY: [Signature]
PROJECT ENGINEER: [Signature]	PROJECT ENGINEER: [Signature]
SCALE: 1/8" = 1'-0"	SCALE: 1/8" = 1'-0"
SHEET: 1 OF 2	SHEET: 1 OF 2
NEXT SHEET: 11/91	
TITLE: UBT	
SIZE CODE: E UA 1004-0-0 J	
REV: 1	

8 7 6 5 4 3 2 1

0-0-00007 V1.2

REWORK INSTRUCTIONS
WIRE ADDS ECO*5
5-2 E16-15 TO E17-10

REVISION HISTORY

DATE	ECO NUMBER	REV

UBI

DOCUMENT NUMBER

REV	CODE	NUMBER	REV
E	JA	L0004-0-0	J

SCALE 1:1 SHEET 1 OF 2

J H F E D C A

UBI L0004-0-0 J

SHEET A1 OF A3

REVISION HISTORY			BASIC PART NO: L0004		DRN: K.FRIEDGEN		DATE: 04-MAY-79		D I G I T A L	
ENG	ECO NUMBER	REV	SECTION A OF A		CHK'D: E.T.GERRY		DATE: 04-MAY-79		TITLE PARTS LIST	
---	INITIAL	C	SECTION VARIATION INDEX		DATE: 04-MAY-79		U.S.I.			
TK	L0004-TW002	[A]	00							
D L	L0004-TW003	[B]								
LL	TW005	[C]			DES.ENG: S.SMITH		DATE: 4-MAY-79			
		[D]								
		[E]			RESP.ENG.: S.SMITH		DATE: 04-MAY-79		DOCUMENT NUMBER	
		[F]								
		[H]							SIZE CODE NUMBER REV	
		[J]								
		[K]			MFG.ENG.: VANCE PARKER		DATE: 8-FEB-80		K	PL L0004-0-DBP F
		[L]								
		[M]			ASSEMBLY NUMBER:		TOP DOCUMENT NUMBER:		FILE NAME: EDIT #	
		[N]			E-UA-L0004-0-0		#B-DD-L0004-0		Z1256F.PLS 20	

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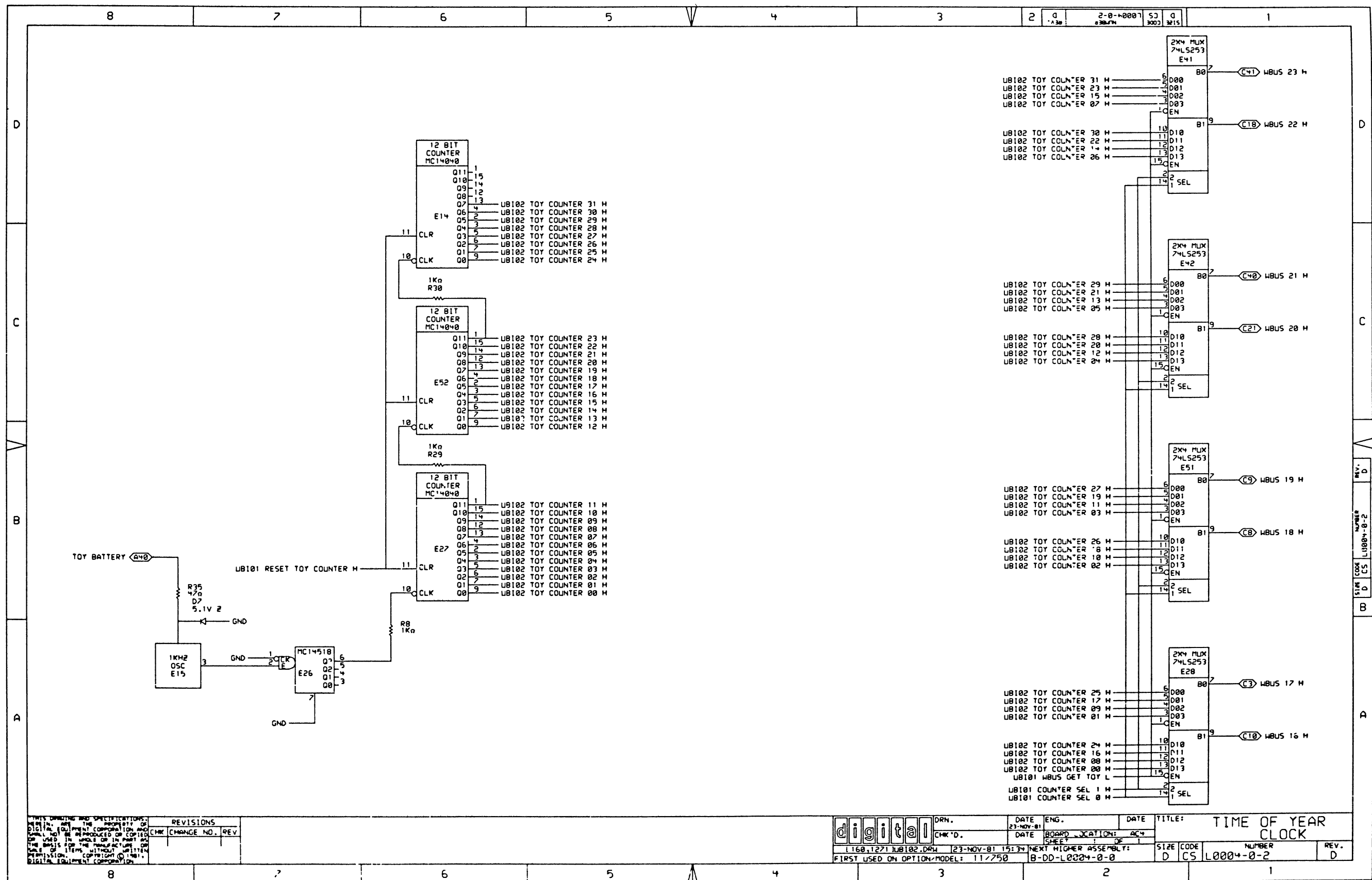
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					00		
30	30		1301571-00	68.0 1.0 W 5.0 % CC	1		R5
31	31		1304838-00	43.0 K .25 W 5.0 % CC	1		R24
32	32		1302514-00	39.0 K .25 W 5.0 % CC	1		R26
33	33		1305125-00	383.0 .25 W 1.0 % RN55D-F10	2		R9,R10
34	34		1312628-00	R NETWORK 14-176.5 14-375 16PIN	2		E93,E114
35	35		1312628-01	R NETWORK 14-176.5 11-375 16PIN	2		E103,E113
36	36		1509649-01	3762 PNP 4W SI 40 35	1		Q1
37	37		1811660-23	OSCILLATOR, XTAL 5.5296 MHZ	1		E90
38	38		1813951-00	OSCILLATOR, XTAL 1.0 KHZ	1		E15
39	39		1909705-00	DEC 8381 NAND GATE-QUAD 2IN 0	2		E94,E123
40	40		1910322-00	DEC 1488L DRIVER, LINE, QUAD, EI	1		E111
41	41		1910323-00	DEC 1489L RECEIVER, LINE, QUAD	1		E53
42	42		1910436-00	DEC 74123 ONE SHOT-DUAL, RETRIG	2		E133,E134
43	43		1910532-00	74500 NAND GATE-QUAD 2IN	4		E30,E66,E77,E100
44	44		1910533-00	74503 NAND GATE-QUAD 2IN, 0	1		E45
45	45		1910534-00	74504 INVERTER GATE-HEX 1I	3		E21,E31,E87
46	46		1910535-00	74505 INVERTER GATE-HEX 1	1		E22
47	47		1910536-00	74510 NAND GATE-TRIPLE 3IN	3		E19,E65,E89
48	48		1910539-00	74520 NAND GATE-DUAL 4INPU	3		E75,E109
49	49		1910544-00	74574 FF-D DUAL, EDGE TRIGG	5		E4,E32,E44,E67,E119
50	50		1910550-00	745174 FF-D HEX	1		E127
51	51		1910957-00	745175 FF-D QUAD COMMON CLO	3		E5,E55,E132
52	52		1911469-00	DEC 8640 RECEIVER, BUS, QUAD, U	2		E104,E124
53	53		1911573-00	745280 PARITY GEN/CHKR 9BIT	1		E3
54	54		1911579-00	8641 TRANSCEIVER, BUS, QUA	11		E83-E85,E95,E96,E105,E106,E115
55	55		1911641-00	SN 745257 MUX, QUAD 2 TO 1	4	CONT	E116,E125,E126
56	56		1911675-00	745138 DECODER/DEMUX 3-8 LIN	1		E107,E120,E122,E131
57	57		1911712-00	74551 AND-OR GATE-INVERT 0	1		E18
58	58		1912388-00	74502 NOR GATE-QUAD 2IN, PO	4		E54
59	59		1912389-00	74508 AND GATE-QUAD 2IN, PO	2		E47,E57,E64,E130
60	60		1912646-00	LS253 MUX 1 OF 4 (DUAL)	4		E35,E46
61	61		1912799-00	LS00 NAND-GATE-QUAD 2IN, P	2		E28,E41,E42,E51
62	62		1912810-00	LS20 NAND GATE-DUAL 4IN, P	1		E73,E128
63	63		1912812-00	LS22 NAND GATE-DUAL 4IN, P	1		E34
64	64		1912815-00	LS30 NAND GATE-SINGLE 8IN	1		E48
65	65		1912827-00	LS83 ADDERS-4BIT	4		E43
66	66		1912863-00	LS273 FF-D OCTAL W/CLEAR	1		E117,E121,E129,E136
67	67		1913340-00	74532 OR GATE-QUAD 2IN	1		E39
68	68		1913493-00	745241 OCTAL BUFFER, TRI-STA	1		E33
69	69		1913671-00	745374 FF-D OCTAL TRISTATE	7		E76
70	70		1913839-00	LS165 SHIFT REG., 8BIT	1		E2,E13,E17,E20,E23,E29,E36
71	71		1914085-00	745260 NOR GATE-DUAL, POS	1		E16
72	72		1914156-00	LM 393 VOLT. COMPARATOR DUAL	1		E1
73	73		1914451-00	74LS393 COUNTER, BINARY, 4BIT	3		E24
74	74		1914558-00	SN 745132N NAND GATE QUAD 2IN P	1		E6,E63,E112
75	75		1914685-00	DC 611B BIPOLAR, LS, 400-GATE	2		E56
76	76		1914692-00	DC 618C BIPOLAR, LS, 400-GATE	4		E108,E118

D	I	G	I	T	A	L	TITLE	U.B.I.	SECTION A OF A	SIZE	CODE	DOCUMENT NUMBER	REV
										K	PL	L0004-0-DBP	F

LINE	ITEM	DOCUMENT NUMBER	PART NUMBER	DESCRIPTION	QTY PER VARIATION	REFERENCE DESIGNATOR
					00	
77	77		1914693-00	DC 619D BIPOLAR,LS,400-GATE	1	E48
78	78		1914704-00	DC 630B BIPOLAR,LS,400-GATE	1	E98
79	79		1915193-00	LS244 DRIVER,LINE,OCTAL,T	9	E59,E69,E79,E80,E86,E97,E99, E101,E110
80	80		1915697-00	RAM 256X4 TRI-STATE	10	E60-E62,E70-E72,E81,E82,E92,E102
81	81		2112623-02	DUAL BAUD RATE GEN/PROG DIVIDER	1	E74
82	82		2113603-00	4001UBNOR GATE-QUAD 2IN CM	1	E25
83	83		2113647-00	4513B COUNTER,DUAL UP BCD	1	E26
84	84		2113653-00	74C89 RAM 64BIT CMOS TRIST	1	E50
85	85		2114462-00	4040B COUNTER/DIVIDER,BINA	3	E14,E27,E52
86	86		23524A2-00	A2-05	1	E7
87	87		23525A2-00	A2-05	1	E8
88	88		23526A2-00	A2-05	1	E9
89	89		23527A2-00	A2-05	1	E10
90	90		23528A2-00	A2-05	1	E11
91	91		23529A2-00	A2-05	1	E12
92	92		23000A2-05	*** THIS ITEM IS NOT USED ***	-	
93	93		9000024-01	EYELET,ROLL FLANGE .1210DX .192	12	
94	94		1002476-00	510.0 MMF 100V 5%200PPM MICA	1	C7
95	95		1304835-00	/REPLACED BY 13-05337-00	1	R27
96	96		1305374-00	910.0 .25 W 5.0 % CC	1	R6
97	97		1314187-00	6.19 K .25 W 1.0 % RN55D-F10	1	R7
98	98		1503121-00	2N 2369 NPN 350MW SI N	1	Q2
99	99		9009185-00	JUMPER, WIRE, INSULATED, BLACK B	1	W1
100	100		1302379-00	75.0 .25 W 5.0 % CC	2	R32,R34
101	101		1910542-00	74564 A-J-I GATE 4-2-3-2	1	E38
102	102		1913670-00	745373 LATCH 8BIT TRASP TR	1	E91
103	103		1300202-00	47.0 .25 W 5.0 % CC	1	R35
104	104		1118585-00	5953 VZ= 5.2 .40W DO	1	D7
105	105		9007200-00	TRANSIPADS #10134	1	XQ2

106 NOTE: SPARE I.C. LOCATIONS ARE:E130,E135
107 NOTE: SOME MODULES WILL HAVE 10-05306 INSTEAD OF 10-12034-01

D	I	G	I	T	A	L	TITLE	SECTION A OF A	SIZE	CODE	DOCUMENT NUMBER	REV
							U.B.I.		K	PL	L0004-0-DBP	F



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REVISIONS
CHK CHANGE NO. REV

digital DRN.
CHK'D.

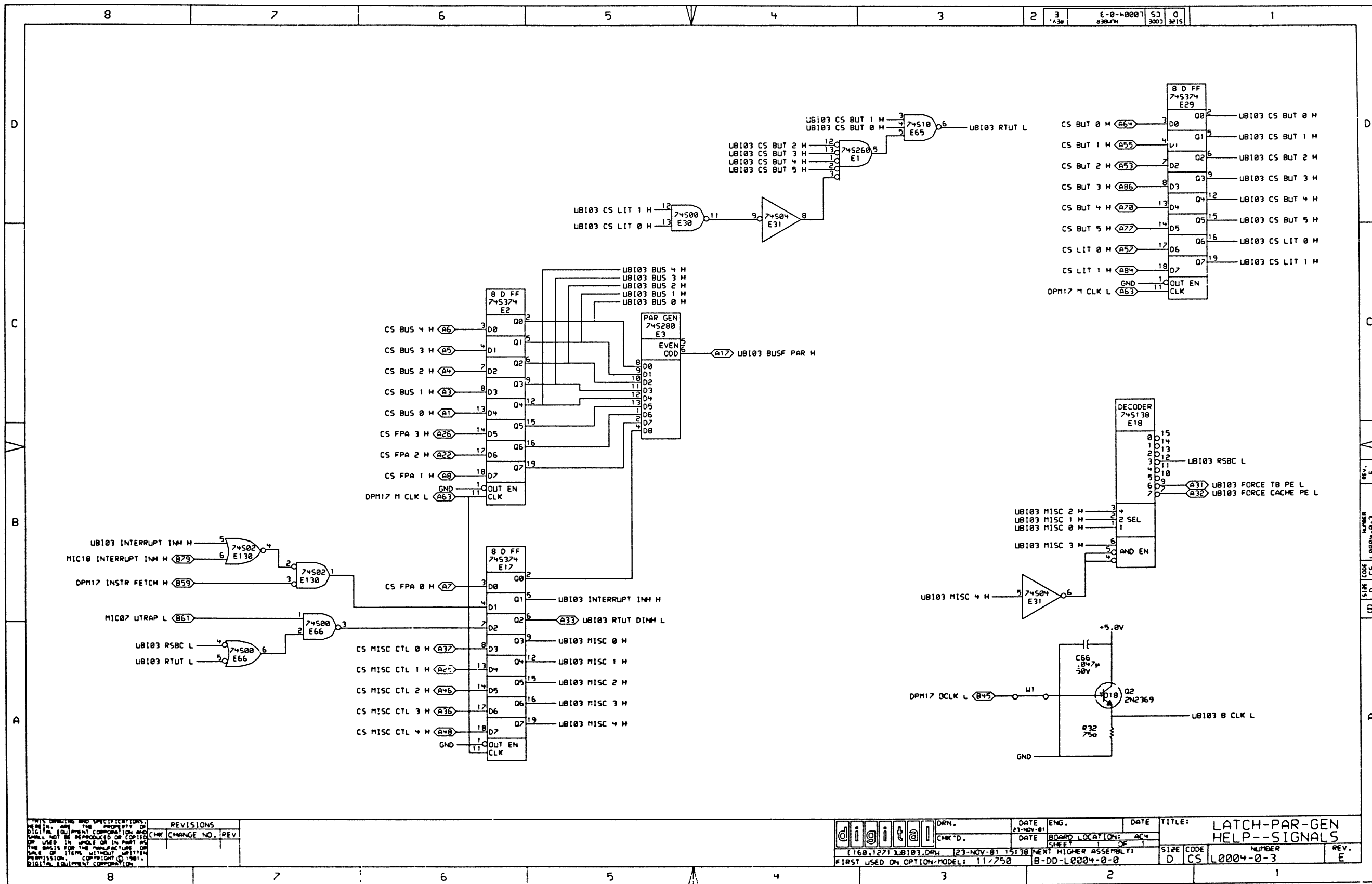
DATE 23-NOV-81 ENG.
DATE 23-NOV-81 BOARD LOCATION: ACY

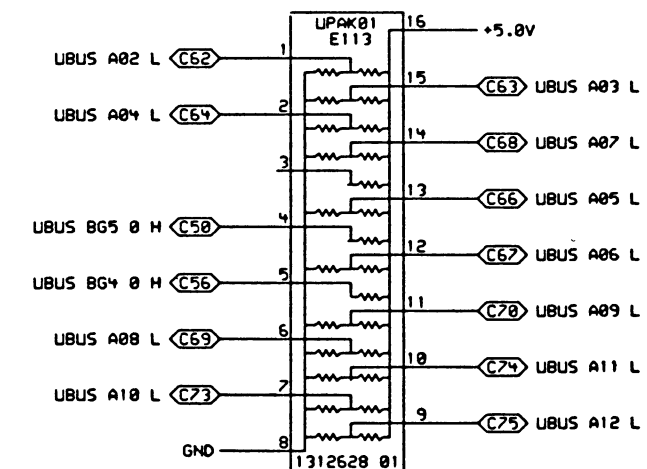
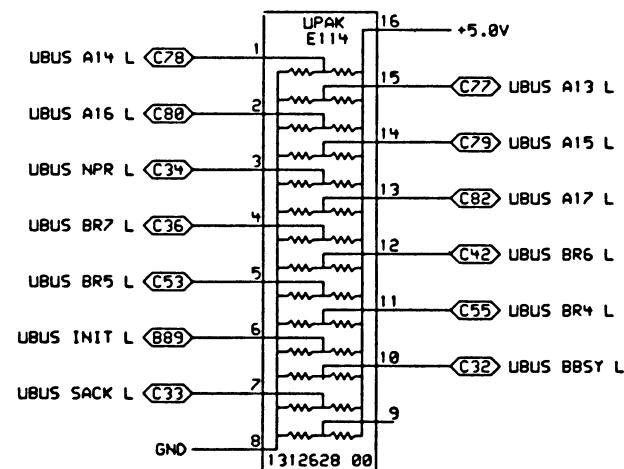
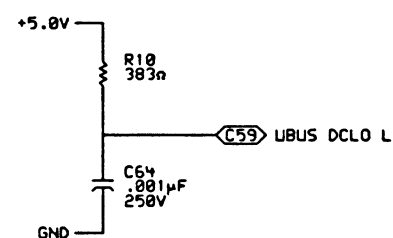
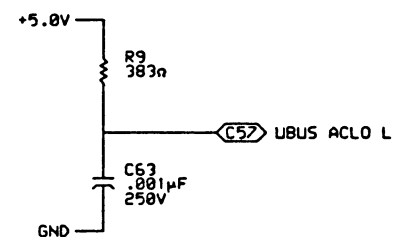
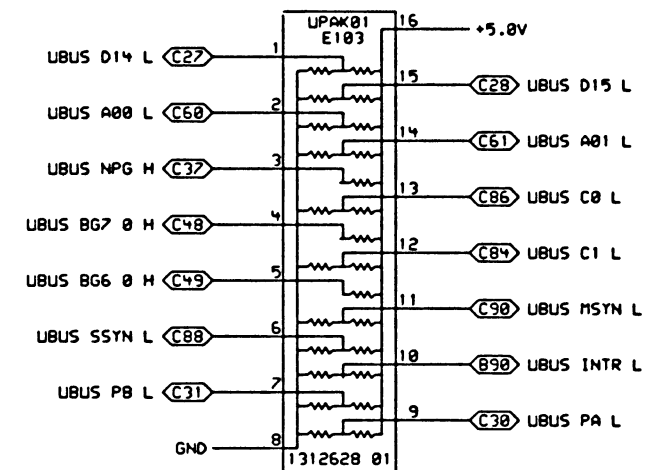
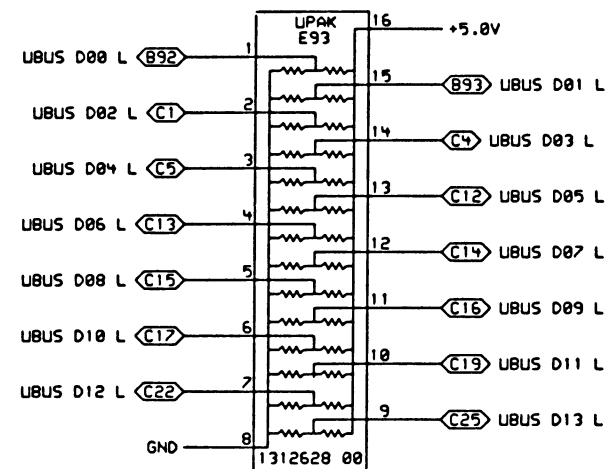
TITLE: TIME OF YEAR CLOCK

L160,1271 UB102.DPL 23-NOV-81 15:34
FIRST USED ON OPTION MODEL: 117750

NEXT HIGHER ASSEMBLY: B-DD-L0004-0-0

SIZE CODE NUMBER REV.
D CS L0004-0-2 D





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REVISIONS		
CHK	CHANGE NO.	REV

digital

DRN. RCPC

	DATE	ENG.
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	DATE
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E	TITLE:
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UNIBUS
RESISTOR PAKS

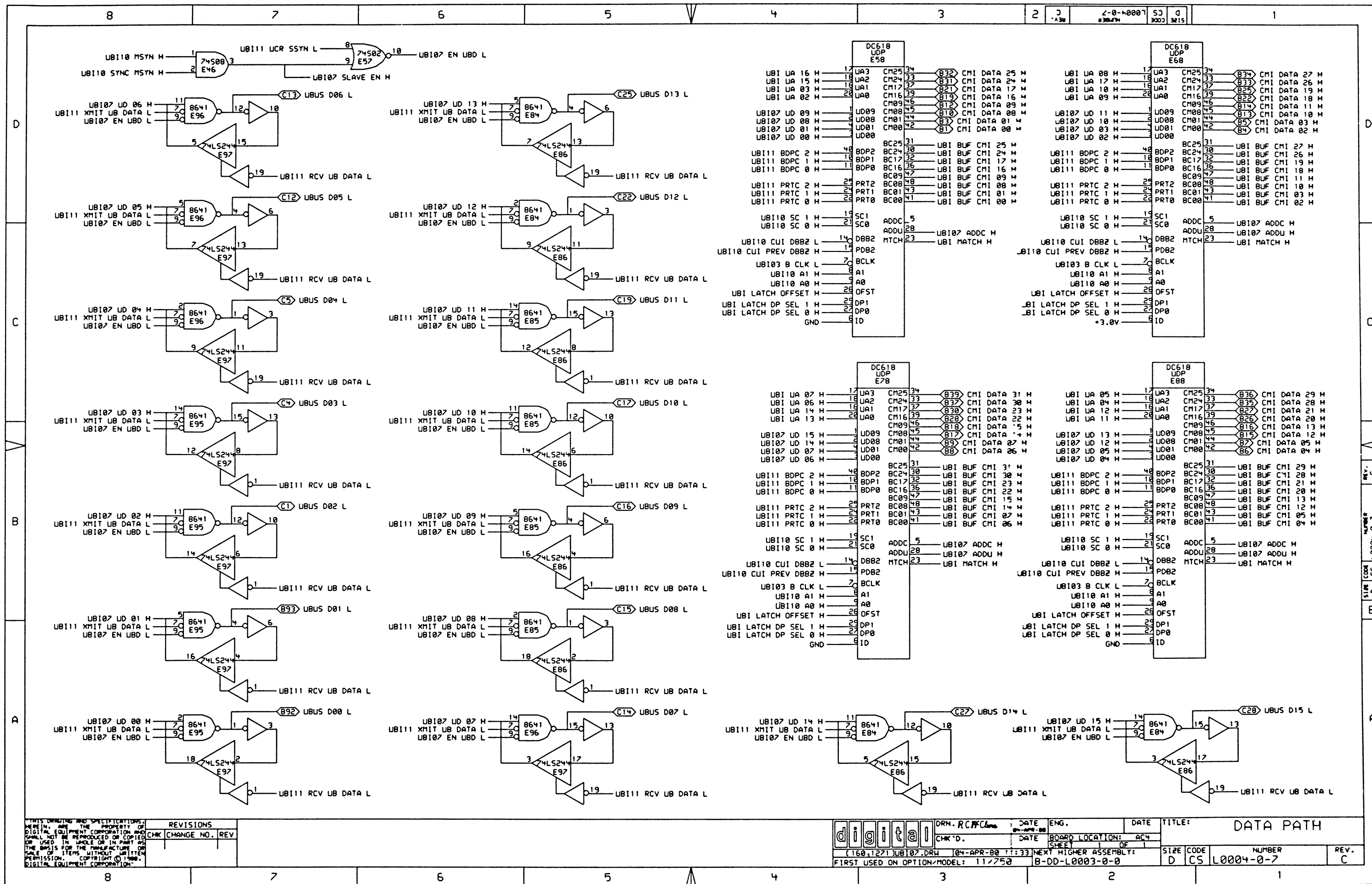
[160,1271]UB[04.DRW 19-MAR-80 1
FIRST USED ON OPTION/MODEL: 11/750

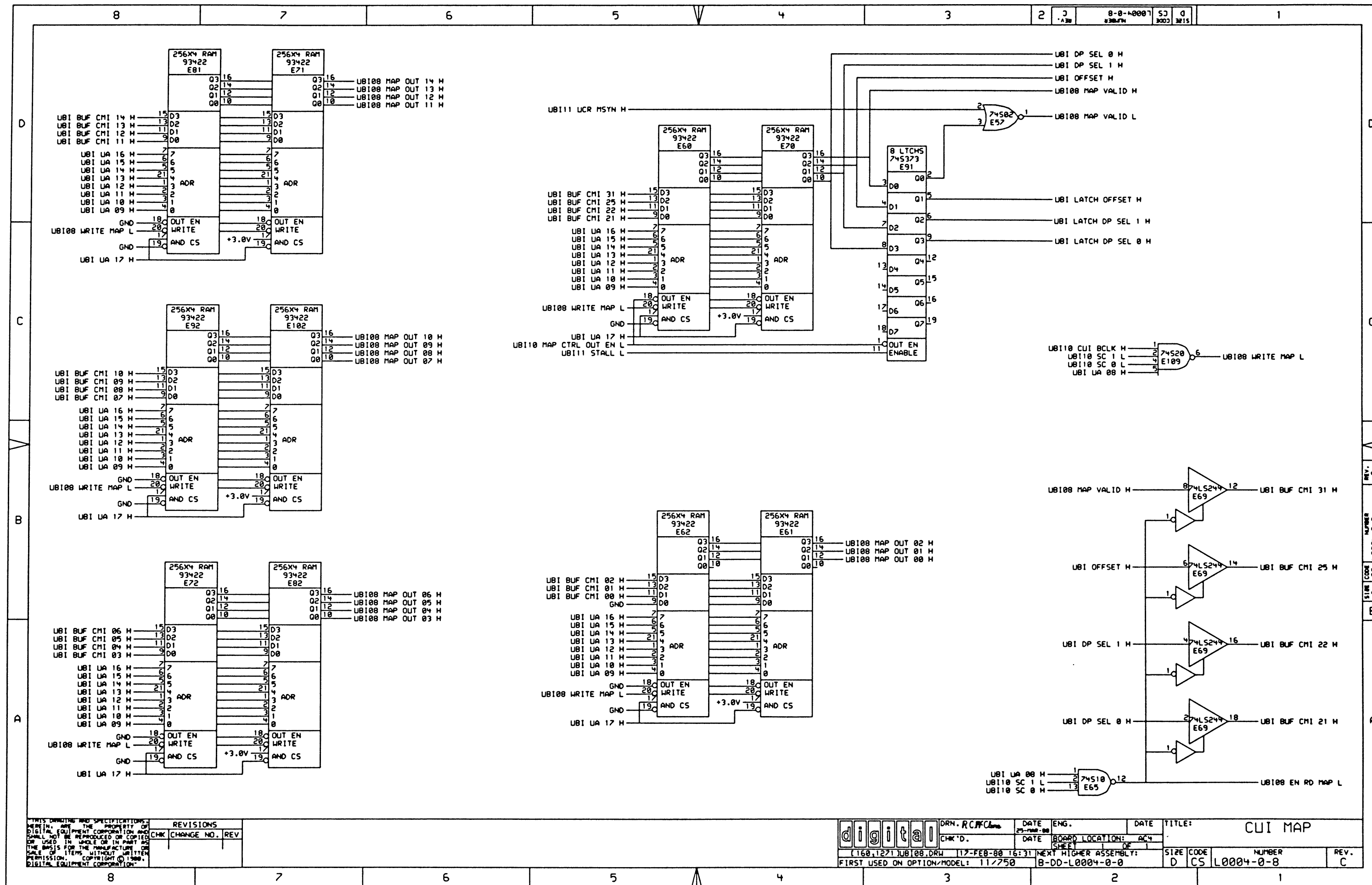
0:37	NEXT HIGHER ASSEMBLY:
	B-DD-L0003-0-0

SIZE	CODE
D	CS

NUMBER L0004-0-4	REV. C
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COMETCHIPS, (160,127)UB104.DPL, SCALE 2, "D" RELEASE BOX
COMETCHIPS UB104 BOX 160-12711 25-MAR-80 12:46

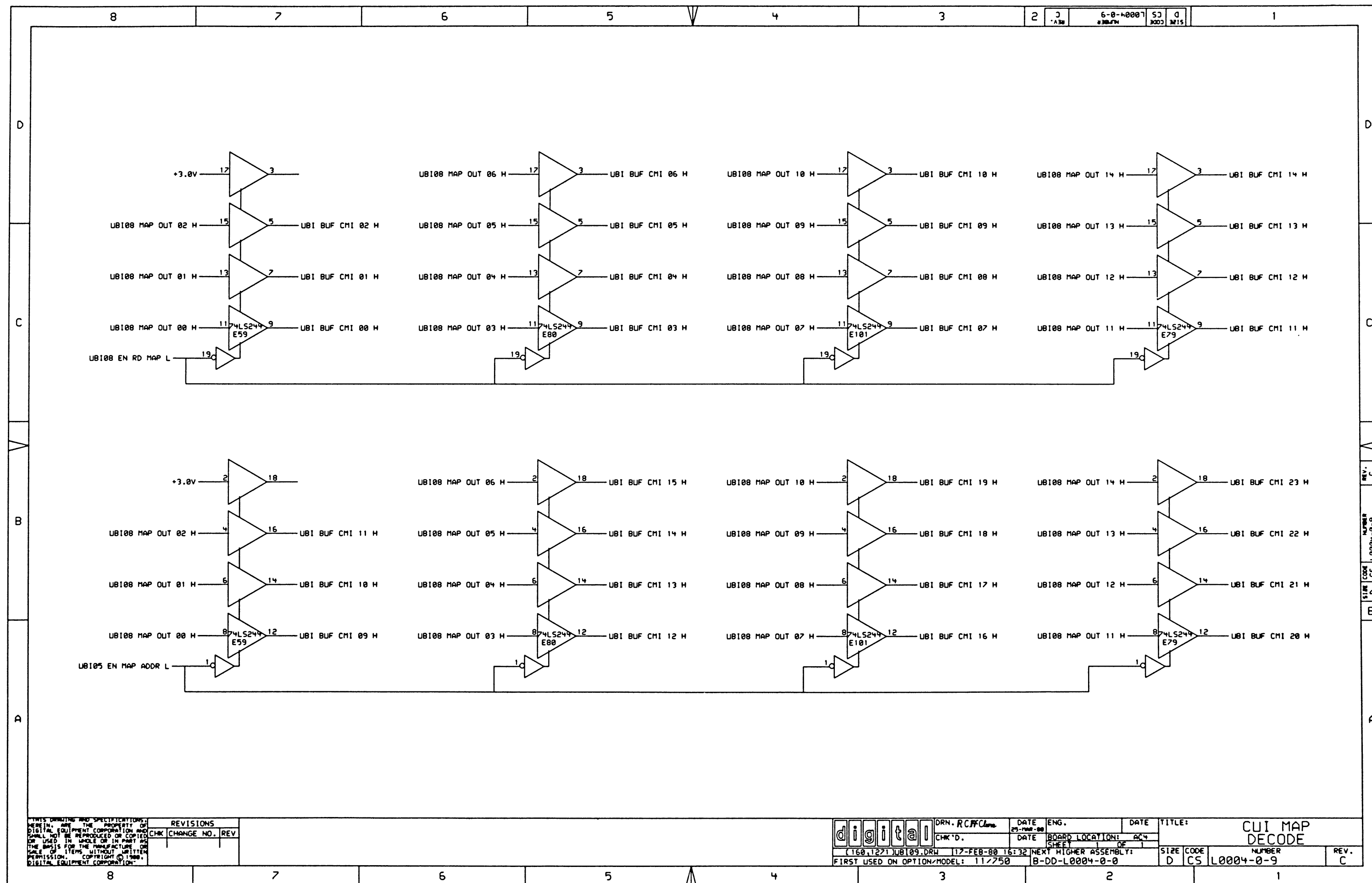




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REVISIONS		
CHK	CHANGE NO.	REV

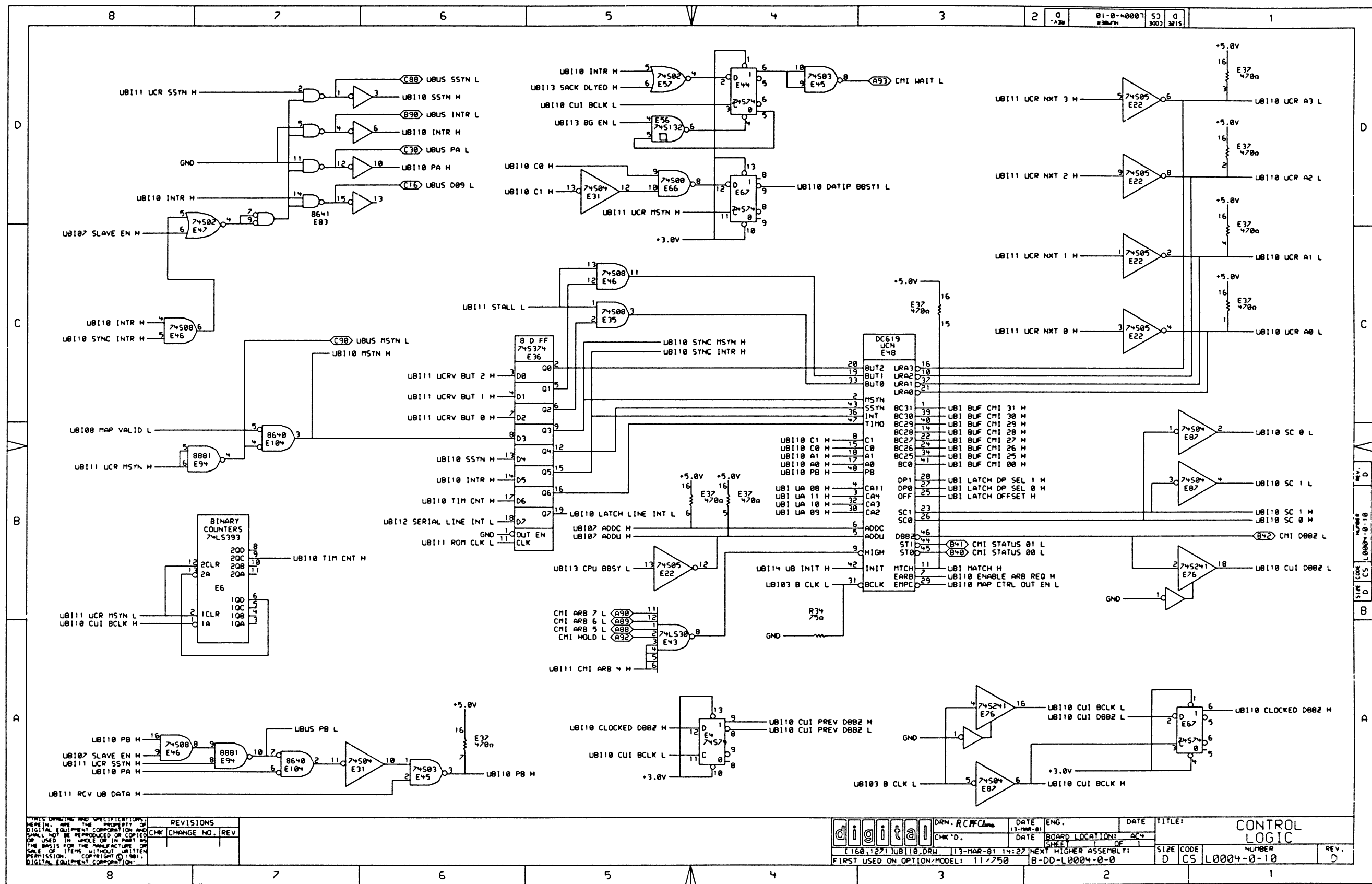
digital	DRN. R.C.F. Chas	DATE	ENG.	DATE	TITLE: CUI MAP				
	CHK'D.	DATE	BOARD LOCATION: AC4		SIZE	CODE	NUMBER	REV.	
			SHEET 1 OF 1						
(160,127) JUB08.DRW		17-FEB-80 16:31		NEXT HIGHER ASSEMBLY:		D	CS	L0004-0-8	C
FIRST USED ON OPTION/MODEL: 11/750		B-DD-L0004-0-0							

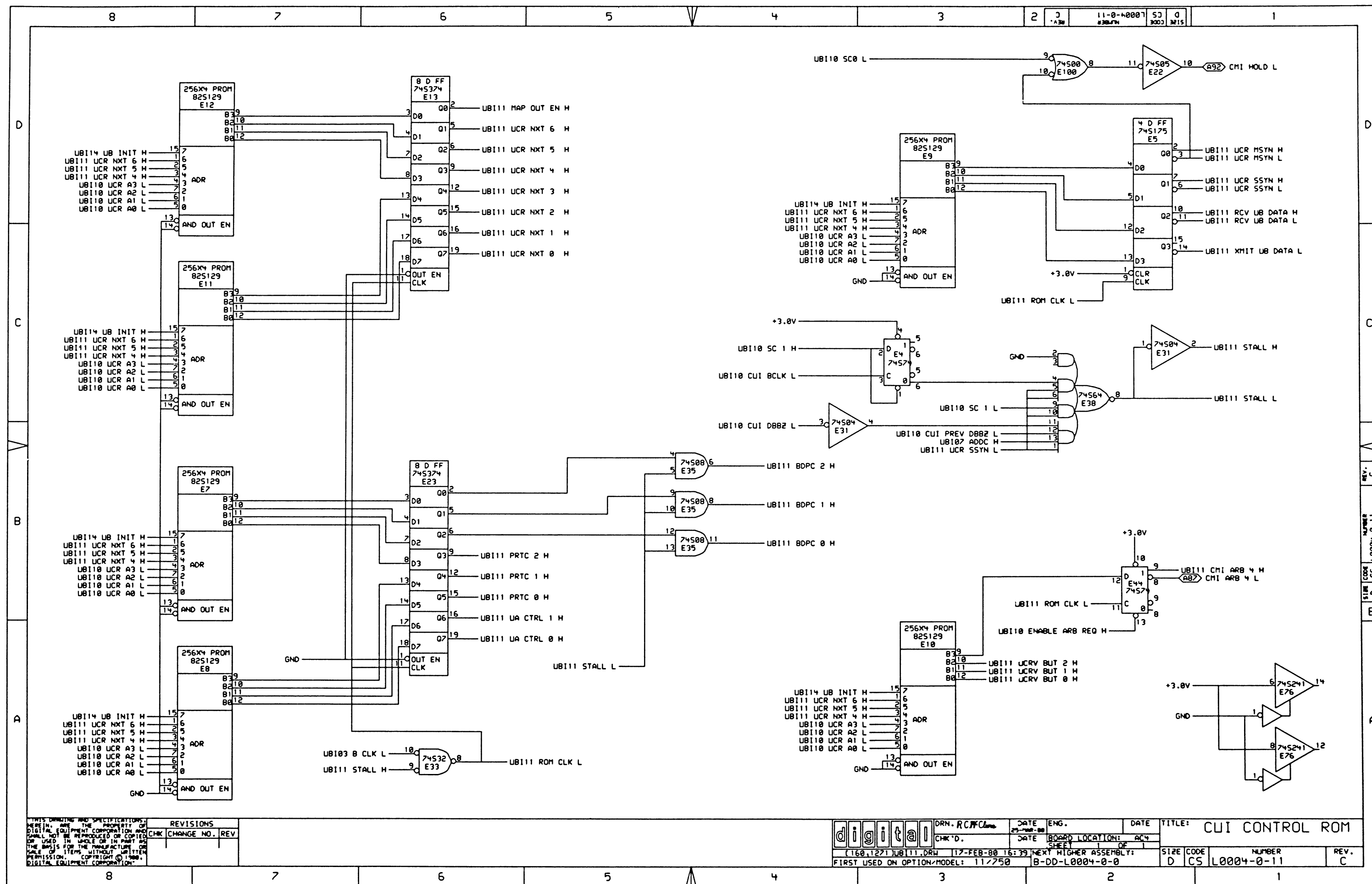


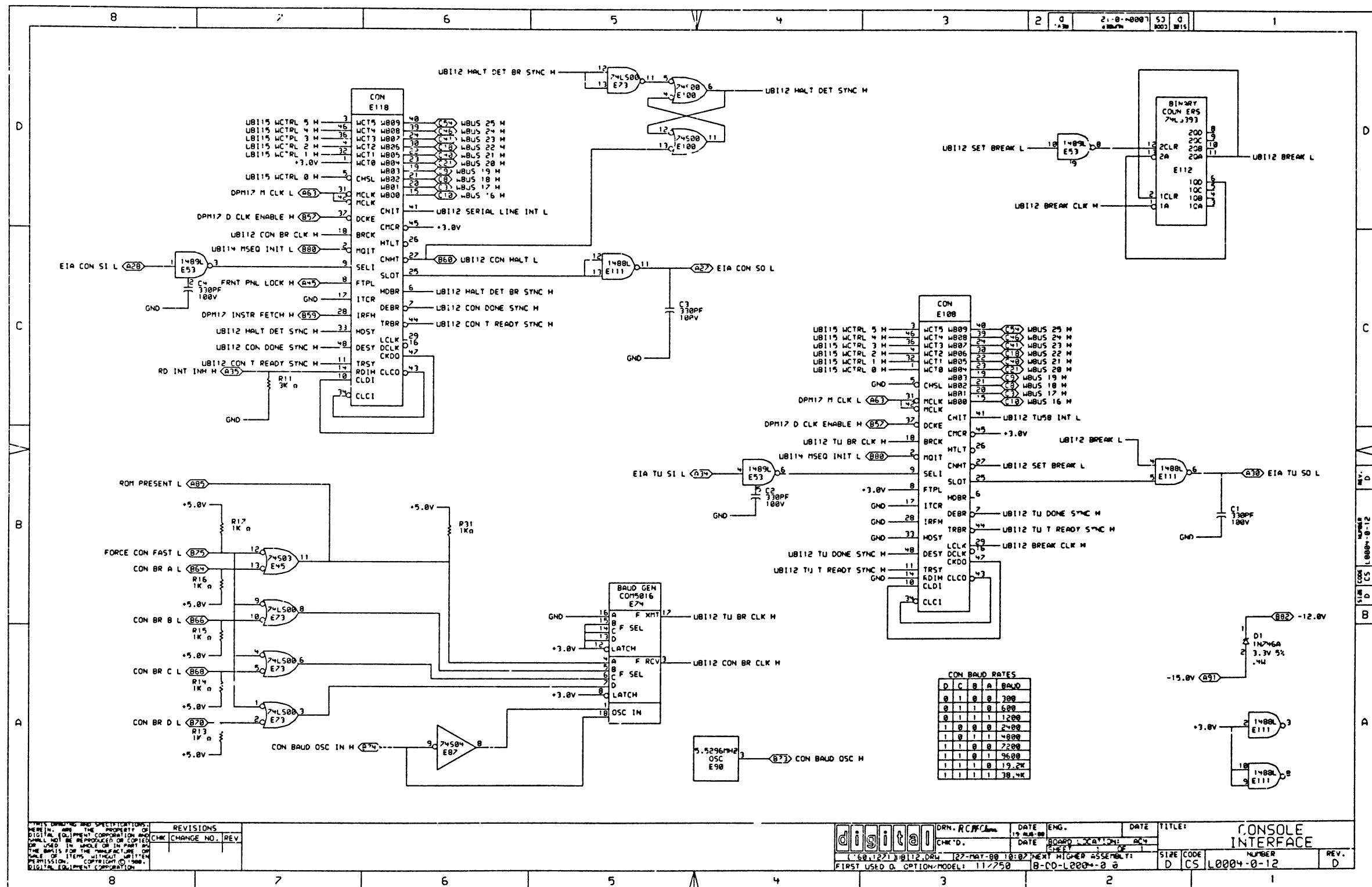
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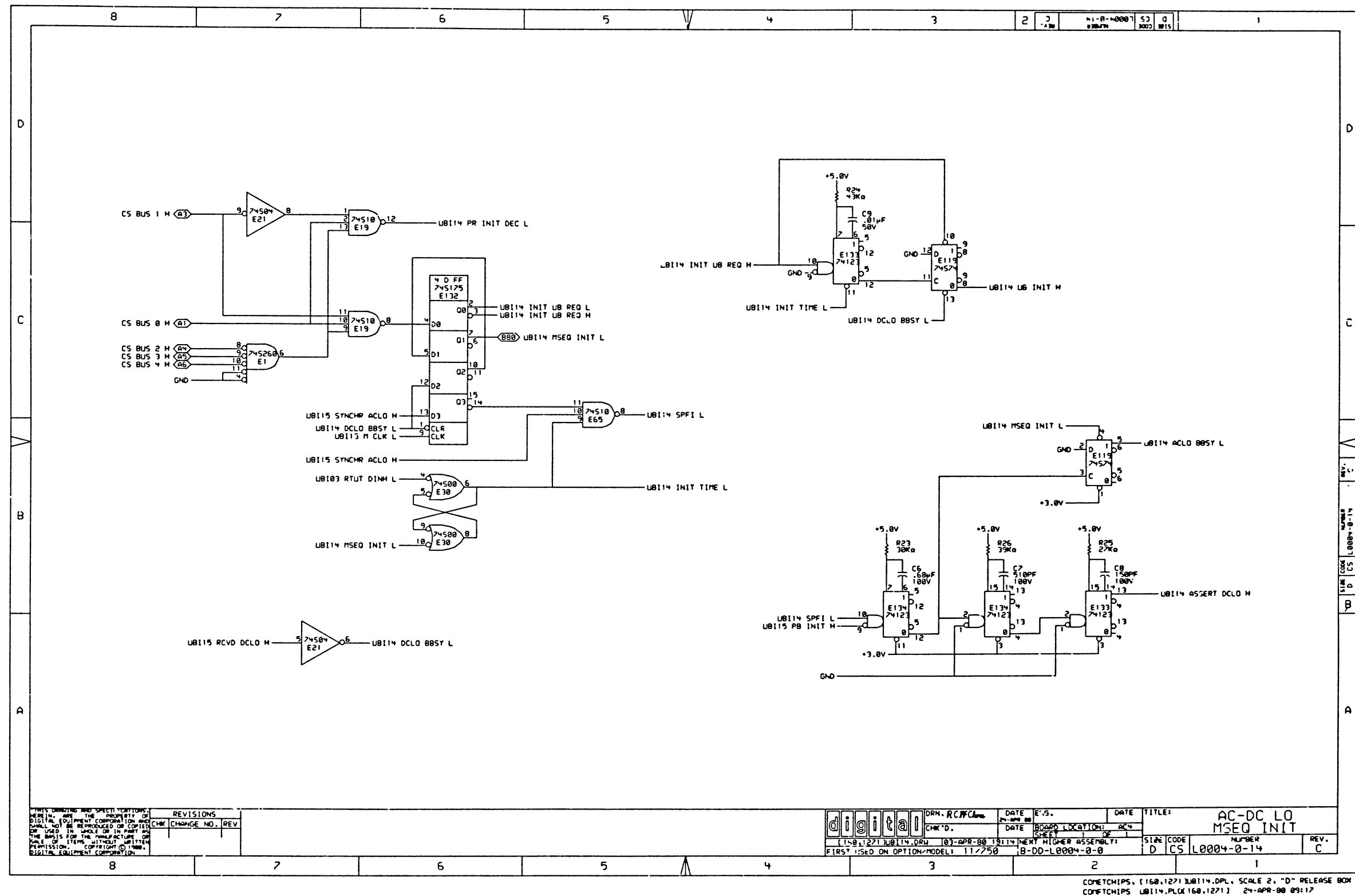
digital	DRN. RCF/CLM	DATE	ENG.	DATE	TITLE: CUI MAP DECODE
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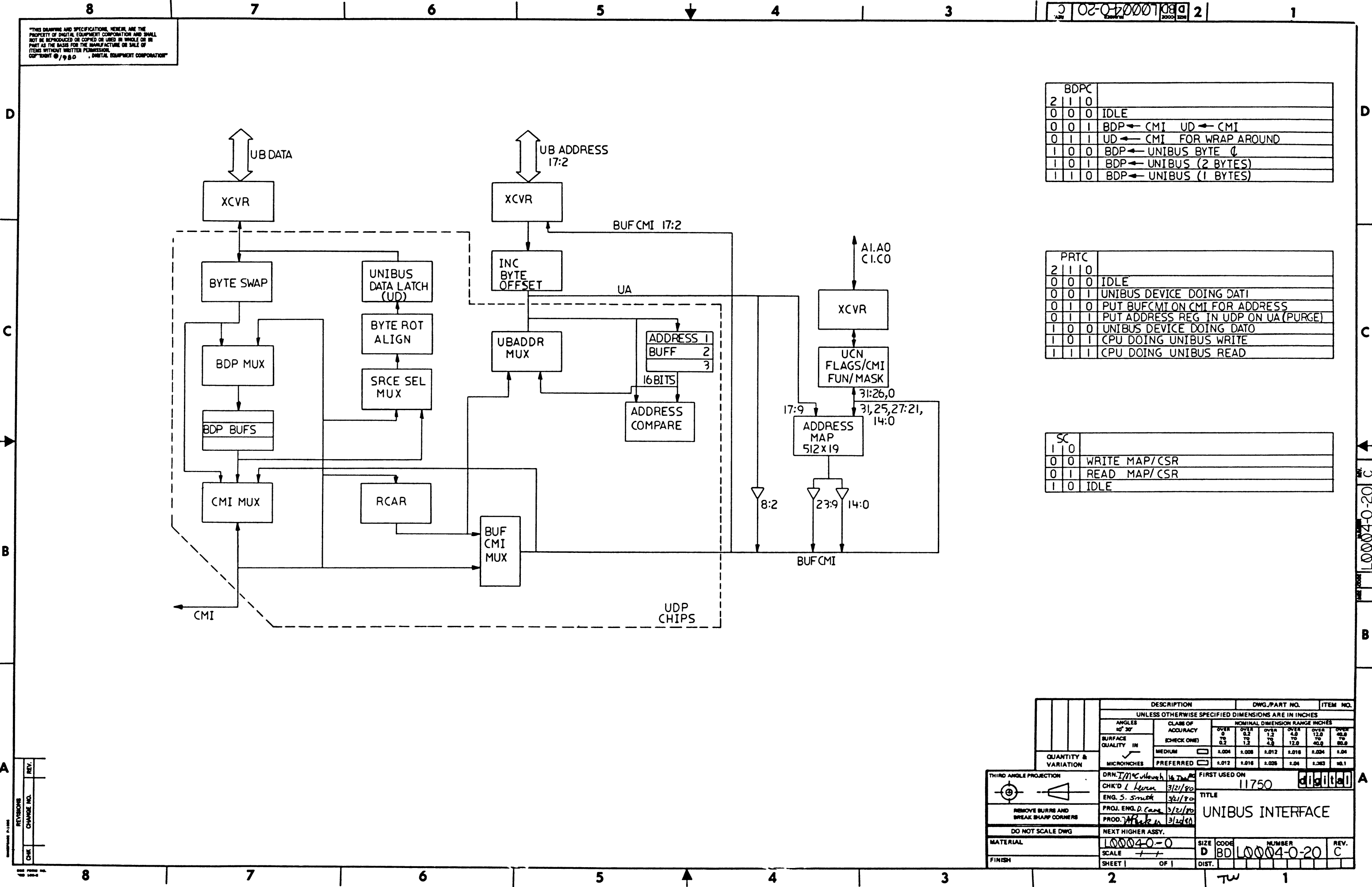
CONETCHIPS, (160,127) RUB112.DPL, SCALE 2, "D" RELEASE BOX
 CONETCHIPS RUB112.DPL(160,127) 19-AUG-88 14:25



COMETCHIPS, (160,1271)UB114.DPL, SCALE 2, "D" RELEASE BOX
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SIGNAL NAME	PAGE NUMBER(S)
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<table><tr><td colspan="2">THIS DRAWING AND SPECIFICATIONS HEREIN ARE THE PROPERTY OF DIGITAL EQUIPMENT CORPORATION AND SHALL NOT BE REPRODUCED OR COPIED OR USED IN WHOLE OR IN PART AS THE BASIS FOR THE MANUFACTURE OR SALE OF ITEMS WITHOUT WRITTEN PERMISSION. COPYRIGHT © 1988 DIGITAL EQUIPMENT CORPORATION</td><td colspan="2">REVISIONS CHK CHANGE NO. REV</td><td colspan="2">digital CHK'D. DR</td><td>DATE 21-MAR-88</td><td>ENG.</td><td>DATE</td><td colspan="2">TITLE: UBI FORWARD REFERENCE</td></tr><tr><td colspan="6">FIRST USED ON OPTION/MODEL: 11/750</td><td colspan="2">NEXT HIGHER ASSEMBLY: B-DD-L0004-0-0</td><td>SIZE D</td><td>CODE CS</td><td>NUMBER L0004-0-19</td><td>REV. C</td></tr></table>												THIS DRAWING AND SPECIFICATIONS HEREIN ARE THE PROPERTY OF DIGITAL EQUIPMENT CORPORATION AND SHALL NOT BE REPRODUCED OR COPIED OR USED IN WHOLE OR IN PART AS THE BASIS FOR THE MANUFACTURE OR SALE OF ITEMS WITHOUT WRITTEN PERMISSION. COPYRIGHT © 1988 DIGITAL EQUIPMENT CORPORATION		REVISIONS CHK CHANGE NO. REV		digital CHK'D. DR		DATE 21-MAR-88	ENG.	DATE	TITLE: UBI FORWARD REFERENCE		FIRST USED ON OPTION/MODEL: 11/750						NEXT HIGHER ASSEMBLY: B-DD-L0004-0-0		SIZE D	CODE CS	NUMBER L0004-0-19	REV. C																																																																																																																																																																																																																																																																																																																																																																																																																																																				
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BDPC			
2	1	0	
0	0	0	IDLE
0	0	1	BDP ← CMI UD ← CMI
0	1	1	UD ← CMI FOR WRAP AROUND
1	0	0	BDP ← UNIBUS BYTE 0
1	0	1	BDP ← UNIBUS (2 BYTES)
1	1	0	BDP ← UNIBUS (1 BYTES)

PRTC			
2	1	0	
0	0	0	IDLE
0	0	1	UNIBUS DEVICE DOING DATA
0	1	0	PUT BUF CMI ON CMI FOR ADDRESS
0	1	1	PUT ADDRESS REG IN UDP ON UA (PURGE)
1	0	0	UNIBUS DEVICE DOING DATA
1	0	1	CPU DOING UNIBUS WRITE
1	1	1	CPU DOING UNIBUS READ

SC			
1	1	0	
0	0	0	WRITE MAP/CSR
0	1	0	READ MAP/CSR
1	0	0	IDLE

REV.	CHANGE NO.
1	1

QUANTITY & VARIATION		DESCRIPTION	DWG./PART NO.		ITEM NO.			
UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES								
SURFACE QUALITY IN	ANGLES NOT 30°	CLASS OF ACCURACY (CHECK ONE)	NOMINAL DIMENSION RANGE INCHES					
	MICROINCHES		OVER 0 TO 0.2	OVER 0.2 TO 1.2	OVER 1.2 TO 4.0	OVER 4.0 TO 12.0	OVER 12.0 TO 40.0	
		MEDIUM <input type="checkbox"/>	2.004	2.008	2.012	2.016	2.024	2.04
		PREFERRED <input type="checkbox"/>	2.012	2.016	2.025	2.04	2.063	2.081
THIRD ANGLE PROJECTION		DRN. J. M. C. 1/16/80		FIRST USED ON				
		CHK'D L. Kern 3/21/80		11750				
REMOVE BURRS AND BREAK SHARP CORNERS		ENG. S. Smith 3/21/80		TITLE				
PROJ. ENG. D. Carr 3/21/80		PROD. M. Baker 3/21/80		UNIBUS INTERFACE				
DO NOT SCALE DWG		NEXT HIGHER ASSY.						
MATERIAL		100040-0		SIZE CODE				
FINISH		SCALE		NUMBER				
		SHEET		REV.				
		OF		BD 100040-0-20 C				
		DIST.						

; UBI .MCP [160,5507] Micro-2.1 1B(40) 0152133 18-Feb-1980 K-MP-L0004-O-21-C Page 1
; Table of Contents
; 1 COMET UNIBUS INTERFACE MICROCODE REV 015 12/19/79
; 33 FIELD DEFINITIONS
; 137 FIRST FORK BREAKOUT
; 231 CPU READS AND WRITES TO THE UNIBUS ARE HANDLED IN THIS SECTION
; 293 DATO THROUGH BUFFERED DATA PATH
; 348 BDP DATI'S
; 454 THIS SECTION HANDLES DATO'S TO THE DDP
; 563 THIS PAGE IS WHERE WE COME FOR DATI'S THROUGH THE DIRECT DATA PATH
; 618 PURGE CODE
; 650 POWER UP CODE

; UBI .MCP [160,5507] Micro-2.1 1B(40) 0152133 18-Feb-1980 Page 2
; UBI .MIC [160,5507] COMET UNIBUS INTERFACE MICROCODE REV 015 12/19/79
; 1 .TOC "COMET UNIBUS INTERFACE MICROCODE REV 015 12/19/79"
; 2
; 3 ;001 7/23/79 DC FIXED DEFINITION OF UA,CTRL,RCV AND RVC,INCR WERE SWAPPED
; 4 ; FIXED DDP AND BDP DATI CODE TO HOLD BYTE 0 OF DATA IN UD
; 5 ; LATCH FOR OFFSET CASE.
; 6 ; ONLY E8,E7 CHANGE
; 7 ;002 7/23/79 DC FIXED SEQUENCE AT BEGINNING OF READ SO AS TO NOT
; 8 ; CAUSE UDP'S TO DRIVE UA BUS. E8,E7 CHANGE
; 9 ;003 7/23/79 PB CHANGED DEFAULT OF UA,CTRL TO "2" TO GO ALONG WITH CHANGE
; 10 ; FOR REV 001.
; 11 ;004 7/24/79 DC ADDED CONSTRAINED WORD IN DDP,DATO WRAP CODE
; 12 ; TO ALLOW U81 TO HOLD DATO DATA ON BUS PROPERLY
; 13 ; ALL ROMS
; 14 ;005 7/24/79 DC FIXED BDP DATI CODE TO HOLD BYTE 0 ON WRAP-CHANGE
; 15 ; TO UBDATA FIELD TO KEEP HI=Z. E9 CHANGES
; 16 ;006 7/24/79 DC FIXED DATOB BDP NO WRITE TO ASSERT SSYN BEFORE
; 17 ; CHECKING TO SEE IF ITS THERE. E12,E11 CHANGE
; 18 ; 6A 8/13 DC CHANGED PFORMAT CONTROL FILE TO BLAST BDPC FIELD LOW TRUE
; 19 ; ONLY E7 CHANGES
; 20 ;007 9/14/79 DC ADDED APB FOR CMI DURING WRAP AROUND READS AND WRITES
; 21 ; TO KEEP CMI DURING BOTH SETS
; 22 ; 7A 9/18/79 DC CHANGED PFORMAT CONTROL TTO BLAST BUT<1> LOW TRUE
; 23 ; ONLY E10 CHANGES
; 24 ;009 9/21/79 CHANGED MAIN,20, ALL ROMS
; 25 ; 9A 9/24/79 DC CHANGED PFORMAT TO BLAST BUT<0> LOW TRUE
; 26 ; E10 CHANGES
; 27 ;010 9/26/79 CHANGED BDP,DATI,50, DDP,40 AND DDP,47 TO BUT ON
; 28 ; SSYN INSTEAD OF MSYN
; 29 ;013 10/9 DC NUMEROUS CHANGES TO FIX UNALIGNED AND PB PROBLEMS
; 30 ;015 12/19/79 DC FIX TO CPU,RO TO PREVENT IT FROM LEAVING
; 31 ; GARBAGE ON THE CMI. E8,E9 CHANGE
; 32

```

;33      .TOC "FIELD DEFINITIONS"
;34      .RTOL
;35      .HEXADECIMAL
;36      ;
;37      ;
;38      ;CODE DIVIDED INTO ROMS AS FOLLOWS:
;39      ;      <23:7> E12
;40      ;      <19:16> F11
;41      ;      <15:12> F7
;42      ;      <11:8> E8
;43      ;      <7:4> E9
;44      ;      <3:0> E10
;45      ;
;46      ;
;47      BUFCMI/= <23:7>, .DEFAULT=0
;48      ADDR=1          ;PUT MAP PFN AND LOW BITS OF UBUS ADDR ON BUFCMI
;49      HI-Z=0
;50
;51      NEXT/= <22:16>, .NEXTADDRESS
;52
;53      BDP/= <15:13>, .DEFAULT=0          ;CONTROLS BDP DATA/ADDR LATCHES
;54      DATI=1          ;BDP<-CMI, UD<-CMI/BDP (NOT BYTE 0 IF BYTE OFFSET),ADDR
;55      DATIW=3         ;UD<-BDP/CMI
;56      DATOW=4         ;BDP BYTE 0<-UNIBUS DATA,ADDR
;57      DATO=5          ;BDP<-UNIBUS DATA (2 BYTES) FUNCTION OF A1,OFFSET,ADDR
;58      DATOB=6         ;BDP<-UNIBUS DATA (1 BYTE) FUNCTION OF A1,OFFSET,ADDR
;59
;60      PRTC/= <12:10>, .DEFAULT=0          ;CONTROLS DATA PORTS ON UDP CHIPS
;61      DATI=1          ;URUS DEVICE DOING DATI(P)
;62      UR,ADDR=2       ;ADDRESS FROM UBUS TO CMI
;63      PURGE,ADDR=3    ;ADDRESS FROM BAR TO CMI
;64      DATO=4
;65      CPU,WRIT=5
;66      CPU,PD=7
;67
;68      UA,CTRL/= <9:8>, .DEFAULT=2          ;CONTROLS UNIBUS ADDRESS XCVRS
;69      XMIT=0          ;DRIVE UNIBUS ADDRESS LINES
;70      HI-Z=1
;71      RCV,INCR=3      ;RECEIVE AND INCREMENT UNIBUS ADDRESS
;72      RCV=2          ;RECEIVE UNIBUS ADDRESS
;73
;74      MSYN/= <7:7>, .DEFAULT=0
;75      ASSERT=1
;76
;77
```

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;78      SSYN/= <6:6>, .DEFAULT=0
;79      ASSERT=1
;80
;81      UBDATA/= <5:4>, .DEFAULT=2          ;CONTROLS URUS DATA XCVRS
;82      RCV=2
;83      DRIVE,UD=1      ;DRIVE UBUS DATA LINES
;84      DRIVE,UD,NOPB=3 ;DRIVE URUS DATA BUT NOT PB LINES
;85      HI-Z=0
;86
;87      CMI,ARB/= <3:3>, .DEFAULT=0
;88      REQUEST=1
;89
;90      RUT/= <2:0>, .DEFAULT=0
;91
;92      EMPTY=1        ;<MSYN, EMPTY PURGE>
;93      ARB=2          ;<MSYN, WON THE BUS L>
;94      SET,FLAG=3     ;<MSYN, WON THE BUS L>, FLAGS<-0001
;95      UB,STATUS=4    ;<MSYN, SSYN OR TIMEOUT>
;96      CLK,FLAGS=5    ;<MSYN, SSYN OR TIMEOUT>, CLOCK FLAGS
;97      CMI,STATUS=6   ;<WRAP L, DBBZ L, NYM L>
;98      FIRST,FORK=7   ;0000 BDP DATOB CMI WRITE NEEDED
;99                   ;0001 BDP DATO CMI WRITE NEEDED
;100                  ;0010 BDP DATOB NO WRITE
;101                  ;0011 BDP DATO NO WRITE, OR WRAP WITH NC MATCH
;102                  ;0100 BDP DATI WRAP 1ST WORD AVAILABLE
;103                  ;0101 BDP DATI DATA AVAILABLE
;104                  ;0110 BDP DATOB OFFSET PUTS IN NEXT LONGWORD
;105                  ;0111 BDP DATI NO DATA AVAILABLE
;106                  ;1000 CPU WRITE
;107                  ;1001 CPU READ
;108                  ;1010 BDP DATO(B)
;109                  ;1011 BDP DATI(P)
;110                  ;1100 PURGE
;111                  ;1101 PURGE
;112                  ;1110 BDP DATOB OFFSET PUTS IN NEXT LONGWORD, OR INT
;113                  ;1111 NOTHING GOING ON
;114
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; UBI .MCR [160,5507] Micro-2.1 1A(40)      0:52:33 18-Feb-1980      K-MP-L0004 -0-21-C      Page 5
; UBI .MIC [160,5507] FIELD DEFINITIONS

;115 REQ,WRT?      "CMI,ARB/REQUEST,PRTC/UB,ADDR,BUT/ARB,BUFCHI/ADDR"
;116 REQ,RD?      "CMI,ARB/REQUEST,PRTC/UB,ADDR,BUT/ARB,UBDATA/HI-Z,BUFCHI/ADDR"
;117 REQ,PUR?      "CMI,ARB/REQUEST,PRTC/PURGE,ADDR,UA,CTRL/HI-Z,BUT/ARB,BUFCHI/ADDR"
;118 EMPTY?      "BUT/EMPTY"
;119 FIRST,FORK?  "BUT/FIRST,FORK,NEXT/MAIN,LOOP"
;120 CMI,STAT?    "BUT/CMI,STATUS"
;121 UB,STAT?     "BUT/UB,STATUS"
;122
;123 SSYN         "SSYN/ASSERT"
;124 MSYN         "MSYN/ASSEPT"
;125 INCR         "UA,CTRL/RCV,INCR"
;126 REQ         "CMI,ARB/REQUEST"
;127
;128 DP_CMI       "BDPC/DATI,PRTC/DATI,UBDATA/HI-Z"
;129 DP_CMI,W     "BDPC/DATI,W,PRTC/DATI,UBDATA/HI-Z,BUFCHI/ADDR"
;130 UB_CMI,WRT   "PRTC/CPU,WRT,UBDATA/DRIVE,UD,UA,CTRL/XMIT"
;131 UB_CMI,WRT,NOPB "PRTC/CPU,WRT,UBDATA/DRIVE,UD,NOPB,UA,CTRL/XMIT"
;132 UB_CMI,ADDR  "PRTC/CPU,RD,UA,CTRL/XMIT"
;133 UB,RD_DP     "PRTC/DATI,UBDATA/DRIVE,UD"
;134 REQ,XTRA?    "CMI,ARB/REQUEST,PRTC/UB,ADDR,BUT/SET,FLAG,UA,CTRL/RCV,INCR,BUFCHI/ADDR"
;135 HOLD,B0     "BDPC/DATI,UBDATA/HI-Z"
;136
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; UBI .MCR [160,5507] Micro-2.1 1B(40)      0:52:33 18-Feb-1980      Page 6
; UBI .MIC [160,5507] FIRST FORK BREAKOUT

;137 .TOC "FIRST FORK BREAKOUT"
;138 #0000
;139 MAIN,LOOP:    ;THIS IS THE TOP OF FIRST FORK
;140 ;0000-----;
;141 BUT/CLK,FLAGS, ;BDP DATOB; CMI WRITE
;142 BDPC/DATOB,
;143 NEXT/BDP,DATO
;144
;145 ;0001-----;
;146 BUT/CLK,FLAGS, ;BDP DATO; CMI WRITE
;147 BDPC/DATO,      ;PUT DATA IN BUFFER
;148 NEXT/BDP,DATO
;149
;150 ;0010-----;
;151 BDPC/DATOB,      ;BDP DATOB,BUFFER NOT FULL
;152 BUT/CLK,FLAGS,
;153 NEXT/BDP,DATO,20
;154
;155 ;0011-----;
;156 BDPC/DATO,        ;BDP DATO,BUFFER NOT FULL
;157 BUT/CLK,FLAGS,   ;OR OFFSET CAUSING WRAPAROUND
;158 NEXT/MAIN,20
;159
;160 ;0100-----;
;161 BDPC/DATI,W,      ;BDP DATI,LONGWORD WRAP
;162 REQ,RD?,INCR,
;163 NEXT/BDP,DATI,30 ;FIRST WORD IS IN THE BUFFER
;164
;165 ;0101-----;
;166 PRTC/DATI,BDPC/DATI,W, ;BDP DATI,DATA AVAILABLE IN
;167 UBDATA/HI-Z,REQ,      ;BUFFER*****
;168 NEXT/BDP,DATI,45
;169
;170 ;0110-----;
;171 BDPC/DATOW,        ;BDP DATOB,OFFSET PUTS BYTE
;172 INCR,BUT/SET,FLAG,  ;IN NEXT LONGGWORD
;173 NEXT/BDP,DATO,20
;174
;175 ;0111-----;
;176 REQ,RD?,           ;BDP DATI,BUFFER EMPTY
;177 NEXT/BDP,DATI,10
;178
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THIS PAGE HOLDS THE SECOND EIGHT PLACES WHERE THE FIRST FORK GOES TO

1000-----;

PRTC/CPU,WRT,

UBDATA/HI-Z,

UA,CTRL/HI-Z,

UB,STAT?,

NEXT/CPU,WRT

1001-----;

PRTC/CPU,RD,

UA,CTRL/HI-Z,

UB,STAT?,

NEXT/CPU,RD

1010-----;

REQ,WRT?,

NEXT/DDP,DATO

1011-----;

REQ,RD?,

NEXT/DDP,DATI

1100-----;

EMPTY?,

NEXT/PURGE

1101-----;

EMPTY?,

NEXT/PURGE

1110-----;

REQ,XTRA?,

NEXT/DDP,DATO,20

1111-----;

FIRST,FORK?

1011-----;

REQ,WRT?,

NEXT/DDP,DATO,05

111-----;

SSYN,NEXT/DDP,45

ICPU DOING WRITE TO UNIBUS

GET READY TO ASSERT STUFF

ON UNIBUS

CHECK SSYN REMOVED FROM UNUS

GO TIME DESKEW INTERVAL

ICPU DOING READ FROM UNIBUS

SEE COMMENTS ABOVE

DDP DATO(B)

DDP DATI

PURGE, CHECK FOR EMPTY

PURGE, CHECK FOR EMPTY

DDP DATOB WRAP TO NEXT LONGWORD

ALSO HERE FOR INTERRUPTS

NOTHING GOING ON, KEEP TRYING

HERE TO SEE IF 10011 WAS NO WRITE OR OFFSET

NOW THAT UDP ADDR IS THERE, TRY AGAIN

WRAP AROUND, WRITE NEEDED

NO WRITE NEEDED

U 008, 1015,04

U 009, 1410,24

U 00A, DC0A,2A

U 00B, F00A,0A

U 00C, 7602,21

U 00D, 7602,21

U 00E, E00B,2B

U 00F, 0002,27

U 012, 1302,26

U 013, C00A,2A

U 017, 6002,60

UBI

.MCR [160,5507]

Micro-2.1 1H(40)

8:52:33 18-Feb-1980

Page 8

UBI

.MIC [160,5507]

CPU READS AND WRITES TO THE UNIBUS ARE HANDLED IN THIS SECTION

231

232

233

234

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261

TOC "CPU READS AND WRITES TO THE UNIBUS ARE HANDLED IN THIS SECTION"

0-----;

UB_CMI,WRT,

NEXT/CPU,WRT,10

1-----;

UB_CMI,WRT,

UB,STAT?,

NEXT/CPU,WRT

-----;

UB_CMI,WRT,NOPB

10-----;

MSYN,UB,STAT?,

UB_CMI,WRT,NOPB,

NEXT/CPU,WRT,20

11-----;

UB_CMI,WRT,NOPB

-----;

UA,CTRL/HI-Z,

NEXT/IDLE

BRANCH COMES HERE IF SSYN NOT ASSERTED

ASSERT ADDRESS AND DATA ON UNIBUS

HERE IF SSYN LEFT ASSERTED FROM

LAST UNBUS TRANSACTION

EATING UP TIME FOR ADDR/DATA

TO MSYN DESKEW

ASSERT MSYN AND WAIT FOR SSYN

MSYN REMOVED,BECAUSE SSYN ARRIVED

PREVENT TRISTATE OVERLAP

U 010, 1614,10

U 011, 1014,14

U 016, 2A14,30

U 02A, 2A14,B4

U 02B, 2014,30

U 020, 0F01,20

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1 UBI .MCR [160,5507] Micro-2, 1 IN(40) 8:52:33 18-Feb-1980 Page 9
1 UBI .MIC [160,5507] CPU READS AND WRITES TO THE UNIBUS ARE HANDLED IN THIS SECTION
K-MP-L0004-O-21-C

1262 ;THIS SECTION FOR CPU READS TO UNIBUS
1263 ;=0
1264 CPU,RD:
1265 ;0-----;
1266 UB_CMI,ADDR, ;SSYN REMOVED FROM LAST TRANSACTION
U 014, 291C,20 1267 NEXT/CPU,RD,10 ;ASSERT ADDRESS AND BEGIN DESKEW TIME
1268
1269 ;1-----;
1270 UB_CMI,ADDR, ;SSYN STILL ASSERTED, DON'T COUNT
U 015, 141C,24 1271 UB_STAT?, ;DESKEW TIME YET
1272 NEXT/CPU,RD
1273
1274 CPU,RD,10:
1275 ;-----;
U 029, 3A1C,20 1276 UB_CMI,ADDR ;EAT 125 FOR DESKEW
1277
1278 ;=10
1279 CPU,RD,20:
1280 ;10-----;
1281 UB_CMI,ADDR, ;ASSERT MSYN AND WAIT FOR SSYN
U 03A, 3A1C,A4 1282 MSYN,UB_STAT?,
1283 NEXT/CPU,RD,20
1284
1285 ;11-----;
U 03B, 381C,A0 1286 UB_CMI,ADDR,MSYN ;KEEP MSYN SO SLAVE HOLDS DATA
1287
1288 ;-----;
1289 PRTC/CPU,WRT, ;KEEP ADDRESS ON UNIBUS
1290 UPDATA/HI-Z,UA,CTRL/XMIT,
U 030, 2014,00 1291 NEXT/CPU,WRT,25
1292

```

```

; UBI      .MCR [160,5507] Micro-2,1 18(40)      8:52:33 18-Feb-1980      Page 10
; UBI      .MIC [160,5507] DATO THROUGH BUFFERED DATA PATH

;293      .IOC "DATO THROUGH BUFFERED DATA PATH"
;294      ;DATO(B) THROUGH BDP THAT NEEDS TO DO A CMI WRITE
;295      =00
;296      BDP,DATO,05;
;297      ;00-----;MSYN DISAPPEARED JUST AS WE WON THE BUS
;298      PRTC/DATO,CMI,STAT?,      ;GO THROUGH WITH IT ANYWAY, WE'RE ALREADY ON BUS
;299      BUFCMI/ADDR,
;300      NEXT/BDP,DATO,10
;301
;302      ;01-----;
;303      NEXT/IDLE      ;LOST MSYN, ABORT
;304
;305      ;10-----;
;306      PRTC/DATO,CMI,STAT?,      ;BUS WON, ASSERT DATA NEXT CYCLE
;307      BUFCMI/ADDR,
;308      NEXT/BDP,DATO,10      ;AND CHECK FOR LONGWORD WRAP&DBBZ
;309      BDP,DATO;
;310      ;11-----;
;311      REQ.WRT?,      ;WAITING TO WIN THE CMI
;312      NEXT/BDP,DATO,05
;313      =000
;314      BDP,DATO,10;
;315      ;THE NEXT FOUR ENTRIES OCCUR FOR THE OFFSET CASE WHERE THE DATA WRAPS
;316      ;000-----;
;317      PRTC/DATO,CMI,STAT?,      ;DBE_ STILL HELD ON BUS, KEEP WAITING
;318      NEXT/BDP,DATO,10
;319
;320      ;001-----;
;321      PRTC/DATO,CMI,STAT?,      ;DBBZ STILL HELD ON BUS, KEEP WAITING
;322      NEXT/BDP,DATO,10
;323
;324      ;010-----;
;325      NEXT/DDP,50      ;NX4
;326
;327      ;011-----;
;328      INCR,DDPC/DATOW,      ;DBZ HAS GONE AWAY, PUT BYTE IN
;329      BUT/SET,LAG,      ;BDP REGS AND SET FLAGS
;330      NEXT/BDP,DATO,20
;331
;332      ;THESE FOUR ARE FOR NO WRAP AROUND
;333      ;100-----;
;334      PRTC/DATO,CMI,STAT?,      ;NO WRAP AROUND, DBBZ STILL ASSERTED
;335      NEXT/BDP,DATO,10      ;SO KEEP THE DATA ON THE BUS
;336
;337      ;101-----;
;338      PRTC/DATO,CMI,STAT?,      ;NO WRAP AROUND, DBBZ STILL ASSERTED
;339      NEXT/BDP,DATO,10      ;SO KEEP THE DATA ON THE BUS
;340
;341      ;110-----;
;342      NEXT/DDP,50      ;NX4
;343
;344      BDP,DATO,20;
;345      ;111-----;
;346      SSYN,NEXT/DDP,45      ;DBBZ WENT AWAY, ASSERT SSYN
;347

```

```

      1348 .TOC "BDP DATI'S"
      1349 ;HERE FOR BDP DATI'S THAT NEED CMI ACTION
      1350 #000
      1351 BDP.DATI.10:
      1352 ;00-----;
      1353 CMI.STAT?, ;GOT THE BUS, JUST AS WE LOST MSYN
      1354 BUFCMI/ADDR,
      1355 NEXT/BDP.DATI.20
      1356
      1357 ;01-----;
      1358 NEXT/IDLE ;LOST MSYN, ABORT
      1359
      1360 ;10-----;
      1361 CMI.STAT?, ;GOT THE BUS ,GET READY FOR DATA
      1362 BUFCMI/ADDR,
      1363 NEXT/BDP.DATI.20
      1364
      1365 ;11-----;
      1366 REQ.RD?, ;WF GOT HERE CAUSE WE DIDN'T WIN THE BUS
      1367 NEXT/BDP.DATI.10
      1368 #000
      1369 BDP.DATI.20:
      1370 ;THESE FOUR ARE FOR THE DATA WRAP AROUND CASE
      1371 ;00-----;
      1372 DP_CMI,CMI.STAT?,REQ, ;DBBZ STILL ASSERTED, KEEP WAITING
      1373 NEXT/BDP.DATI.20
      1374
      1375 ;001-----;
      1376 DP_CMI,CMI.STAT?,REQ, ;DBBZ STILL ASSERTED, KEEP WAITING
      1377 NEXT/BDP.DATI.20
      1378
      1379 ;010-----;
      1380 NEXT/DDP.50 ;DBBZ GONE, NXM STATUS RETURNED
      1381
      1382 ;011-----;
      1383 BDPC/DATI.W, ;DATA IN BUFFER, NOW MOVE TO UD
      1384 UD.DATI/BI=Z,REQ,
      1385 NEXT/BDP.DATI.35
      1386
      1387 ;THESE FOUR ENTRIES ARE FOR NO WRAP-AROUND
      1388 ;100-----;
      1389 DP_CMI,CMI.STAT?, ;DBBZ STILL ASSERTED, KEEP WAITING
      1390 NEXT/BDP.DATI.20,REQ ;*****REMOVE REQ WITH UCN=C
      1391
      1392 ;101-----;
      1393 DP_CMI,CMI.STAT?, ;DBBZ STILL ASSERTED, KEEP WAITING
      1394 NEXT/BDP.DATI.20,REQ ;*****REMOVE REQ WITH UCN=C
      1395
      1396 ;110-----;
      1397 NEXT/DDP.50 ;DBBZ GONE, NXM STATUS RETURNED
      1398
      1399 ;111-----;
      1400 UD.RD_DP,SSYN, ;DBBZ'S GONE, WE GOT THE DATA
      1401 NEXT/BDP.DATI.55,REQ ;*****REMOVE REQ WITH UCN=C
      1402

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      1403 #000
      1404 BDP.DATI.30:
      1405 ;00-----;
      1406 HOLD.B0,CMI.STAT?, ;GOT THE BUS ,GET READY FOR DATA
      1407 BUFCMI/ADDR,INCR,
      1408 NEXT/BDP.DATI.40
      1409
      1410 ;01-----;
      1411 NEXT/IDLE ;LOST MSYN,ABORT
      1412
      1413 ;10-----;
      1414 HOLD.B0,CMI.STAT?, ;GOT THE BUS ,GET READY FOR DATA
      1415 BUFCMI/ADDR,INCR,
      1416 NEXT/BDP.DATI.40
      1417
      1418 BDP.DATI.35:
      1419 ;11-----;
      1420 REQ.RD?,INCR,BDPC/DATI, ;GET THE BUS FOR THE SECOND
      1421 NEXT/BDP.DATI.30
      1422
      1423 #100
      1424 BDP.DATI.40:
      1425 ;100-----;
      1426 DP_CMI,CMI.STAT?,INCR, ;DBBZ STILL ASSERTED, KEEP WAITING
      1427 NEXT/BDP.DATI.40,REQ ;*****REMOVE REQ WITH UCN=C
      1428
      1429 ;101-----;
      1430 DP_CMI,CMI.STAT?,INCR, ;DBBZ STILL ASSERTED, KEEP WAITING
      1431 NEXT/BDP.DATI.40,REQ ;*****REMOVE REQ WITH UCN=C
      1432
      1433 ;110-----;
      1434 NEXT/DDP.50 ;DBBZ GONE, NXM STATUS RETURNED
      1435
      1436 BDP.DATI.45:
      1437 ;111-----;
      1438 UD.RD_DP,SSYN, ;DBBZ'S GONE, WE GOT THE DATA
      1439 NEXT/BDP.DATI.55,REQ ;*****REMOVE REQ WITH UCN=C
      1440
      1441 #10
      1442 BDP.DATI.50:
      1443 ;01-----;
      1444 FIRST,FORK? ;NO MSYN, REMOVE DATA AND SSYN
      1445
      1446 BDP.DATI.55:
      1447 ;11-----;
      1448 UD.RD_DP,
      1449 SSYN,UD.STAT?, ;WAIT FOR MSYN TO GO AWAY
      1450 NEXT/BDP.DATI.50,REQ ;*****REMOVE REQ WITH UCN=C
      1451
      1452
      1453

```

; UBI .MCR [160,5507] Micro-2.1 18(40) 8:52:33 18-Feb-1980
; UBI .MIC [160,5507] THIS SECTION HANDLES DATO'S TO THE DDP

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1454 .TOC "THIS SECTION HANDLES DATO'S TO THE DDP"
1455 #00
1456 DDP,DATO:
1457 ;00-----;
1458 PRTC/DATO,BUFCHI/ADDR, ;WE GOT IT
1459 CMI,STAT?,
1460 NEXT/DDP,DATO,10
U 05C, 8012,26
1461
1462 ;11-----;
1463 NEXT/IDLF ;MSYN DISAPPEARED
1464
1465 ;10-----;
1466 PRTC/DATO,BUFCHI/ADDR, ;WE GOT IT
1467 CMI,STAT?,
1468 NEXT/DDP,DATO,10
U 05E, 8012,26
1469
1470 ;11-----;
1471 REQ,4R??, ;TRYING TO GET THE BUS
1472 NEXT/DDP,DATO
U 05F, DC0A,2A
1473
1474 #000
1475 DDP,DATO,10:
1476 ;THE FOUR CASES ARE FOR THE WRAPAROUND SITUATION
1477
1478 ;000-----;
1479 PRTC/DATO,CMI,STAT?, ;WAITING FOR DBBZ
1480 REQ,NEXT/DDP,DATO,10
U 030, 3012,2E
1481
1482 ;001-----;
1483 PRTC/DATO,CMI,STAT?, ;WAITING FOR DBBZ
1484 REQ,NEXT/DDP,DATO,10
U 031, 3012,2E
1485
1486 ;010-----;
1487 NEXT/DDP,50 ;NXM STATUS
1488
1489 ;011-----;
1490 REQ,XTRA?, ;DONE WITH THE FIRST, DO THE SECOND
1491 NEXT/DDP,DATO,20
U 033, E008,2B
1492
1493 ;THESE CASES ARE FOR NO WRAP
1494 ;100-----;
1495 PRTC/DATO,CMI,STAT?, ;WAITING FOR DBBZ
1496 NEXT/DDP,DATO,10
U 034, 3012,26
1497
1498 ;101-----;
1499 PRTC/DATO,CMI,STAT?, ;WAITING FOR DBBZ
1500 NEXT/DDP,DATO,10
U 035, 3012,26
1501
1502 ;110-----;
1503 NEXT/DDP,50 ;NXM STATUS
1504
1505 ;111-----;
1506 SSYN,NEXT/DDP,45 ;DONE
1507
```

; UBI .MCR [160,5507] Micro-2.1 18(40) 8:52:33 18-Feb-1980
; UBI .MIC [160,5507] THIS SECTION HANDLES DATO'S TO THE DDP

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1508 ;THIS PAGE CONTINUES THE DDP DATO WRAP CASE CODE,
1509 ;AND ALSO HAS THE WAITING FOR MSYN TO GO AWAY STUFF
1510 #00
1511 DDP,DATO,20:
1512 ;00-----;
1513 PRTC/DATO,BUT/SET,FLAG, ;BUS WON
1514 INCR,BUFCHI/ADDR,
1515 NEXT/DDP,DATO,25
U 060, E713,23
1516
1517 ;01-----;
1518 NEXT/IDLE ;MSYN WENT AWAY
1519
1520 ;10-----;
1521 PRTC/DATO,BUT/SET,FLAG, ;BUS WON
1522 INCR,BUFCHI/ADDR,
1523 NEXT/DDP,DATO,25
U 062, E713,23
1524
1525 ;11-----;
1526 REQ,XTRA?, ;TRYING TO GET THE BUS
1527 NEXT/DDP,DATO,20
U 063, E00B,2B
1528
1529 #11
1530 DDP,DATO,25:
1531 ;11-----;
1532 PRTC/DATO,CMI,STAT?, ;THIS IS CONSTRAINED AS TARGET OF SET,FLAG
1533
1534 #100
1535 DDP,DATO,30:
1536 ;100-----;
1537 PRTC/DATO,CMI,STAT?, ;WAITING FOR NO DBBZ
1538 NEXT/DDP,DATO,30
U 03C, 3C12,26
1539
1540 ;101-----;
1541 PRTC/DATO,CMI,STAT?, ;WAITING FOR NO DBBZ
1542 NEXT/DDP,DATO,30
U 03D, 3C12,26
1543
1544 ;110-----;
1545 NEXT/DDP,50 ;NXM
1546
1547 ;111-----;
1548 SSYN,NEXT/DDP,45 ;DONE
1549
1550 #10
1551 DDP,40:
1552 ;10-----;
1553 FIRST,FORK? ;NO MSYN, REMOVE DATA AND SSYN
1554
1555 DDP,45:
1556 ;11-----;
1557 SSYN,UB,STAT?, ;WAITING FOR MSYN OR INT TO GO AWAY
1558 NEXT/DDP,40 ;BY SEEING IF SSYN GOT CLEARED
1559
1560 #10
1561 DDP,47:
1562 ;01-----;
1563 FIRST,FORK? ;NO MSYN, REMOVE DATA AND SSYN
1564
1565 DDP,50:
1566 ;11-----;
1567 UB,STAT?,NEXT/DDP,47 ;WAITING FOR MSYN TO GO AWAY
1568
```

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;563 .TOC "THIS PAGE IS WHERE WE COME FOR DATI'S THROUGH THE DIRECT DATA PATH"
;564
;565 EQU
;566 DDP,DATI:
;567 ;00-----;
;568 CMI,STAT?, ;GOT THE BUS,WAIT FOR DATA
;569 BUFCMI/ADDR,
;570 NEXT/ODP,DATI,10
U 070, C002,26 ;571
;572 ;01-----;
;573 NEXT/IDLE ;LOST MSYN
;574
;575 ;10-----;
;576 CMI,STAT?, ;GOT THE BUS,WAIT FOR DATA
;577 BUFCMI/ADDR,
;578 NEXT/ODP,DATI,10
U 072, C002,26 ;579
;580 ;11-----;
;581 REQ,RD?, ;TRY TO GET THAT BUS
;582 NEXT/ODP,DATI
;583
;584 EQU
;585 DDP,DATI,10:
;586 ;THESE FOUR ARE FOR THE WRAP CASE
;587 ;000-----;
;588 DP_CMI.W,CMI,STAT?,REQ, ;WAITING FOR DATA
;589 NEXT/ODP,DATI,10
U 040, C066,0E ;590
;591 ;001-----;
;592 DP_CMI.W,CMI,STAT?,REQ, ;WAITING FOR DATA
;593 NEXT/ODP,DATI,10
U 041, C066,0E ;594
;595 ;010-----;
;596 NEXT/ODP,50 ;NXM
;597
;598 ;011-----;
;599 REQ,RD?,INCR,BDPC/DATI, ;WE GOT THE FIRST, NOW DO THE SECOND
;600 NEXT/BDP,DATI,30
U 043, D42B,0A ;601
;602 ;100-----;
;603 DP_CMI.W,CMI,STAT?, ;WAITING FOR DATA, NO WRAP-AROUND
;604 NEXT/ODP,DATI,10,REQ ;*****REMOVE REQ WITH UCN-C
;605
;606 ;101-----;
;607 DP_CMI.W,CMI,STAT?, ;WAITING FOR DATA, NO WRAP-AROUND
;608 NEXT/ODP,DATI,10,REQ ;*****REMOVE REQ WITH UCN-C
;609
;610 ;110-----;
;611 NEXT/ODP,50 ;NXM, WAIT THE MSYN OUT
;612
;613 ;111-----;
;614 UN,RD_OP, ;DBBZ WENT AWAY
;615 SSYN, ;GIVE THE UBUS DATA, AND ISSUE SSYN
;616 NEXT/BDP,DATI,55,REQ ;*****REMOVE REQ WITH UCN-C
;617

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;618 .TOC "PURGE CODE"
;619 ;THIS PAGE HANDLES PURGES
;620
;621 EQU
;622 PURGE:
;623 ;10-----;
;624 REQ,PUR?, ;NOT EMPTY IF WE GET HERE
;625 NEXT/PURGE,10
U 076, FA0D,2A ;626
;627 ;1-----;
;628 NEXT/IDLE ;IF PUREG WAS EMPTY WE CLEARED IT
;629
;630 EQU
;631 PURGE,10:
;632 ;10-----;
;633 PRTC/DATO,UA,CTRL/HI=Z,
;634 BUFCMI/ADDR,
;635 NEXT/PURGE,20
U 07A, CD11,20 ;636
;637 ;11-----;
;638 REQ,PUR?,
;639 NEXT/PURGE,10
U 07B, FA0D,2A ;640
;641 EQU
;642 PURGE,20:
;643 ;101-----;
;644 PRTC/DATO,CMI,STAT?, ;DO WRITE AND WAIT FOR NO DBBZ
;645 NEXT/PURGE,20
U 04D, 4D12,26 ;646
;647 ;111-----;
;648 NEXT/IDLE ;ALL DONE
;649

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[illegible]

Page 20

[illegible]

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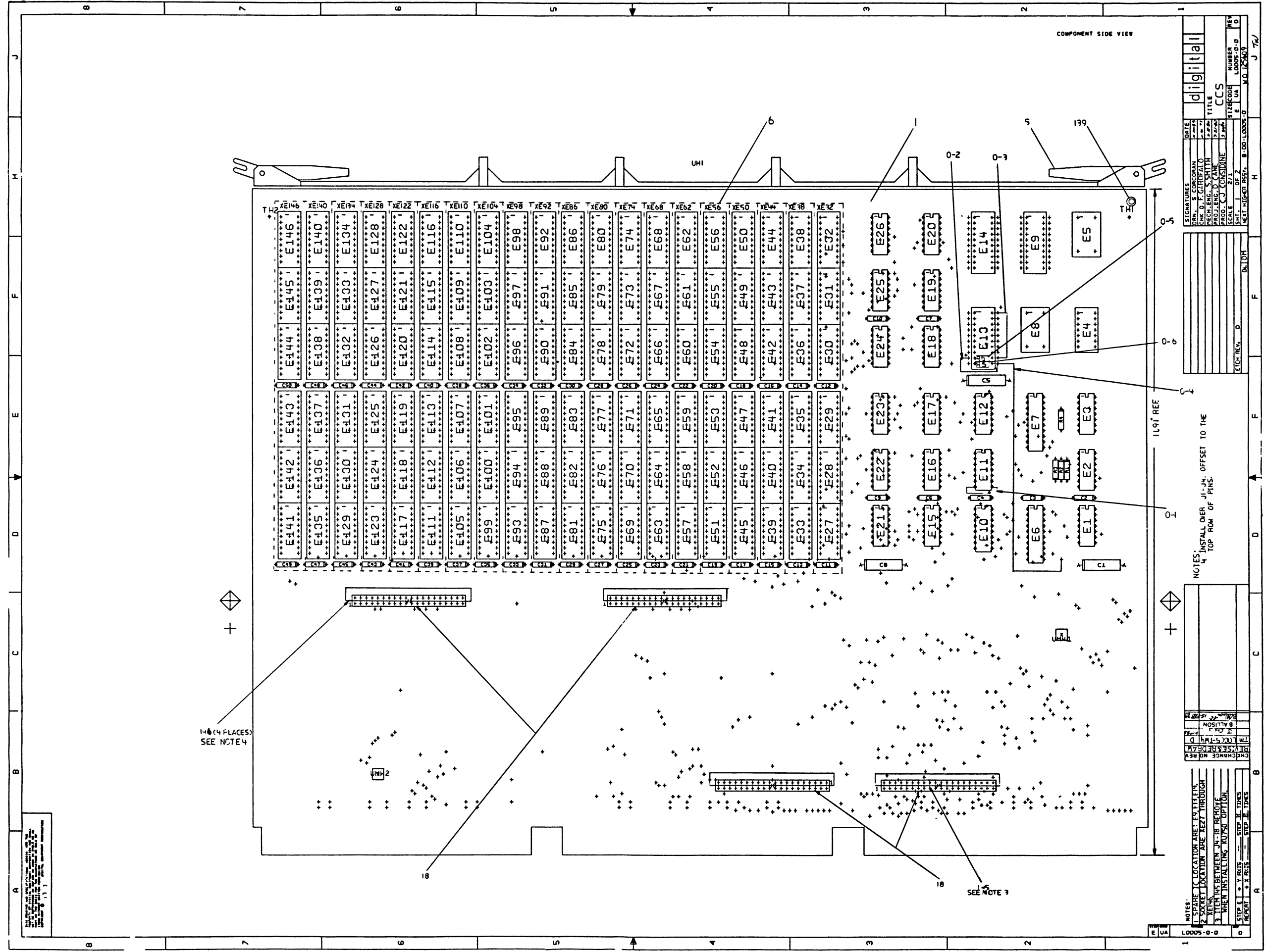
UBI	MCR [160,5507] Micro-2.1 1B(40)	R:52:33 1B-1980	Page 22
	Cross Reference Listing -	Field Names and Defined Values	
BDPC	53 #		
	DATI 54 # 372	376 389 393 407 415 421 427 431 # 599 #	
	DATI 55 # 161	166 383 588 592 603 607	
	DATO 57 # 147	156	
	DATOB 58 # 142	151	
	DATOW 56 # 171	328	
BUFCMI	47 #		
	ADDR 48 # 162	176 195 199 211 225 299 307 311 # 354 # 362	
	366 408 416 421 458 466 471 490 514 # 522 # 526 569		
	577 581 588 592 599 603 607 624 # 634 # 638		
BUT	HI=Z 49 #		
	90 #		
	ARB 93 # 162	176 195 199 225 311 366 421 471 # 581 # 599	
	624 638		
	CLK,FLAGS 96 # 141	146 152 157	
	CMI,STATUS 97 # 221	298 306 317 321 334 338 353 361 # 372 # 376	
	339 393 407 415 427 431 459 467 479 # 483 # 495 499		
	531 535 539 568 576 588 592		
	EMPTY 92 # 203	207	
	FIRST,FORK 98 # 216	445 550 558	
	SET,FLAG 94 # 172	211 329 490 513 521 526	
	UR,STATUS 95 # 185	191 240 250 271 282 450 553 561 #	
CMI,ARB	87 #		
	REQUEST 88 # 162	167 176 195 199 211 225 311 366 # 372 # 376	
	384 390 394 401 421 428 432 440 451 # 471 # 480 484		
	490 526 581 588 592 599 604 608 # 616 # 624 638		
MSYN	74 #		
	75 # 250	282 286	
NEXT	51 #		
	BDP,DATI,10 177 351 # 367		
	BDP,DATI,20 355 363 369 # 373 377 390 394		
	BDP,DATI,30 163 405 # 422 600		
	BDP,DATI,35 385 419 #		
	BDP,DATI,40 409 417 425 # 428 432		
	BDP,DATI,45 168 437 #		
	BDP,DATI,50 443 # 451		
	BDP,DATI,55 401 440 447 # 616		
	BDP,DATO 143 148 309 #		
	HDP,DATO,05 226 296 # 312		
	BDP,DATO,10 300 308 314 # 318 322 335 339		
	BDP,DATO,20 153 173 330 344 #		
	CPU,PD 192 264 # 272		
	CPU,RD,10 267 274 #		
	CPU,RD,20 279 # 283		
	CPU,WRT 186 233 # 241		
	CPU,WRT,10 236 243 #		
	CPU,WRT,20 248 # 252		
	CPU,WRT,25 257 # 291		
	DDP,40 548 # 554		
	DDP,45 279 346 506 546 551 #		
	DDP,47 556 # 561		
	DDP,50 325 342 380 397 435 487 503 543 559 # 596 # 611 #		
	DDP,DATI 200 566 # 582		
	DDP,DATI,10 570 578 585 # 589 593 604 608		
	DDP,DATO 196 456 # 472		

PRTC	DDP,DATA,10	460	468	475 #	480	484	496	500						
	DDP,DATA,20	212	491	511 #	527									
	DDP,DATA,25	515	523	529 #										
	DDP,DATA,30	513 #	536	540										
	IDLE	214 #	260	303	358	412	463	510	573	628	648 #	653 #	654	
		655	656	657	658	659	660	661	662	663 #	664 #	665	666	
		667	668	669	670	671	672	673	674 #	675 #	676	677	678	
		679	680	681	682	683	684	686 #	687 #	688	689	690	691	
		692	693	694	695	696	697 #	698 #	699	700	701	702	703	
		704	705	706	707	708 #	709 #	710	711	712	713	714	715	
		716	717	719	720 #	721 #	722	723	724	725	726	727	728	
		729	730	731 #	732 #	733	734	735	736	737	738	739	740	
		741	742 #	743 #	744	745	746	747	748	749	750	752	753	
		754 #	755 #	756	757	758	759	760	761	762	763	764	765 #	
		766 #	767	768	769	770	771	772	773	774	775	776 #	777 #	
		778	779	780	781	782	783							
		MAIN,20	158	219 #										
		MAIN,LOOP	139 #	216	445	550	558							
		PURGE	204	208	622 #									
		PURGE,10	625	631 #	639									
		PURGE,20	635	642 #	645									
			60 #											
		CPU,RD	66 #	189	266	270	276	281	286					
		CPU,WRT	65 #	182	235	239	245	251	255	289				
		DATI	61 #	166	372	376	389	393	400	427	431	439 #	449 #	588
		592	603	607	614									
	DATO	64 #	298	306	317	321	334	338	458	466	479 #	483 #	495	
		499	513	521	531	535	539	633	644					
	PURGE,ADDR	63 #	624	638										
	UB,ADDR	62 #	162	176	195	199	211	225	311	366	421 #	471 #	490	
		526	581	599										
SSYN		78 #												
UA.CTRL	ASSERT	79 #	229	346	400	439	450	506	546	553	615 #			
		68 #												
	HI-Z	70 #	184	190	259	624	633	638						
	RCV	72 #												
	RCV,INCR	71 #	162	172	211	328	408	416	421	427	431 #	490 #	514	
		522	526	599										
	XMIT	69 #	235	239	245	251	255	266	270	276	281 #	286 #	290	
UBDATA		81 #												
	DRIVE,UD	83 #	235	239	400	439	449	614						
	DRIVE,UD,NOPB	84 #	245	251	255									
	HI-Z	85 #	162	167	176	183	199	290	366	372	376 #	384 #	389	
		393	407	415	421	427	431	581	588	592 #	599 #	603	607	
	RCV	82 #												

CMI,STAT?	120 #	298	306	317	321	334	338	353	361	372 #	376 #	389
	393	407	415	427	431	459	467	479	483 #	495 #	499	531
	535	539	568	576	588	592	603	607 #	644 #			
DP_CMI	128 #	372	376	389	393	427	431					
OP_CMI.W	129 #	588	592	603	607							
EMPTY?	118 #	203	207									
FIRST.FORK?	119 #	216	445	550	558							
HOLD.80	135 #	407	415									
INCR	125 #	162	172	328	408	416	421	427	431	514 #	522 #	599
MSYN	124 #	250	282	286								
REQ	126 #	167	372	376	384	390	394	401	428	432 #	440 #	451
	480	484	588	592	604	608	616					
REQ.PUR?	117 #	624	638									
REQ.RD?	116 #	162	176	199	366	421	581	599				
REQ.WRT?	115 #	195	225	311	471							
REQ.XTRA?	134 #	211	490	526								
SSYN	123 #	229	346	400	439	450	506	546	553	615 #		
UB.RD_DP	133 #	400	439	449	614							
UB,STAT?	121 #	185	191	240	250	271	282	450	553	561 #		
UB_CMI.ADDR	132 #	266	270	276	281	286						
UB_CMI.WRT	130 #	235	239									
UB_CMI.WRT.NOPB	131 #	245	251	255								

; UBI		.MCR [160,5507] Micro-2.1 1B(40)		8:52:33 18-Feb-1980		K-MP -L0004 -O-21 -C		Page 25	
;		Location / Line		Number		Index			
U 000	143	148	153	158	163	168	173	177	
U 008	186	192	196	200	204	208	212	216	
U 010	236	241	221	226	267	277	245	229	
U 018	318	322	325	330	335	339	342	346	
U 020	373	377	380	385	390	394	397	401	
U 028	260	276	252	255	428	432	435	440	
U 030	480	484	487	491	496	500	503	506	
U 038	291		283	286	536	540	543	546	
U 040	589	593	596	600	604	608	611	616	
U 048	300	303	308	312		645		648	
U 050	355	358	363	367	409	412	417	422	
U 058			445	451	460	463	468	472	
U 060	515	518	523	527				531	
U 068			550	554			558	561	
U 070	570	573	578	582			625	628	
U 078			635	639					
U 080	653	654	655	656	657	658	659	660	
U 088	661	662	663	664	665	666	667	668	
U 090	669	670	671	672	673	674	675	676	
U 098	677	678	679	680	681	682	683	684	
U 0A0	686	687	688	689	690	691	692	693	
U 0A8	694	695	696	697	698	699	700	701	
U 0B0	702	703	704	705	706	707	708	709	
U 0B8	710	711	712	713	714	715	716	717	
U 0C0	719	720	721	722	723	724	725	726	
U 0C8	727	728	729	730	731	732	733	734	
U 0D0	735	736	737	738	739	740	741	742	
U 0D8	743	744	745	746	747	748	749	750	
U 0E0	752	753	754	755	756	757	758	759	
U 0E8	760	761	762	763	764	765	766	767	
U 0F0	768	769	770	771	772	773	774	775	
U 0F8	776	777	778	779	780	781	782	783	

; UBI		.MCR [160,5507] Micro-2.1 1B(40)		8:52:33 18-Feb-1980		Page 26	
;		Error Summary					
Memory	No. Microwords	High Addr					
U	236	255					
Total number of microwords used:		236					
Highest address(decimal):		255					
Pass 1 warnings detected:		0		Pass 2 warnings detected:		0	
Pass 1 errors detected:		0		Pass 2 errors detected:		0	



REWORK INSTRUCTIONS

REWORK INSTRUCTIONS

WIRE ADDS SIDE 1:

- O-1 FROM E11-7 TO E11-8
- O-2 FROM E13-9 TO E13-11
- O-3 FROM E13-12 TO E13-20
- O-4 FROM E13-11 TO RIGHT OF E6-11

COMPONENT ADDS SIDE 1:

- O-5 ADD R6 FROM E13-9 TO E13-12
- C-6 ADD R5 FROM E13-10 TO E13-11

CCS

UA L0005-0-0

D

LINE	ITEM	DOCUMENT NUMBER	PART NUMBER	DESCRIPTION	QTY PER VARIATION 00	REFERENCE DESIGNATOR
1	1	E-MD-5013516-0-0	5013516-00	ETCH CIRCUIT BOARD C.S.	1	
2	2	SEE NOTES	1012084-01	8 MFD 25V +75-10% AL EL	3	C1,C5,C8
3	3		1012784-00	.047 MFD 50V +80-20% CER	7	C2,C3,C4,C6,C7,C9,C10
4	4		1017897-00	.22 MFD 50V +80-20% CER	40	C11-C50
5	5		1216988-02	HANDLE,MODULE,HEX TWO EJECTORS	1	
6	6		1215006-03	SKT,IC 18PIN DIP TIN PLATE	120	XE27-XE146
7	7		1300316-00	470.0 .25 W 5.0 % CC	3	R1,R2,R4
8	8		1301424-00	680.0 .25 W 5.0 % CC	1	R3
9	9		1910533-00	74S03 NAND GATE-QUAD 2IN,0	1	E3
10	10		1910534-00	74S04 INVERTER GATE-HEX 1I	4	E12,E15,E18,E20
11	11		1910544-00	74S74 FF-D DUAL,EDGE TRIGG	1	E2
12	12		1911573-00	74S280 PARITY GEN/CHKR,9BIT	1	E11
13	13		1911675-00	74S138 DECODER/DEMUX 3-8 LIN	1	E10
14	14		1912746-00	DEC 74S37 NAND GATE-QUAD 2IN	9	E16,E17,E19,E21-E26
15	15		1913340-00	74S32 OR GATE-QUAD 2IN	1	E1
16	16		1913462-00	74S240 OCTAL BUFFER,INVERTI	1	E7
17	17		1913671-00	74S374 FF-D OCTAL TRISTATE	1	E6
18	18		1217293-00	PIN,SQUARE ON NYLON STRIP	160	
19	19		23821F1-00	F1-01	1	E27
20	20		23822F1-00	F1-01	1	E28
21	21		23823F1-00	F1-01	1	E29
22	22		23824F1-00	F1-01	1	E30
23	23		23825F1-00	F1-01	1	E31
24	24		23826F1-00	F1-01	1	E32
25	25		23827F1-00	F1-01	1	E33
26	26		23828F1-00	F1-01	1	E34
27	27		23829F1-00	F1-01	1	E35
28	28		23830F1-00	F1-01	1	E36
29	29		23831F1-00	F1-01	1	E37
30	30		23832F1-00	F1-01	1	E38

REVISION HISTORY		BASIC PART NO: L0005		DRN:	K.FRIEDGEN	DATE: 17-MAY-79	DIGIT - A L			
ENG	ECO NUMBER	REV	SECTION A OF A	CHK'D: E.T.GERRY		DATE: 17-MAY-79	TITLE PARTS LIST			
---	INITIAL	IC	SECTION.VARIATION INDEX	DES.ENG: S.SMITH		DATE: 17-MAY-79	CCS			
S	!TW001	!D	! [A] 00							
D	!TW002	!E	! [B]							
G	!L0005-TW003	!F	! [C]							
			! [D]							
			! [E]							
			! [F]	RESP.ENG.: S.SMITH		DATE: 17-MAY-79	DOCUMENT NUMBER			
			! [H]							
			! [J]				SIZE	CODE	NUMBER	REV
			! [K]	MFG.ENG.: K.O'BRIEN		DATE: 8-FEB-80	K	PL	L0005-0-DBP	F
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			! [N]	!E-UA-L0005-0-0		!#B-DD-L0005-0	! Z1260.PLS		! 29	

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AUTOMATED BY FRTLST.3L(31)

PARTS LIST

SHEET A2 OF A4

LINE	ITEM	DOCUMENT	NUMBER
------	------	----------	--------

PART NUMBER

DESCRIPTION

QTY PER VARIATION
00

REFERENCE DESIGNATOR

31	31	23833F1-00	F1-01	1	E39
32	32	23834F1-00	F1-01	1	E40
33	33	23835F1-00	F1-01	1	E41
34	34	23836F1-00	F1-01	1	E42
35	35	23837F1-00	F1-01	1	E43
36	36	23838F1-00	F1-01	1	E44
37	37	23839F1-00	F1-01	1	E45
38	38	23840F1-00	F1-01	1	E46
39	39	23841F1-00	F1-01	1	E47
40	40	23842F1-00	F1-01	1	E48
41	41	23718F1-00	F1-01	1	E49
42	42	23843F1-00	F1-01	1	E50
43	43	23504F1-00	F1-01	1	E51
44	44	23505F1-00	F1-01	1	E52
45	45	23506F1-00	F1-01	1	E53
46	46	23507F1-00	F1-01	1	E54
47	47	23719F1-00	F1-01	1	E55
48	48	23844F1-00	F1-01	1	E56
49	49	23845F1-00	F1-01	1	E57
50	50	23511F1-00	F1-01	1	E58
51	51	23720F1-00	F1-01	1	E59
52	52	23846F1-00	F1-01	1	E60
53	53	23721F1-00	F1-01	1	E61
54	54	23847F1-00	F1-01	1	E62
55	55	23848F1-00	F1-01	1	E63
56	56	23849F1-00	F1-01	1	E64
57	57	23786F1-00	F1-01	1	E65
58	58	23850F1-00	F1-01	1	E66
59	59	23724F1-00	F1-01	1	E67
60	60	23851F1-00	F1-01	1	E68
61	61	23852F1-00	F1-01	1	E69
62	62	23778F1-00	F1-01	1	E70
63	63	23726F1-00	F1-01	1	E71
64	64	23853F1-00	F1-01	1	E72
65	65	23727F1-00	F1-01	1	E73
66	66	23854F1-00	F1-01	1	E74
67	67	23855F1-00	F1-01	1	E75
68	68	23779F1-00	F1-01	1	E76
69	69	23856F1-00	F1-01	1	E77
70	70	23857F1-00	F1-01	1	E78
71	71	23732F1-00	F1-01	1	E79
72	72	23858F1-00	F1-01	1	E80
73	73	23859F1-00	F1-01	1	E81
74	74	23535F1-00	F1-01	1	E82
75	75	23733F1-00	F1-01	1	E83
76	76	23860F1-00	F1-01	1	E84
77	77	23734F1-00	F1-01	1	E85
78	78	23861F1-00	F1-01	1	E86

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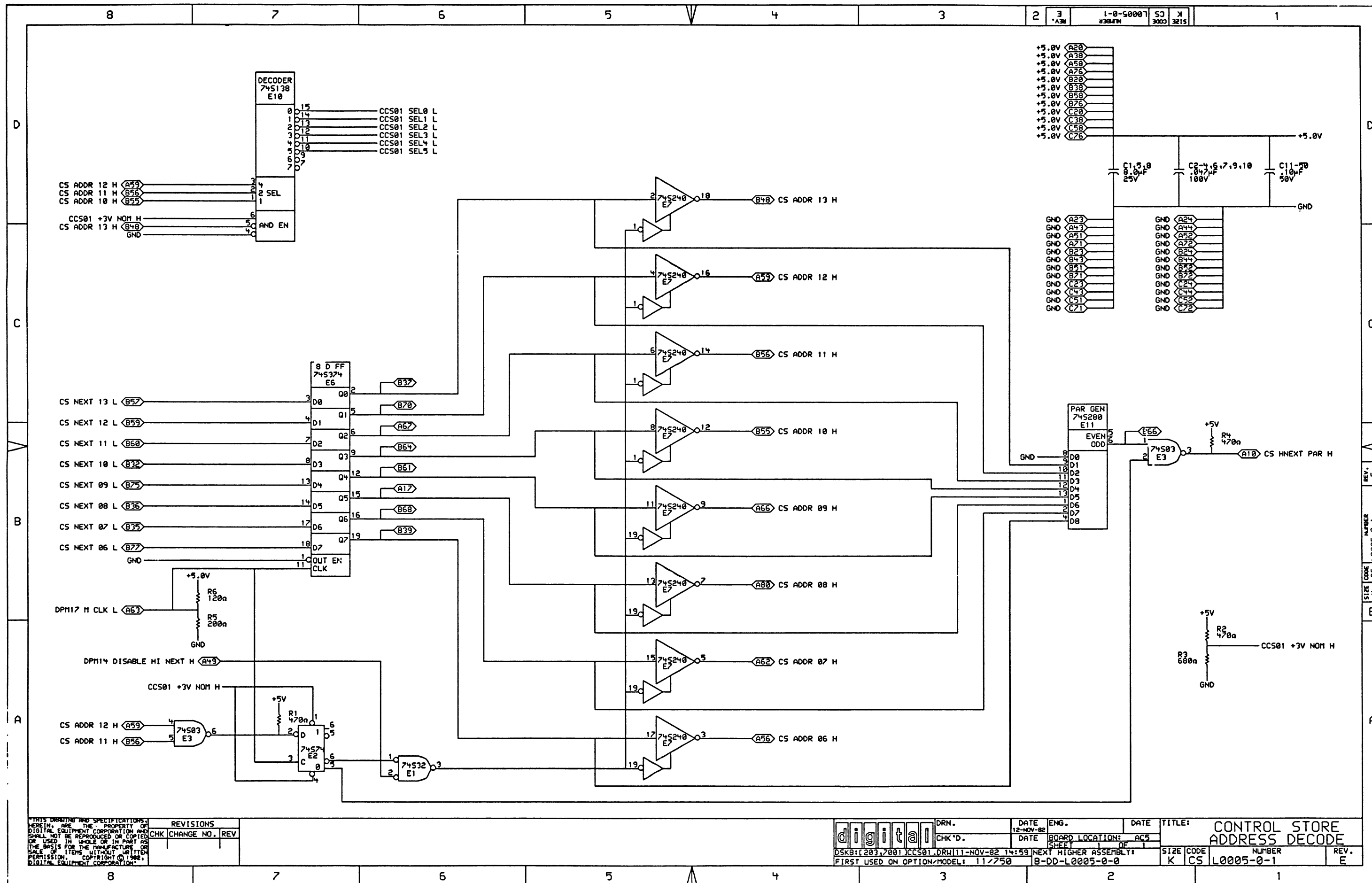
LINE ITEM		DOCUMENT NUMBER	PART NUMBER	DESCRIPTION	QTY PER VARIATION 00	REFERENCE DESIGNATOR
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81	81		23864F1-00	F1-01	1	E89
82	82		23865F1-00	F1-01	1	E90
83	83		23866F1-00	F1-01	1	E91
84	84		23867F1-00	F1-01	1	E92
85	85		23868F1-00	F1-01	1	E93
86	86		23869F1-00	F1-01	1	E94
87	87		23740F1-00	F1-01	1	E95
88	88		23870F1-00	F1-01	1	E96
89	89		23741F1-00	F1-01	1	E97
90	90		23871F1-00	F1-01	1	E98
91	91		23872F1-00	F1-01	1	E99
92	92		23553F1-00	F1-01	1	E100
93	93		23743F1-00	F1-01	1	E101
94	94		23873F1-00	F1-01	1	E102
95	95		23744F1-00	F1-01	1	E103
96	96		23874F1-00	F1-01	1	E104
97	97		23875F1-00	F1-01	1	E105
98	98		23559F1-00	F1-01	1	E106
99	99		23745F1-00	F1-01	1	E107
100	100		23876F1-00	F1-01	1	E108
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102	102		23877F1-00	F1-01	1	E110
103	103		23878F1-00	F1-01	1	E111
104	104		23565F1-00	F1-01	1	E112
105	105		23747F1-00	F1-01	1	E113
106	106		23879F1-00	F1-01	1	E114
107	107		23748F1-00	F1-01	1	E115
108	108		23880F1-00	F1-01	1	E116
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113	113		23750F1-00	F1-01	1	E121
114	114		23884F1-00	F1-01	1	E122
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119	119		23753F1-00	F1-01	1	E127
120	120		23887F1-00	F1-01	1	E128
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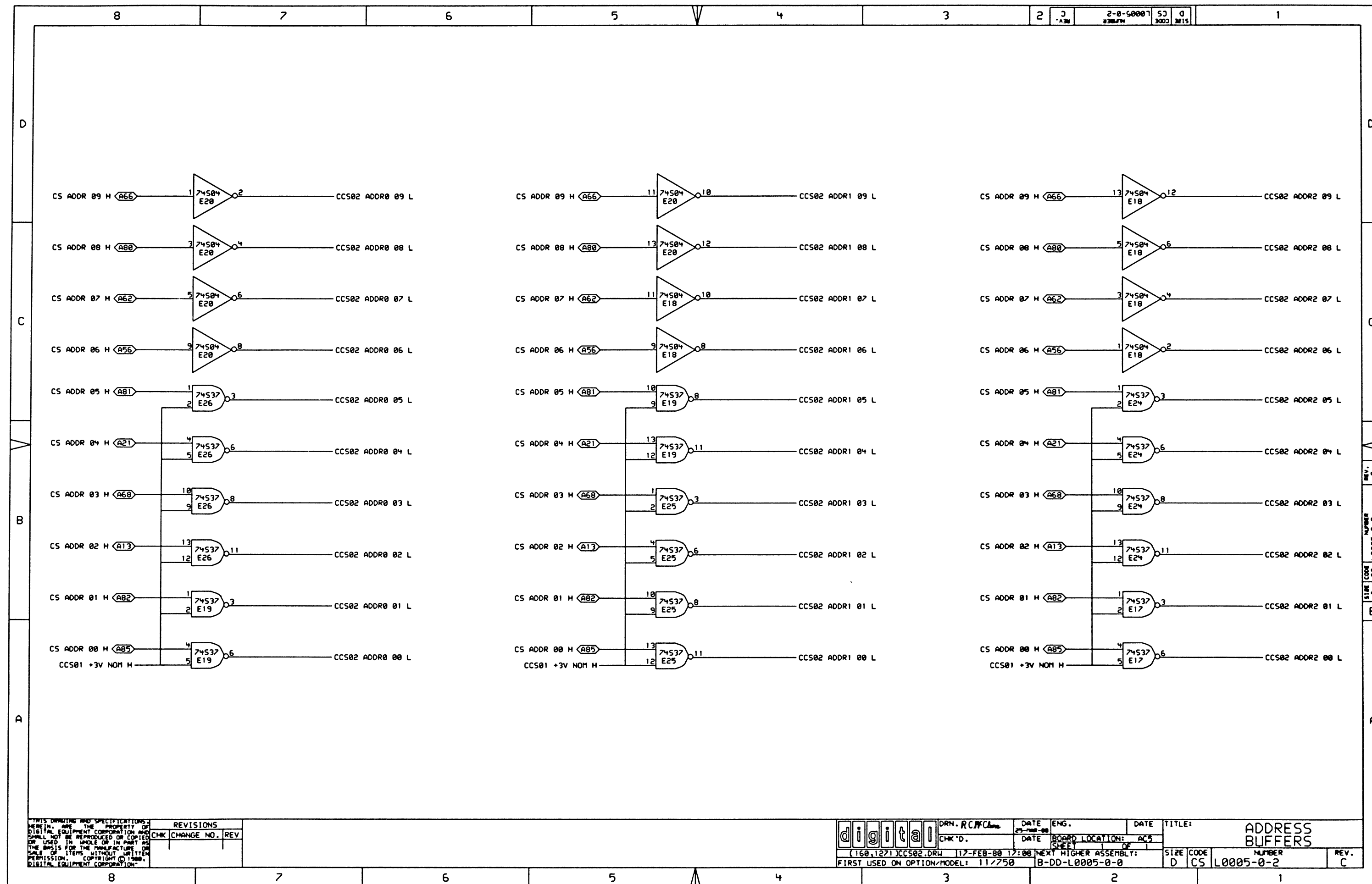
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! D ! I ! G ! I ! T ! A ! L !	CCS	! ! ! ! ! ! ! !	SECTION A OF A	! ! ! ! ! ! ! !	! ! ! ! ! ! ! !
! ! ! ! ! ! ! !		! ! ! ! ! ! ! !		! K ! PL !	L0005-0-DBP ! F !

SHEET A4 OF A4

146 NOTE: ITEM #18 IS USED ON J1-J4.
147 NOTE: SOME MODULES WILL HAVE 10-05306 INSTEAD OF 10-12084-01

!	D	!	I	!	G	!	I	!	T	!	A	!	L	!	TITLE	CCS	!	SECTION A	OF A	!	!	SIZE	!	CODE	!	DOCUMENT NUMBER	!	REV	!
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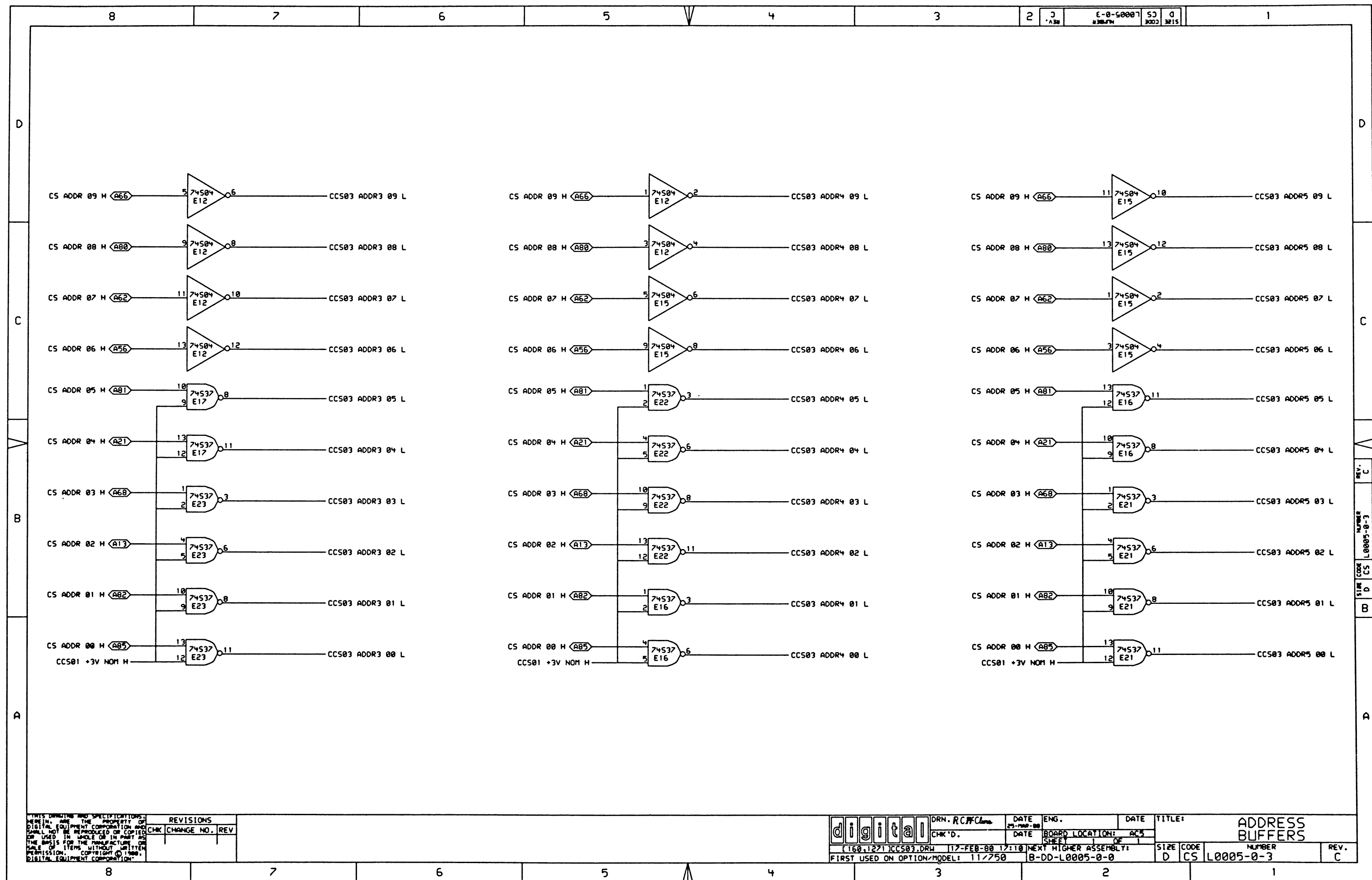
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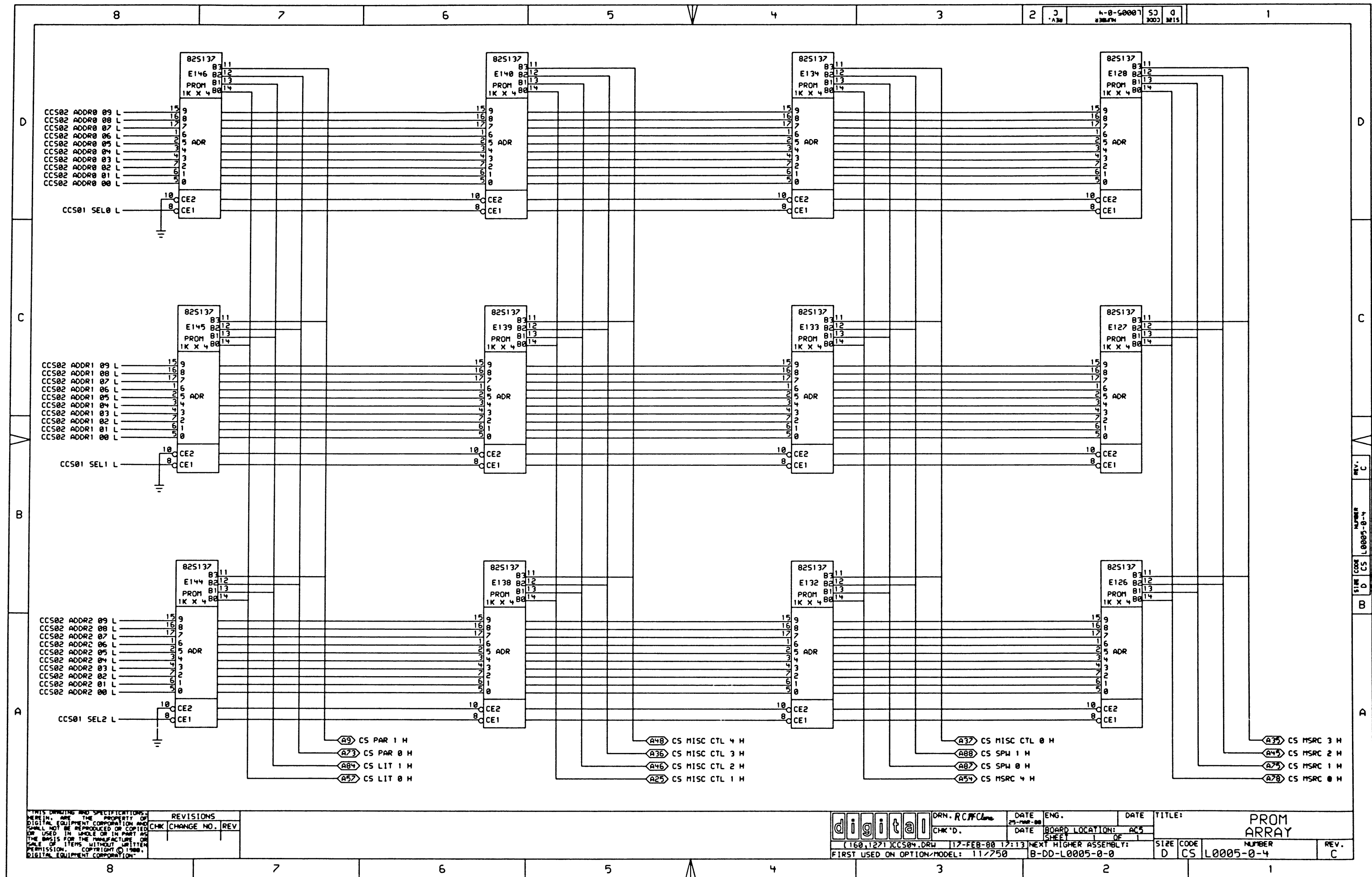
REVISIONS		
CHK	CHANGE NO.	REV

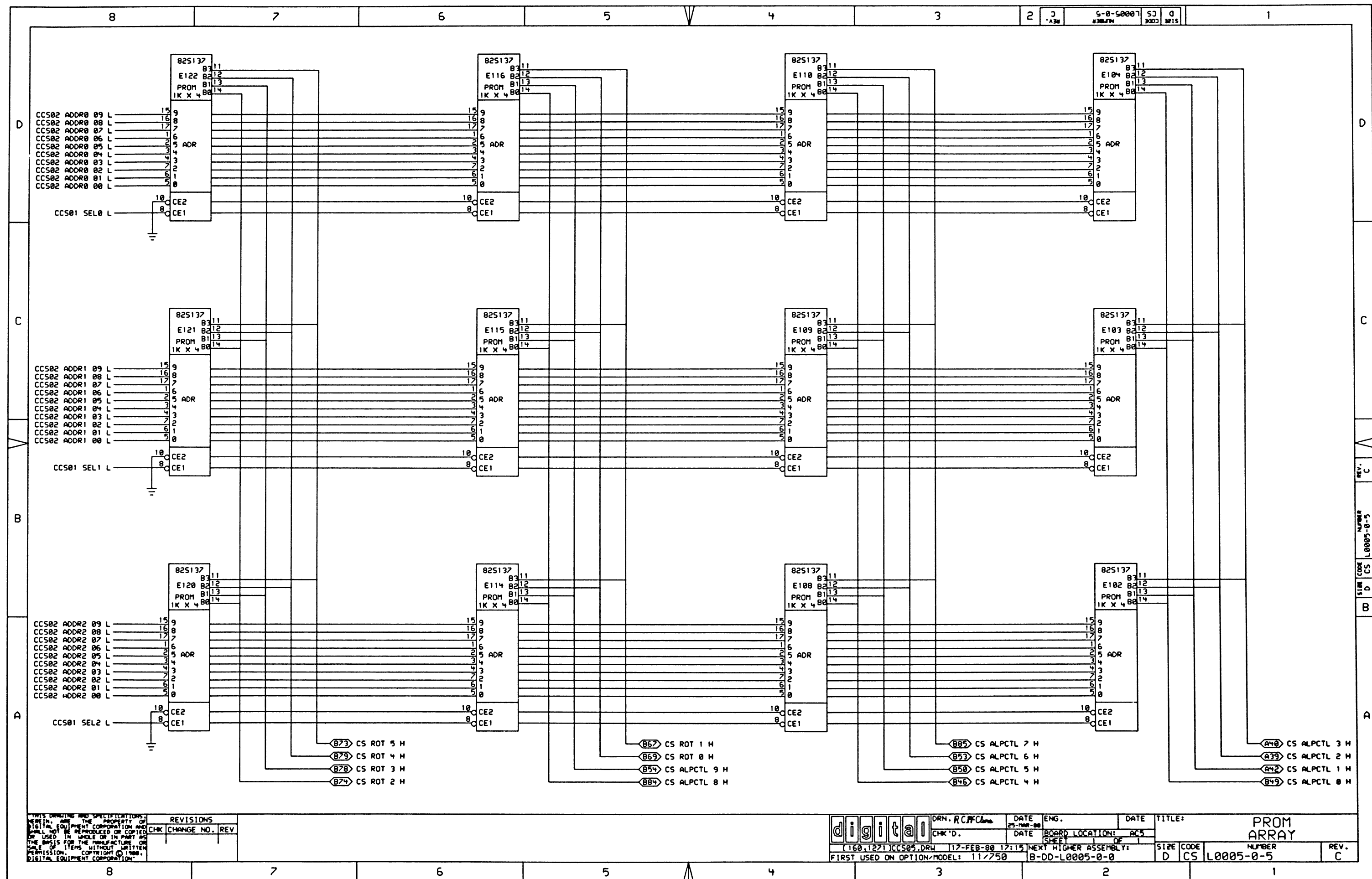
digital
(160,1271) CCS02.DRW
FIRST USED ON OPTION/MODEL: 11/750

DRN. R.C.F.C.	DATE	ENG.	DATE	TITLE:
CHK'D.	25-MAR-80			ADDRESS BUFFERS

SIZE	CODE	NUMBER	REV.
D	CS	L0005-0-2	C



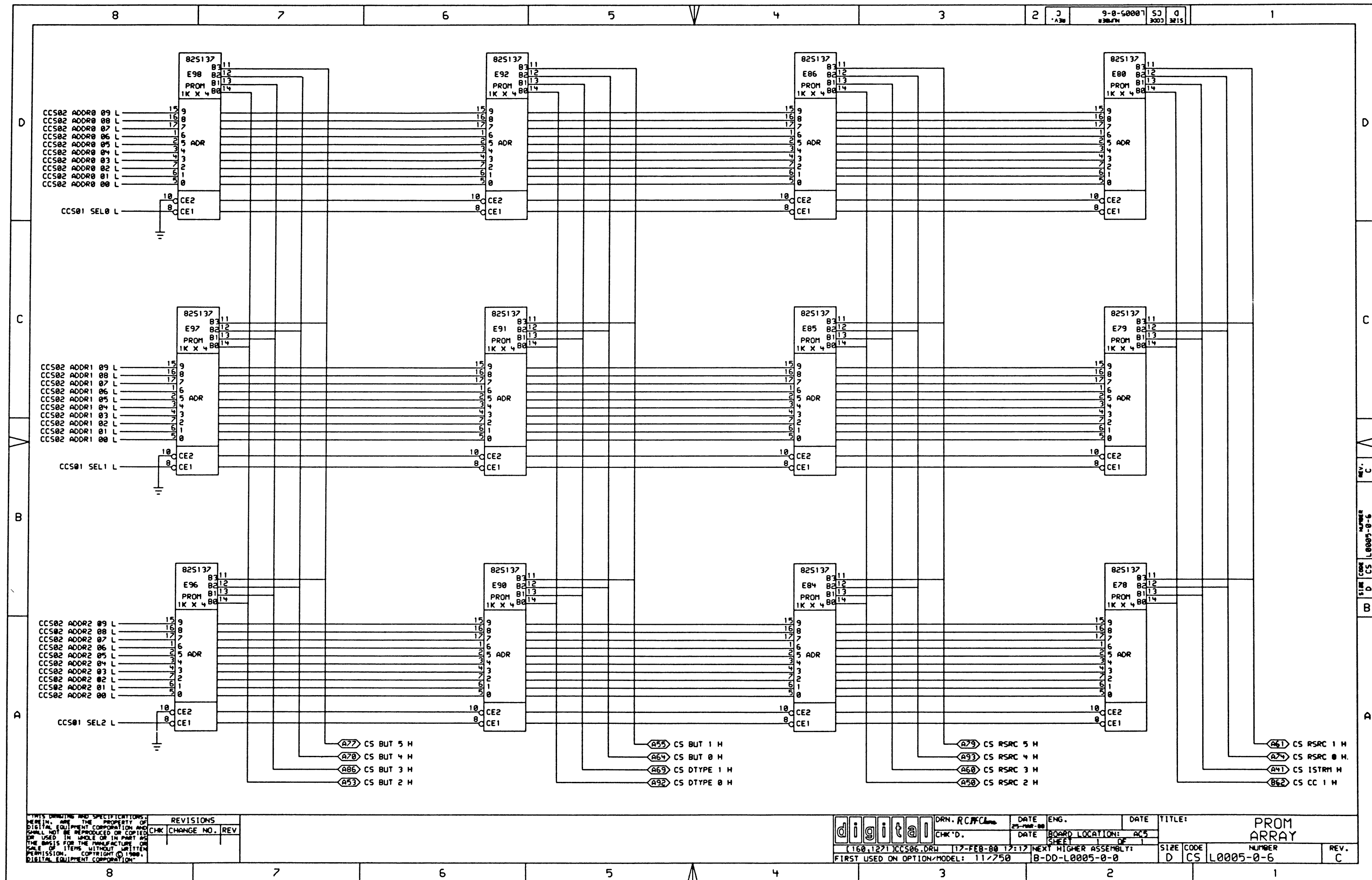


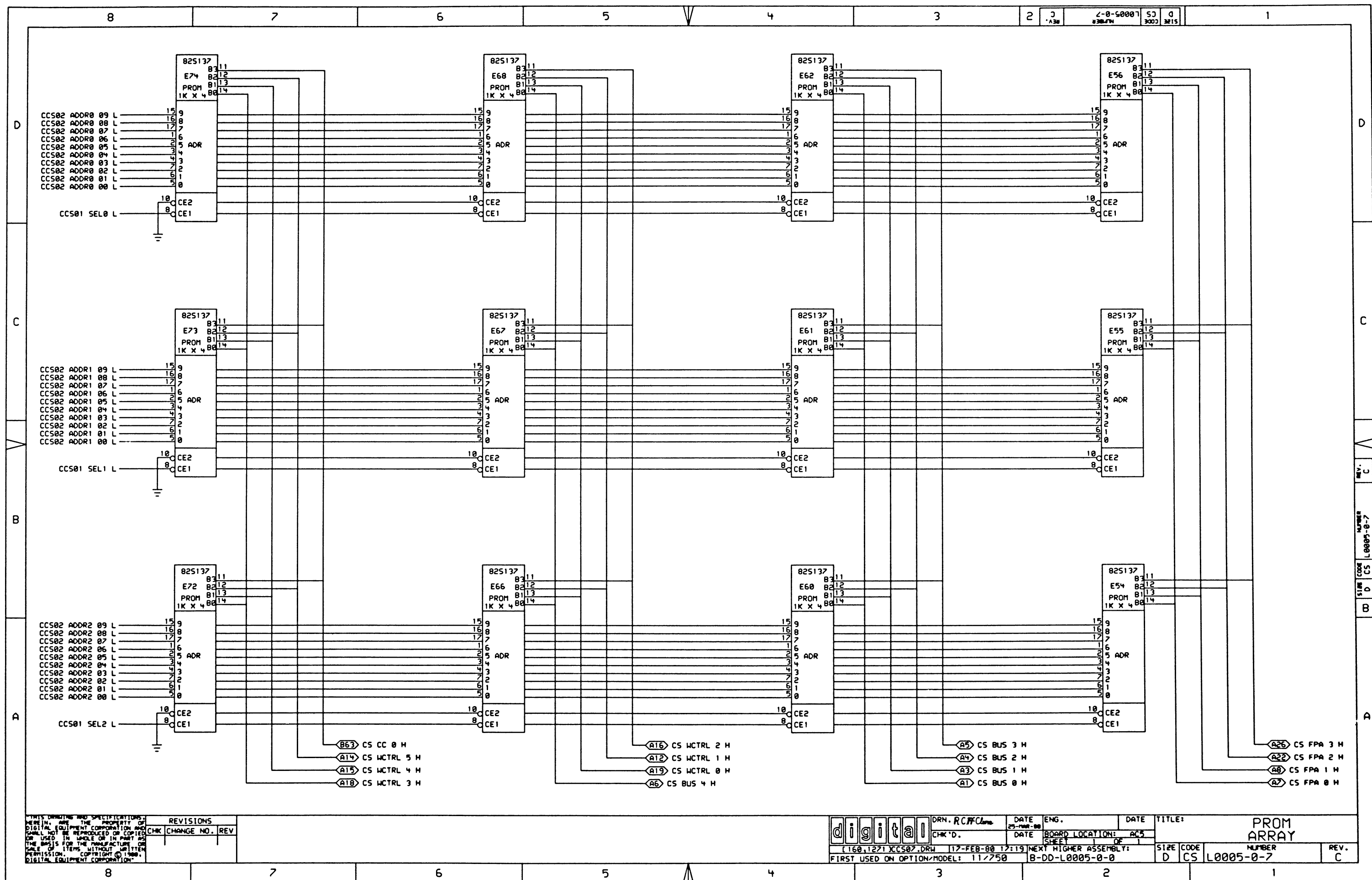


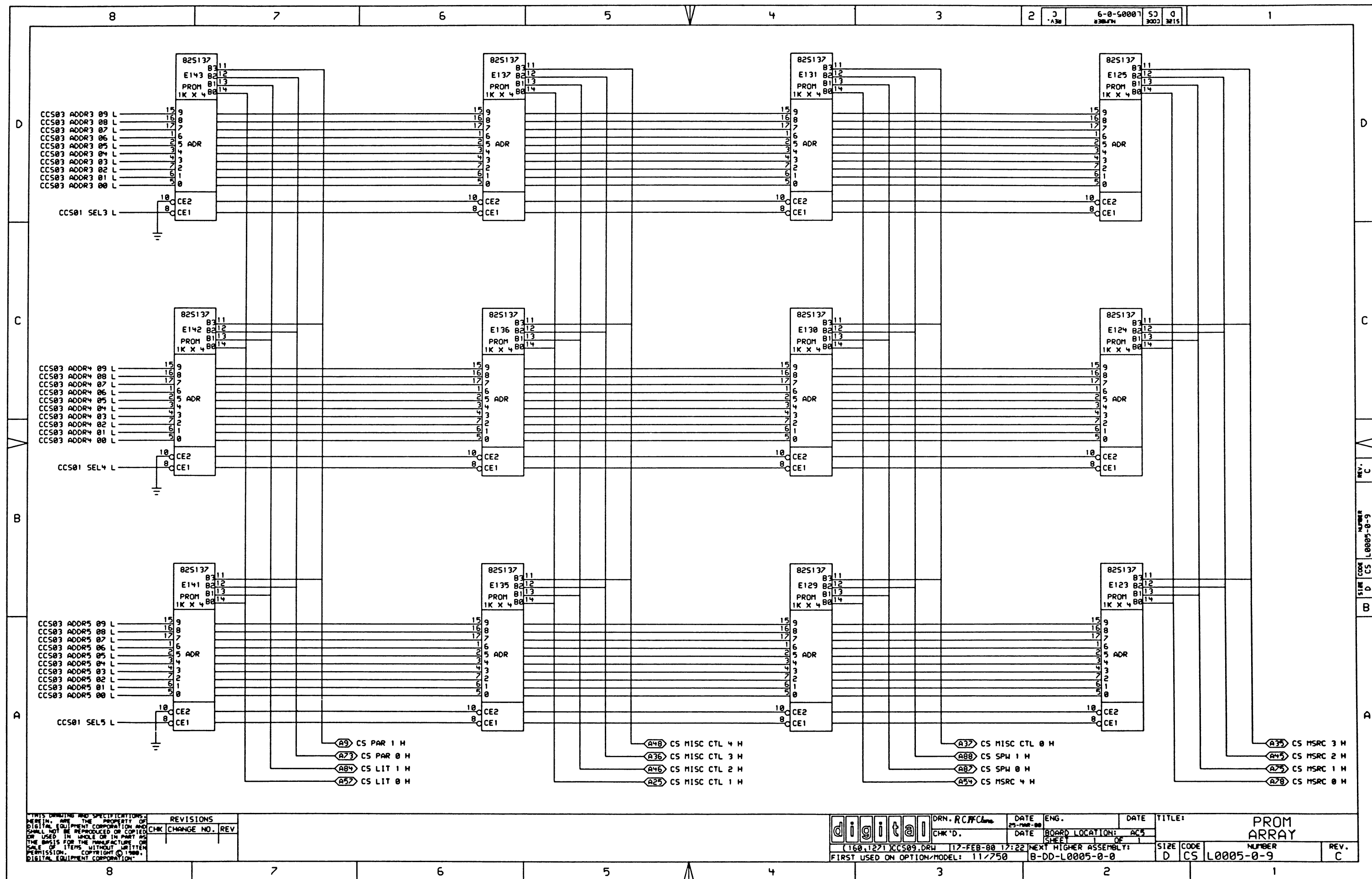
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REVISIONS		
CHK	CHANGE NO.	REV

digital	DRN. RCAF/Chm	DATE	ENG.	DATE	TITLE: PROM ARRAY	
	CHK'D.	DATE	BOARD LOCATION: AC5	DATE	SIZE	CODE
[160,127] CCS05.DRW		117-FEB-80 17:15	NEXT HIGHER ASSEMBLY:	B-DD-L0005-0-0		NUMBER
FIRST USED ON OPTION/MODEL: 11/750		B-DD-L0005-0-0		D	CS	L0005-0-5
						REV. C







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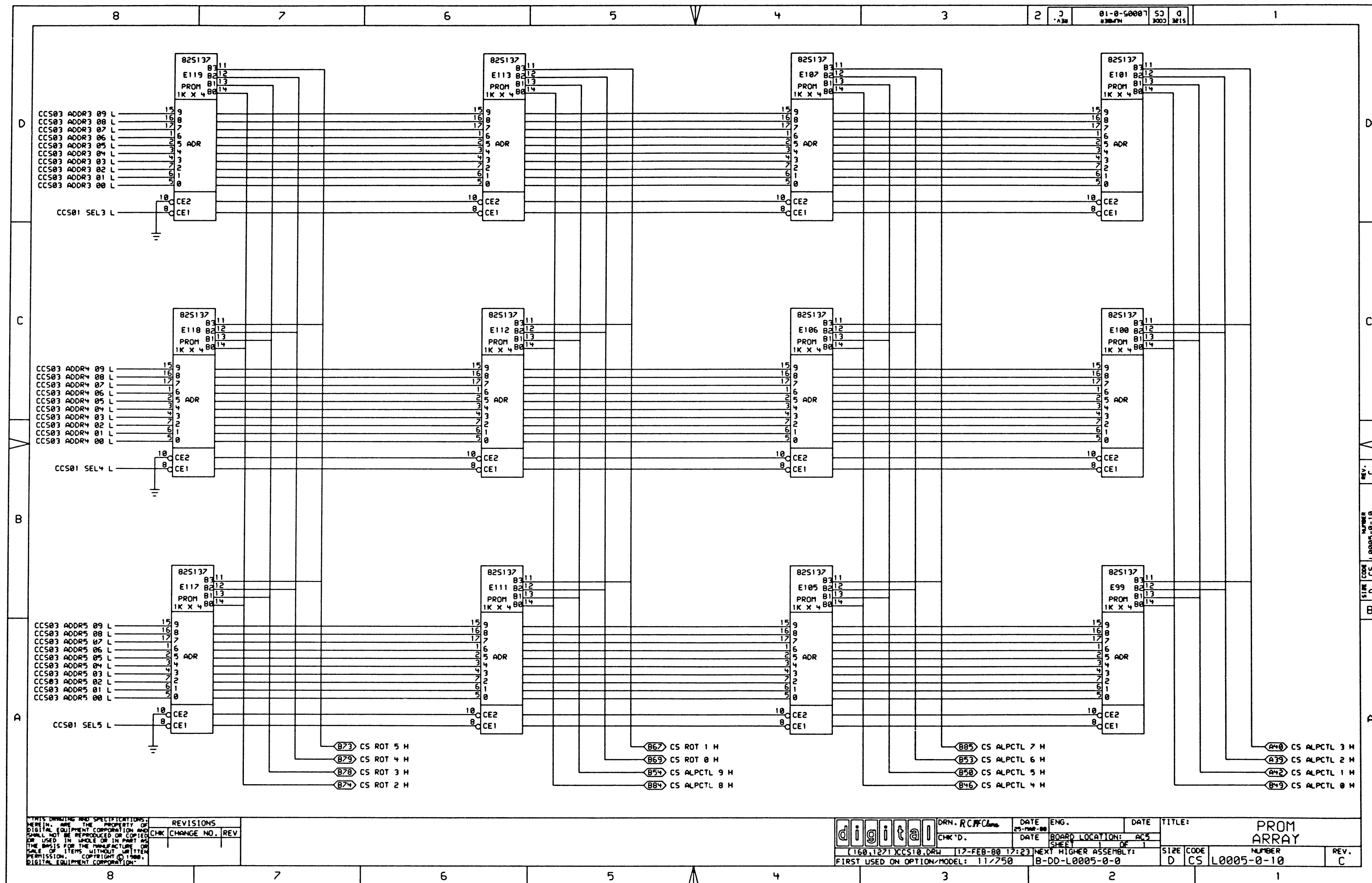
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CHK	CHANGE NO.	REV

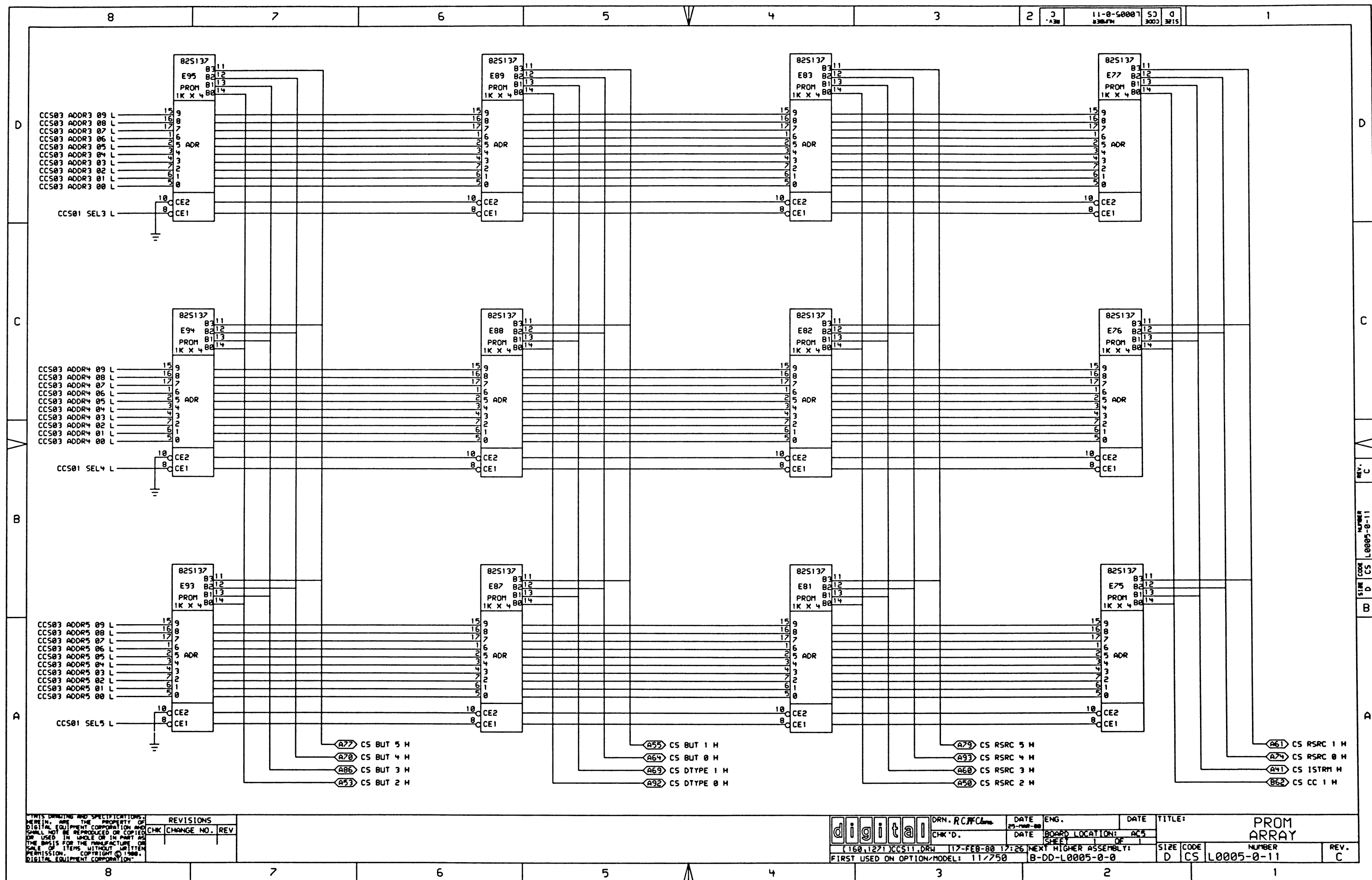
digital DRN. RCAF/Chm
CHK'D.

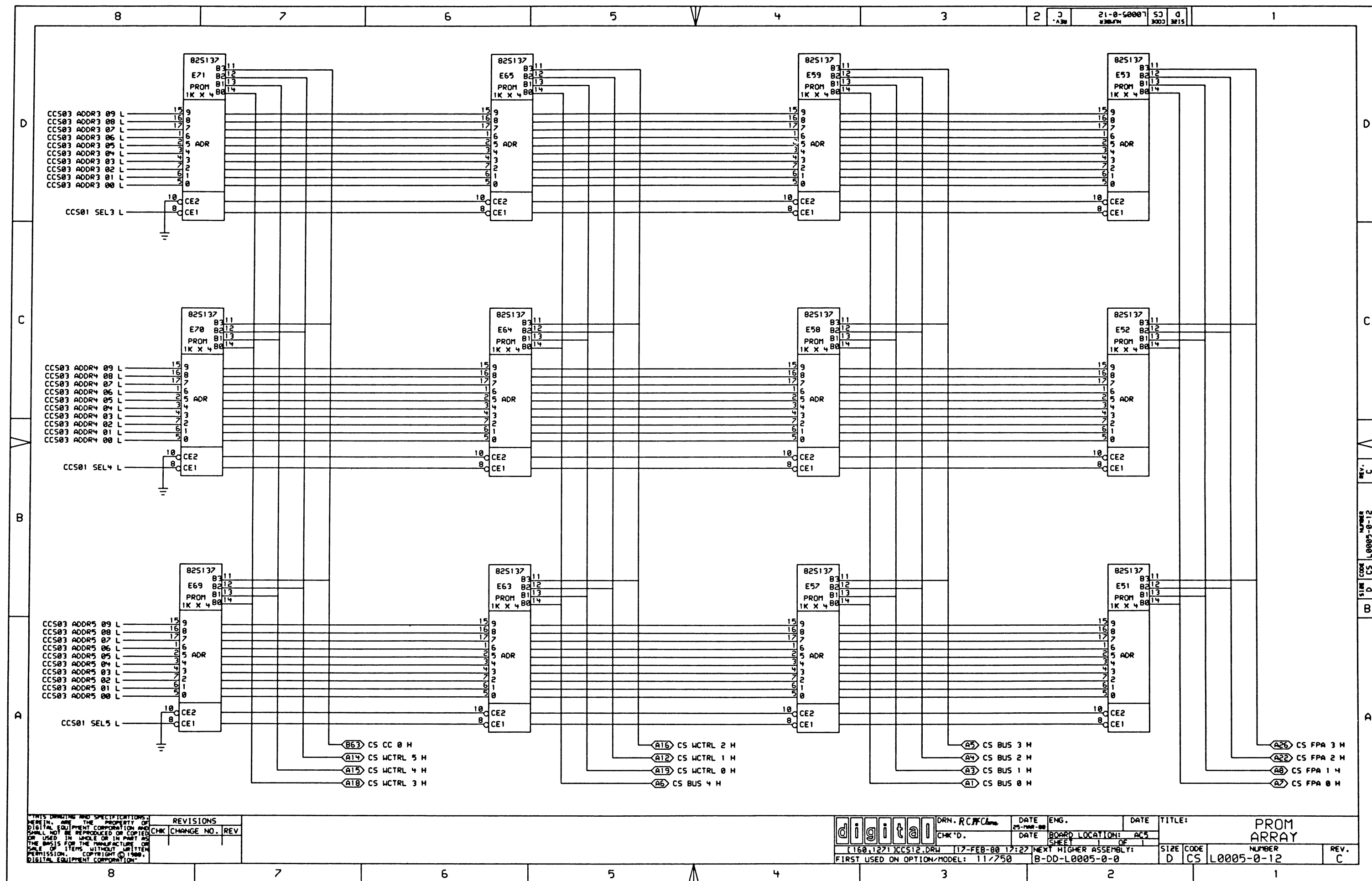
DATE 25-MAR-80
DATE 17-FEB-80 17:22
FIRST USED ON OPTION/MODEL: 11/750

ENG. DATE
BOARD LOCATION: AC5
SHEET 1 OF 1
NEXT HIGHER ASSEMBLY: B-DD-L0005-0-0

TITLE: **PROM ARRAY**
SIZE CODE D CS L0005-0-9
NUMBER
REV. C







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REVISIONS		
CHK	CHANGE NO.	REV

digital

DRN. RCM/CLM
CHK'D.

DATE 25-MAR-80
DATE 17-FEB-80

ENG.
SHEET 1 OF 1

DATE 17-FEB-80
BOARD LOCATION: AC5

TITLE:
PROM ARRAY

(160,127) CCS12.DRW
FIRST USED ON OPTION/MODEL: 11/750

NEXT HIGHER ASSEMBLY:
B-DD-L0005-0-0

SIZE CODE D CS
NUMBER L0005-0-12

REV. C

8		7		6		5		4		3		2		1	

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91-0-60007
8380/74

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3003

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D

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B

A

SIGNAL NAME

PAGE NUMBER(S)

SIGNAL NAME

PAGE NUMBER(S)

SIGNAL NAME

PAGE NUMBER(S)

CS MISC CTL 3 H

09,04,14

CS MISC CTL 4 H

09,04,14

CS MSRC 0 H

09,04,14

CS MSRC 1 H

09,04,14

CS MSRC 2 H

09,04,14

CS MSRC 3 H

09,04,14

CS MSRC 4 H

09,04,14

CS NEXT 00 H

13,08,14

CS NEXT 01 H

13,08,14

CS NEXT 02 H

13,08,14

CS NEXT 03 H

13,08,14

CS NEXT 04 H

13,08,14

CS NEXT 05 H

13,08,14

CS NEXT 06 L

01,13,08,14

CS NEXT 07 L

01,13,08,14

CS NEXT 08 L

01,13,08,14

CS NEXT 09 L

01,13,08,14

CS NEXT 10 L

01,13,08,14

CS NEXT 11 L

01,13,08,14

CS NEXT 12 L

01,13,08,14

CS NEXT 13 L

01,13,08,14

CS PAR 0 H

09,04,14

CS PAR 1 H

09,04,14

CS ROT 0 H

10,05,14

CS ROT 1 H

10,05,14

CS ROT 2 H

10,05,14

CS ROT 3 H

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CS ROT 4 H

10,05,14

CS ROT 5 H

10,05,14

CS RSRC 0 H

11,06,14

CS RSRC 1 H

11,06,14

CS RSRC 2 H

11,06,14

CS RSRC 3 H

11,06,14

CS RSRC 4 H

11,06,14

CS RSRC 5 H

11,06,14

CS SPW 0 H

09,04,14

CS SPW 1 H

09,04,14

CS WCTRL 0 H

12,07,14

CS WCTRL 1 H

12,07,14

CS WCTRL 2 H

12,07,14

CS WCTRL 3 H

12,07,14

CS WCTRL 4 H

12,07,14

CS WCTRL 5 H

12,07,14

DPH14 DISABLE HI NEXT H

01

DPH17 B CLK L

14

DPH17 M CLK L

01

DPH17 PHASE 1 H

14

UBI13 MSED INIT L

14

WCS PRESENT L

14

NOTES:

1. THIS PAGE LISTS THE SCHEMATIC PAGE NUMBER(S) WHERE A SIGNAL NAME IS REFERENCED.

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REVISIONS

CHK

CHANGE NO.

REV

digital

DRN. 017000

DATE 25-MAR-88

ENG.

DATE

TITLE: CCS FORWARD REFERENCE

(160,1271)CCS16.DRW

12-FEB-88 17:36

NEXT HIGHER ASSEMBLY:

SIZE CODE

NUMBER

REV.

FIRST USED ON OPTION/MODEL: 11/750

B-DD-L0005-0-0

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CS

L0005-0-16

C

8

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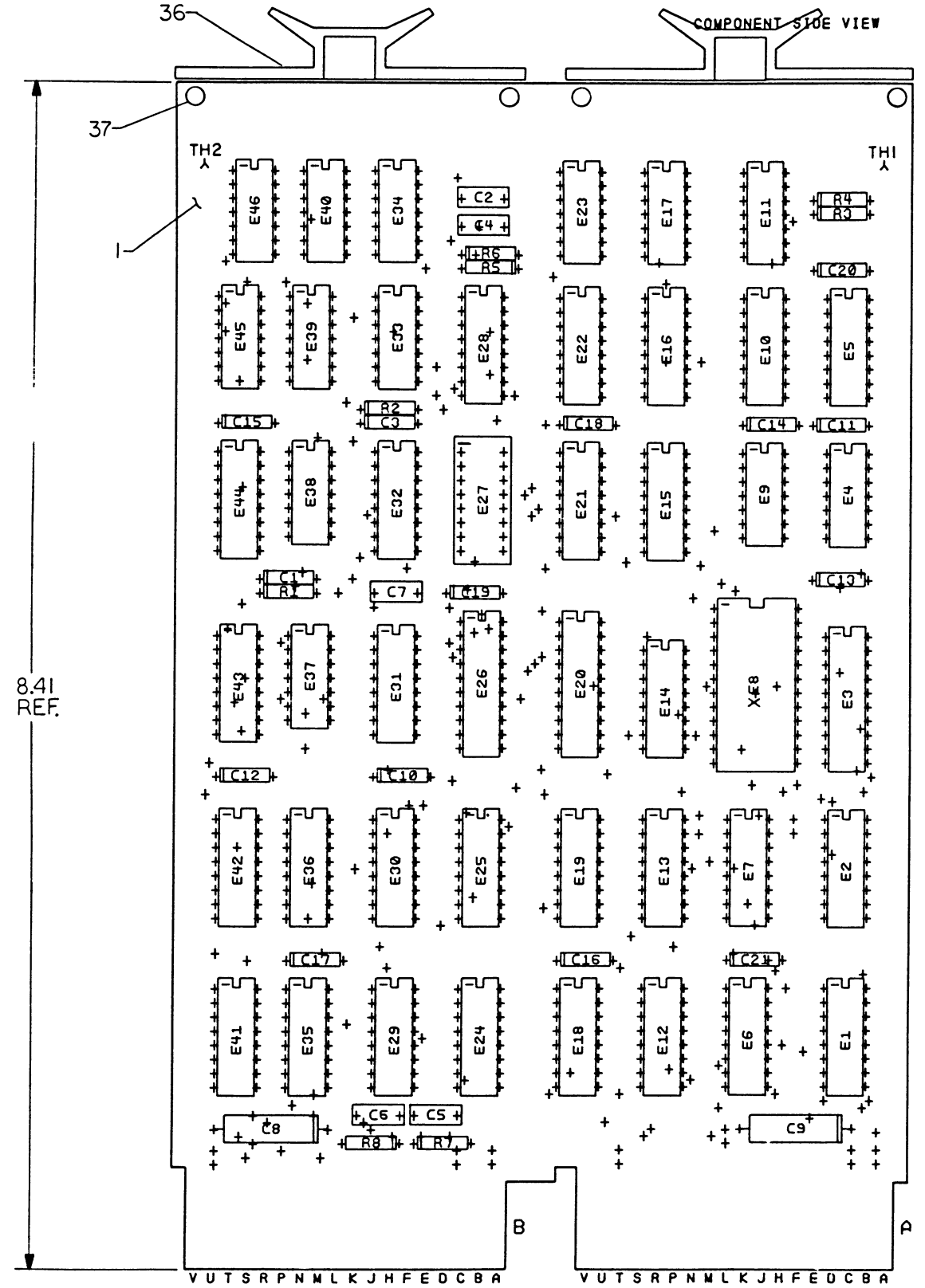
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1

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D
C
B
A



D
C
B
A

NOTES:

STEP 1	↑ Y AXIS	STEP	TIMES
REPEAT	→ X AXIS	STEP	TIMES

CHK	CHANGE	NO	REV

ETCH REV.	8
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SIGNATURES	DATE	digital
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CHK'D. E.T. GERRY	7-31-79	
MECH. ENG. <i>[Signature]</i>	8-21-80	
PROJ. ENG. <i>[Signature]</i>	3-21-80	
PROD. <i>[Signature]</i>	3-20-80	TITLE
UET		
SCALE 2:1	SIZE CODE	NUMBER
SHT. 1 OF 3	0	UA M9313-0-0
NEXT HIGHER ASSY. B-DD-M9313-0		REV
		A

8

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4

3

1 2 3 4 5 6 7 8

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501384 7B M9313

501384 7B M9313

LAYER 1

SIDE 1
E46 E40 E34

E23 E17 E11

digital

E28

E5

SIDE 1
E46 E40 E34

E23 E17 E11

digital

E28

E5

A

B

C

D

A

B

C

D

REVISIONS		
CHK	CHANGE NO	REV

8

7

6

5

4

3

2

1

TITLE
UET

SCALE 2 - 1

SHEET 2 OF 3

SIZE CODE
D UA

DIST

NUMBER
M9313 - 0 - 0

REV
A

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REVERSE

5013847B M9313
SIDE 2

UET

5013847B M9313
SIDE 2

UET

REVISIONS		
CHK	CHANGE NO	REV

TITLE		UET		SIZE CODE		NUMBER		REV	
SCALE 2 - 1		SHEET 3 OF 3		DIST.		DUA M9313 - 0 - 0		A	

LINE	ITEM	DOCUMENT NUMBER	PART NUMBER	DESCRIPTION	QTY	PER VARIATION	REFERENCE DESIGNATOR
1	1		5013847-00	VET	1		
2	2		1215006-06	SKT, IC	1		XE8
3	3		1300005-04	R NETWORK 15-470	1		E27
4	4		1300202-00	47.0 .25 W 5.0 % CC	2		R1, R2
5	5		1300229-00	100.0 .25 W 5.0 % CC	2		R3, R4
6	6		1312628-00	R NETWORK 14-176.5 14-375 16PIN	2		E2, E42
7	7		1312628-01	R NETWORK 14-176.5 14-375 16PIN	2		E12, E29
8	8		1909704-00	DEC 314A NOR GATE-SINGLE 7IN	1		E37
9	9		1909705-00	DEC 8881 NAND GATE-QUAD 2IN 0	1		E9
10	10		1910436-00	DEC 74123 ONE SHOT-DUAL, RETRIG	1		E28
11	11		1911469-00	DEC 8640 RECEIVER, BUS, QUAD, U	1		E4
12	12		1911579-00	8641 TRANSCEIVER, BUS, QUA	10	CONT	E1, E7, E18, E19, E30, E31, E35, E36, E41, E44
13	13		1911983-00	74S133 NAND GATE-POSITIVE 1	1		E43
14	14		1912799-00	LS00 NAND-GATE-QUAD 2IN, P	1		E40
15	15		1912803-00	74LS04 INVERTER GATE, HEX	2		E17, E39
16	16		1910535-00	74S05 INVERTER GATE-HEX 1	1		E33
17	17		1912805-00	LS08 AND GATE-QUAD 2IN, P0	1		E46
18	18		1912808-00	LS11 AND GATE-TRIPLE 3IN	1		E34
19	19		1912810-00	LS20 NAND GATE-DUAL 4IN	1		E23
20	20		1912824-00	LS74 FF-D DUAL, EDGE TRIGG	2		E38, E45
21	21		1912853-00	LS175 FF-D QUAD	1		E21
22	22		1914214-00	LS374 FF-D OCTAL EDGE TRIG	2		E3, E26
23	23		1914438-00	DC 013 UNIBUS INTERRUPT-BIP	5		E5, E10, E15, E16, E22
24	24		1914845-00	2918 FF-D QUAD TRI-STATE	5		E6, E13, E14, E24, E25
25	25		23272A1-00	A1-03, A1-04, A1-05	1		E32
26	26		1000055-00	2200.0 MMF 250V 20% Y5S DISC	2		C2, C4
27	27		1000043-00	1000.0 MMF 250V 20% Y5F DISC	2		C5, C6
28	28		1001610-01	.01 MFD50/100V +80-20% DISC	1		C7
29	29		1611243-00	DELAY=25-250NS, 10TAPS RCL#L-183	1		E11

REVISION HISTORY		BASIC PART NO: M9313		DRN: M.FUNARO		DATE: 22-MAY-79		D I G I T A L			
ENG	ECO NUMBER	REV	SECTION A OF A	CHK'D: F.GAROFALO		DATE: 22-MAY-79		TITLE PARTS LIST			
---	INITIAL	A	SECTION VARIATION INDEX	DES.ENG: B.ARMSTRONG		DATE: 22-MAY-79		DOCUMENT NUMBER			
SB	M9312-TW001	B	[A] 00	RESP.ENG.: B.ARMSTRONG		DATE: 22-MAY-79		SIZE CODE NUMBER REV			
			[B]	MFG.ENG.: K.O'BRIEN		DATE: 13-FEB-80		K	PL	M9313-0-DBP	B
			[C]	ASSEMBLY NUMBER:		TOP DOCUMENT NUMBER:		FILE NAME: EDIT #			
			[D]	D-UA-M9313-0-0		B-DD-M9313-0-0		Z1259B.PLS 15			
			[E]	"THIS DRAWING AND SPECIFICATIONS HEREIN, ARE THE PROPERTY OF DIGITAL EQUIPMENT CORPORATION AND SHALL NOT BE REPRODUCED OR COPIED OR USED IN WHOLE OR IN PART AS THE BASIS FOR THE MANUFACTURE OR SALE OF ITEMS WITHOUT WRITTEN PERMISSION. COPYRIGHT (C) 1981. DIGITAL EQUIPMENT CORPORATION"							
			[F]								
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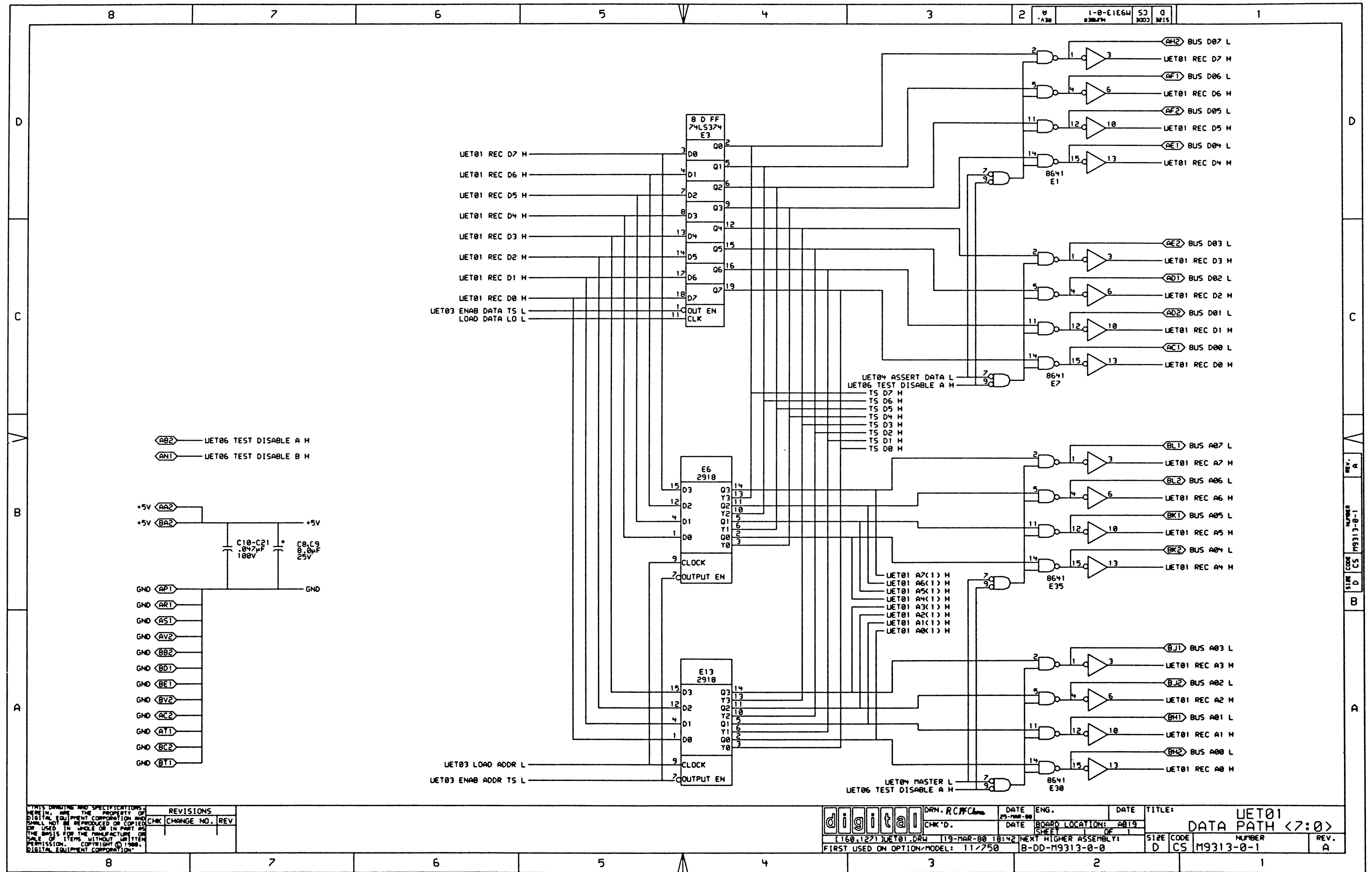
PARTS LIST

SHEET A2 OF A2

LINE	ITEM	DOCUMENT NUMBER	PART NUMBER	DESCRIPTION	QTY PER VARIATION	REFERENCE DESIGNATOR
30	30		1913777-00	LS240 DRIVER, LINE, OCTAL, T	1	E20
31	31		1302394-00	30.0 K .25 W 5.0 % CC	2	R5, R6
32	32		1305125-00	383.0 .25 W 1.0 % RN55D-F10	2	R7, R8
33	33	SEE NOTES	1012784-00	.047 MFD 50V +80-20% CER	1	C10-C21
34	34		1012084-01	8 MFD 25V +75-10% AL EL	2	C8, C9
35	35		1000024-00	470.0 MMF 100V 5%200PPM MICA	2	C1, C3
36	36		9008337-06	HANDLE, FLIP CHIP, MAGENTA	2	
37	37		9000024-01	EYELET, ROLL FLANGE .12100X .192	4	

38 NOTE: SOME MODULES WILL HAVE 10-05306 INSTEAD OF 10-12084-01

D	I	G	I	T	A	L	TITLE	SECTION A OF A	SIZE	CODE	DOCUMENT NUMBER	REV
							UET		K	PL	M9313-0-DBP	B



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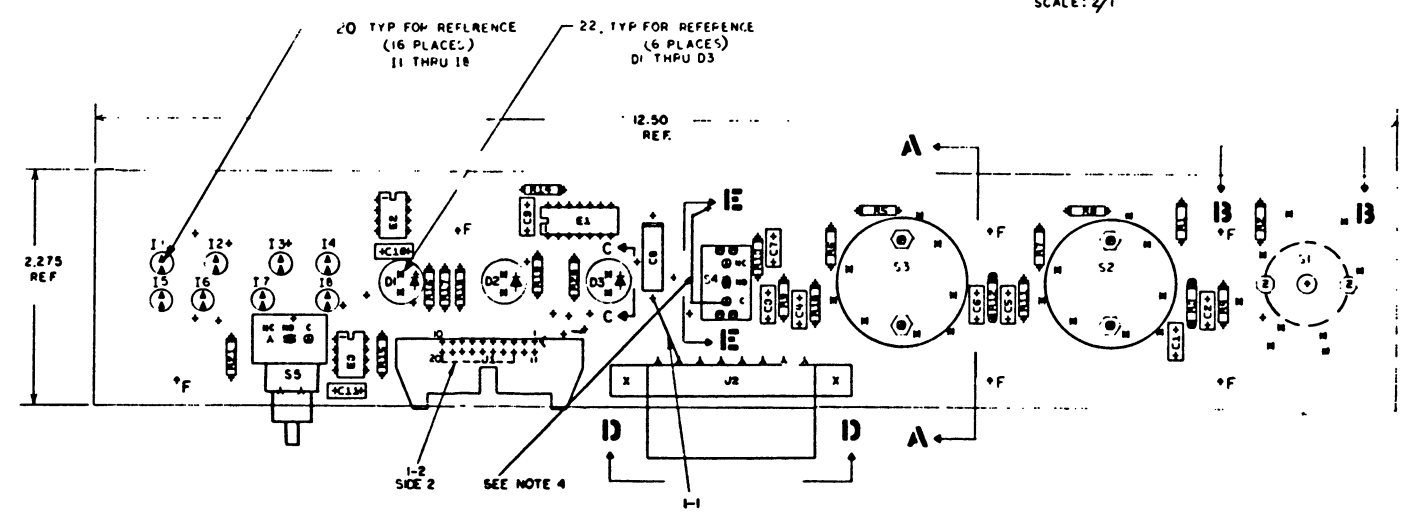
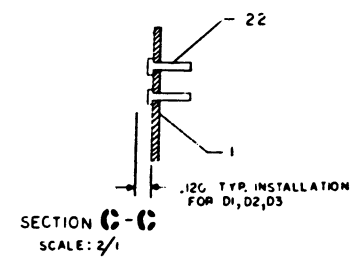
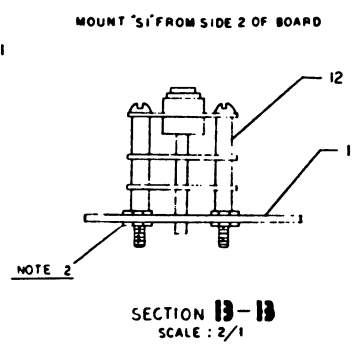
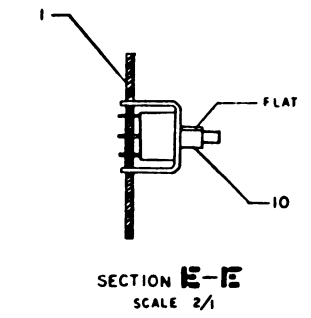
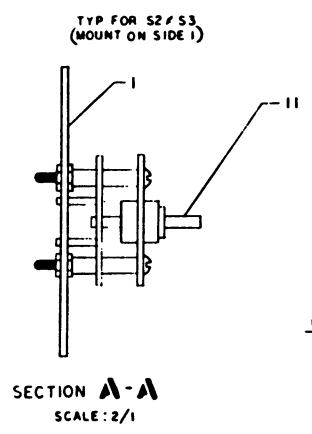
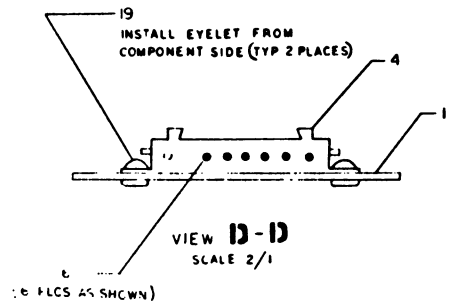
REVISIONS		
CHK	CHANGE NO.	REV

digital	DRN. R.C.M.C.	DATE 25-MAR-80	ENG.	DATE	TITLE: UET01 DATA PATH <7:0>			
	CHK'D.	DATE	BOARD LOCATION: AA19 SHEET 1 OF 1		SIZE D	CODE CS	NUMBER M9313-0-1	REV. A
(160,1271) UET01.DRW		19-MAR-80 18:42	NEXT HIGHER ASSEMBLY:					
FIRST USED ON OPTION/MODEL: 11750		B-DD-M9313-0-0						

8	7	6	5	4	3	2	1																																																		
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UET05 BR5 (0)H		05																																																							
UET05 BR6 (0)H		05																																																							
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UET05 INTERRUPT MASTER L		05,04																																																							
UET05 MASTER 04 L		05																																																							
UET05 MASTER 05 L		05																																																							
UET05 MASTER 06 L		05																																																							
UET05 MASTER 07 L		05																																																							
UET05 SACK L		05																																																							
UET06 +3V H		06,05,03,04																																																							
UET06 TEST DISABLE A H		01,02,03,04																																																							
UET06 TEST DISABLE B H		03																																																							
NOTES: 1. THIS PAGE LISTS THE SCHEMATIC PAGE NUMBER(S) WHERE A SIGNAL NAME IS REFERENCED.																																																									
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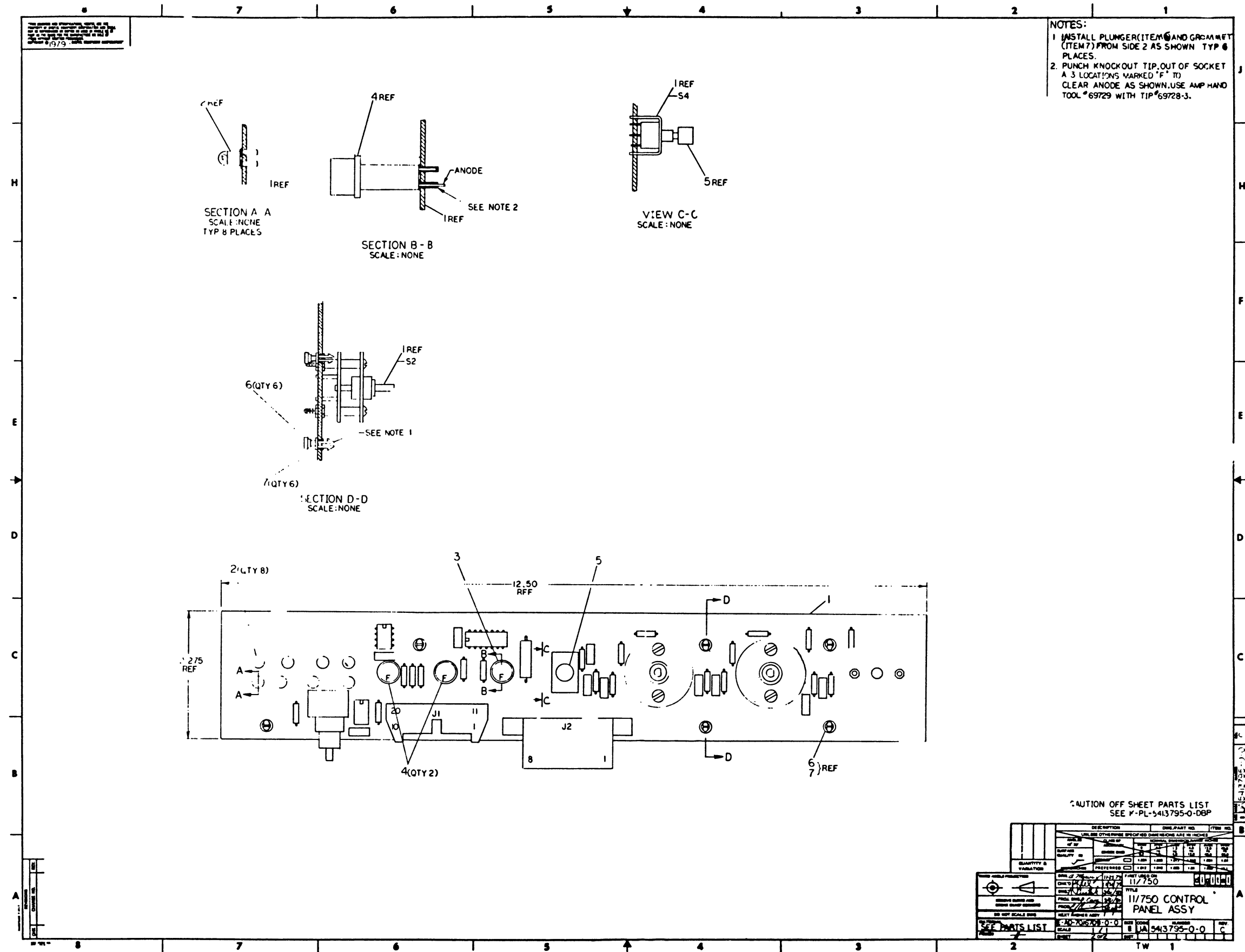
ECO 1 RERWORK INSTRUCTIONS
 WIRE ADDS SIDE 1
 1-1 ADD 18 AWG 9107560-03 WIRE AND TUBING
 9107279-06 BETWEEN BOTTOM LEAD OF C8
 AND J2-1.
 WIRE ADDS SIDE 2
 1-2 BETWEEN J1-14 AND J1-20

COMPONENT SIDE VIEW



NOTES:
 4. OLD WIRE FROM LOWER FAC OF S4 TO
 REC. THRU ABOVE S4

REVISIONS	DATE	BY	CHKD	APP'D	DESCRIPTION
1	11/750				CONTROL PANEL
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SHEET A1 OF A2

REVISION HISTORY			BASIC PART NO: 5413795		DRN: P.TELLIER		DATE: 17-MAY-83		D I G I T A L						
ENG	ECO NUMBER		REV	SECTION A OF A		CHK'D: F.GAROFALO		DATE: 17-MAY-83		TITLE		PARTS LIST			
---	INITIAL		B	SECTION VARIATION INDEX		CHK'D: F.GAROFALO <td colspan="2">DATE: 17-MAY-83<td colspan="2">11/750 CONTROL PANEL</td><td colspan="4"></td></td>		DATE: 17-MAY-83 <td colspan="2">11/750 CONTROL PANEL</td> <td colspan="4"></td>		11/750 CONTROL PANEL					
RC	5413795-TW01A		C	[A] 00		DES.ENG: R.CIESLUK <td colspan="2">DATE: 17-MAY-83<td colspan="2"></td><td colspan="4"></td></td>		DATE: 17-MAY-83 <td colspan="2"></td> <td colspan="4"></td>							
RC	5413795-TW002		D	[B]		RESP.ENG.: D.CANE <td colspan="2">DATE: 17-MAY-83<td colspan="2"></td><td colspan="4"></td></td>		DATE: 17-MAY-83 <td colspan="2"></td> <td colspan="4"></td>							
SB	5413795-TW003		E	[C]		MFG.ENG.: K.O'BRIEN <td colspan="2">DATE: 17-MAY-83<td colspan="2"></td><td colspan="4"></td></td>		DATE: 17-MAY-83 <td colspan="2"></td> <td colspan="4"></td>							
				[D]		ASSEMBLY NUMBER:		TOP DOCUMENT NUMBER:							
				[E]		D-UA-5413795-0-0		B-DD-5413795-0-0							
				[F]											
				[H]											
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P A R T S L I S T

SHEET A2 OF A2

LINE	ITEM	DOCUMENT NUMBER	PART NUMBER	DESCRIPTION	QTY PER VARIATION 00	REFERENCE DESIGNATOR
------	------	-----------------	-------------	-------------	-------------------------	----------------------

30 NOTE: SOME MODULES WILL HAVE 10-05306 INSTEAD OF 1012084-01

D	I	G	I	T	A	L	TITLE	11/750 CONTROL PANEL	SECTION A OF A	SIZE	CODE	DOCUMENT NUMBER	REV
										K	PL	5413795-0-DBP	E

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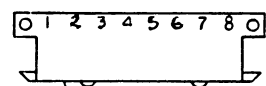
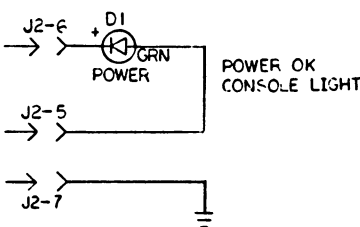
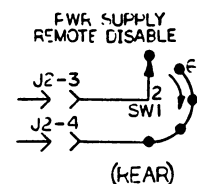
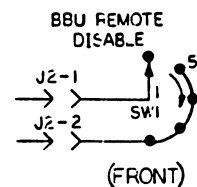
SWITCH 1 (SHOWN IN POSITION ONE)		
SWITCH POS	FUNCTION	CONNECTION
1	OFF	
2	SECURE	1-5, 2-6
3	LOCAL	1-5, 2-6, 3-7
4	REMOTE SECURE	1-5, 2-6, 3-8, 4-9
5	REMOTE	1-5, 2-6, 4-9

SWITCH 2 (SHOWN IN POSITION ONE)		
SWITCH POS	FUNCTION	CONNECTION
1	BOOT	8-10
2	FESTART	1-9, 6-10
3	HALT	
4	RESTART	1-5

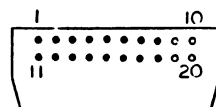
SWITCH 3		
SWITCH POS	FUNCTION	CONNECTION
1	A	
2	B	1-9
3	C	4-10
4	D	1-5, 2-10

NOTES:

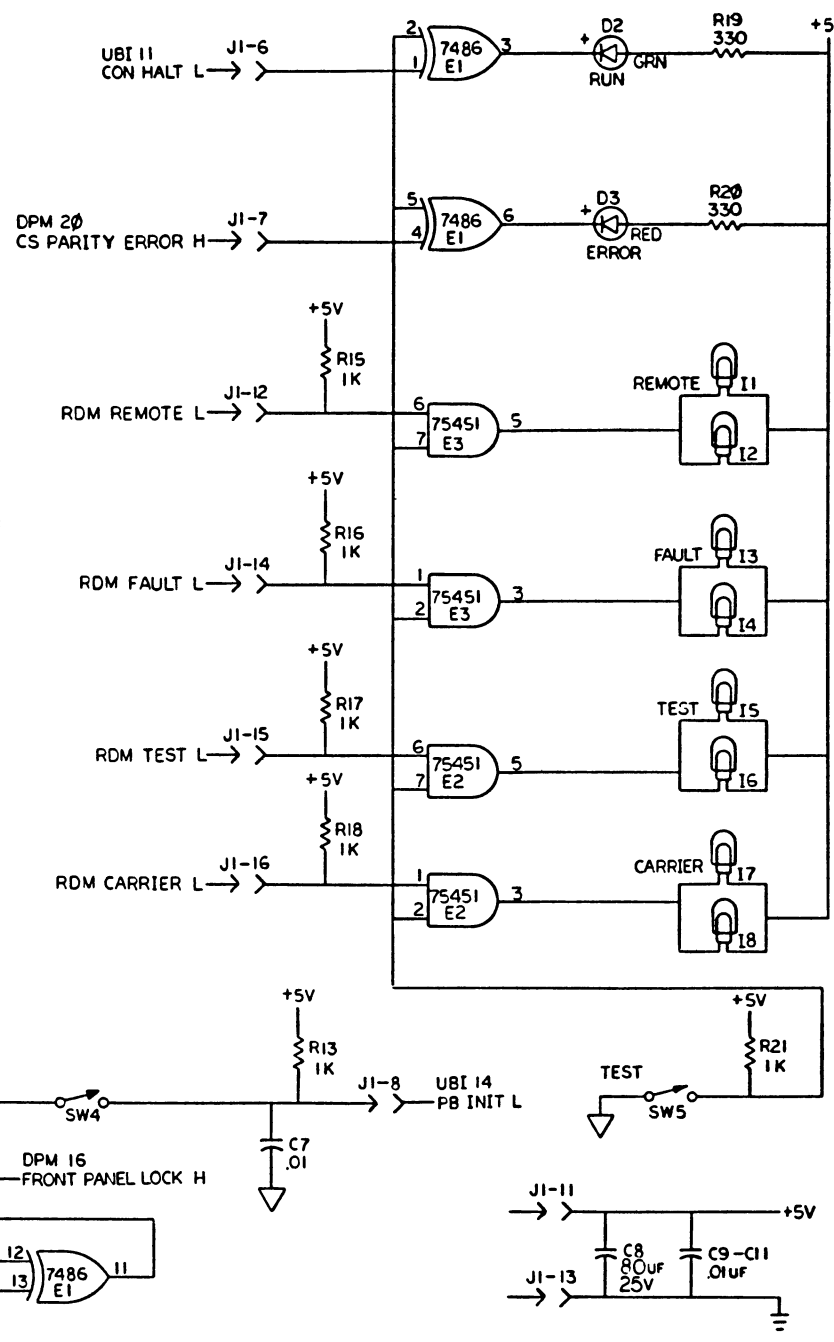
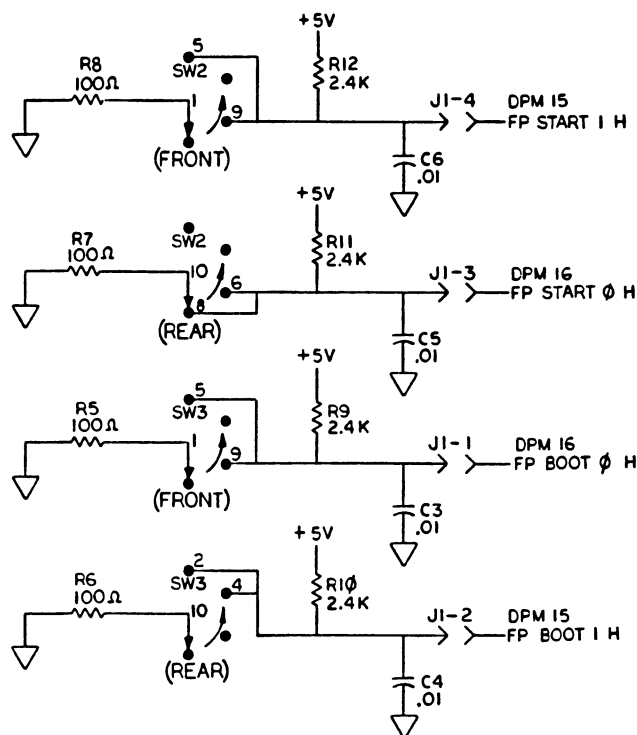
- SWITCHES 2 AND 3 ARE IN POSITION ONE WHEN FULLY CLOCKWISE.



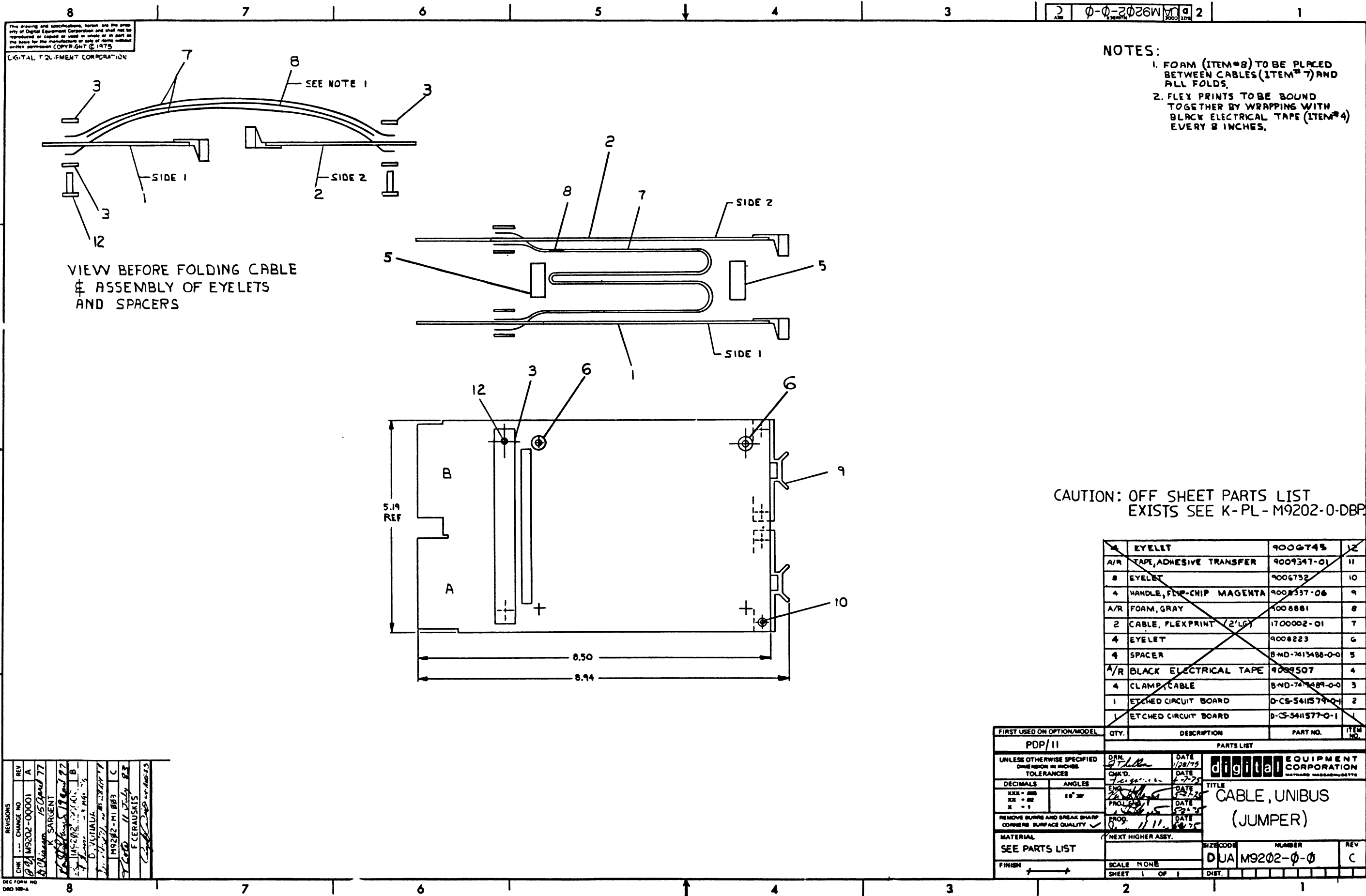
DEC P/N: 1209340-00



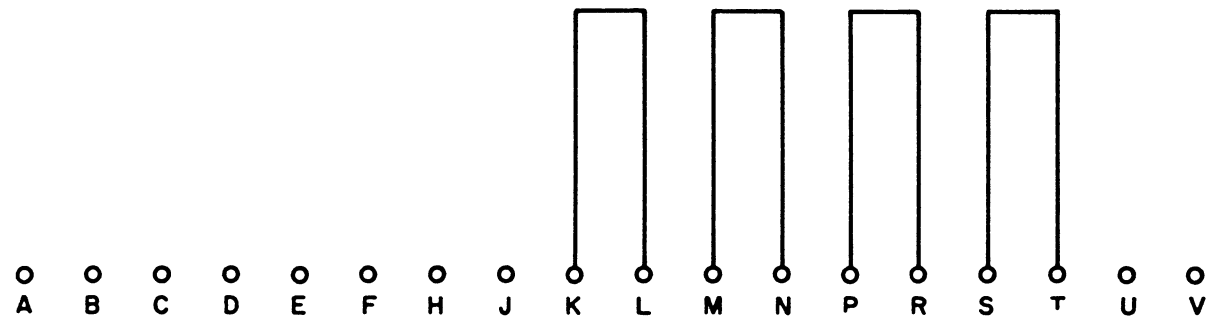
DEC P/N: 1209941-06



DRN. <i>D. Chisum</i>	3/21/77	FIRST USED ON	<i>11/750</i>
CHK'D <i>T. Chisum</i>	3/21/77	TITLE	11/750 CONTROL PANEL
ENG. <i>R. Chisum</i>	3/21/77	SIZE	D
PROJ. ENG. <i>D. Chisum</i>	3/21/77	CODE	CS
PROD. <i>J. Chisum</i>	3/21/77	NUMBER	5413795-0-1
NEXT HIGHER ASBY.		REV.	C
8-DD-5413795-00		SHEET	1 OF 1
SCALE		DIST.	



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REVISIONS	CHK	CHG NO.	REV.	DRN.	DATE	TRANSISTOR & DIODE CONVERSION CHART				digital EQUIPMENT CORPORATION MAYNARD, MASSACHUSETTS	TITLE GRANT CONTINUITY G727				
				<i>BUTLER</i>	<i>11-19-69</i>	DEC	EIA	DEC	EIA		SIZE	CODE	NUMBER	REV.	
				CHK'D	<i>P. J. Smith</i>	<i>6-12-70</i>						B	CS	G727-0-1	
				ENG	<i>P. E. Jensen</i>	<i>1/28/70</i>									
				PROD.											

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